SPEC® CINT2006 Result

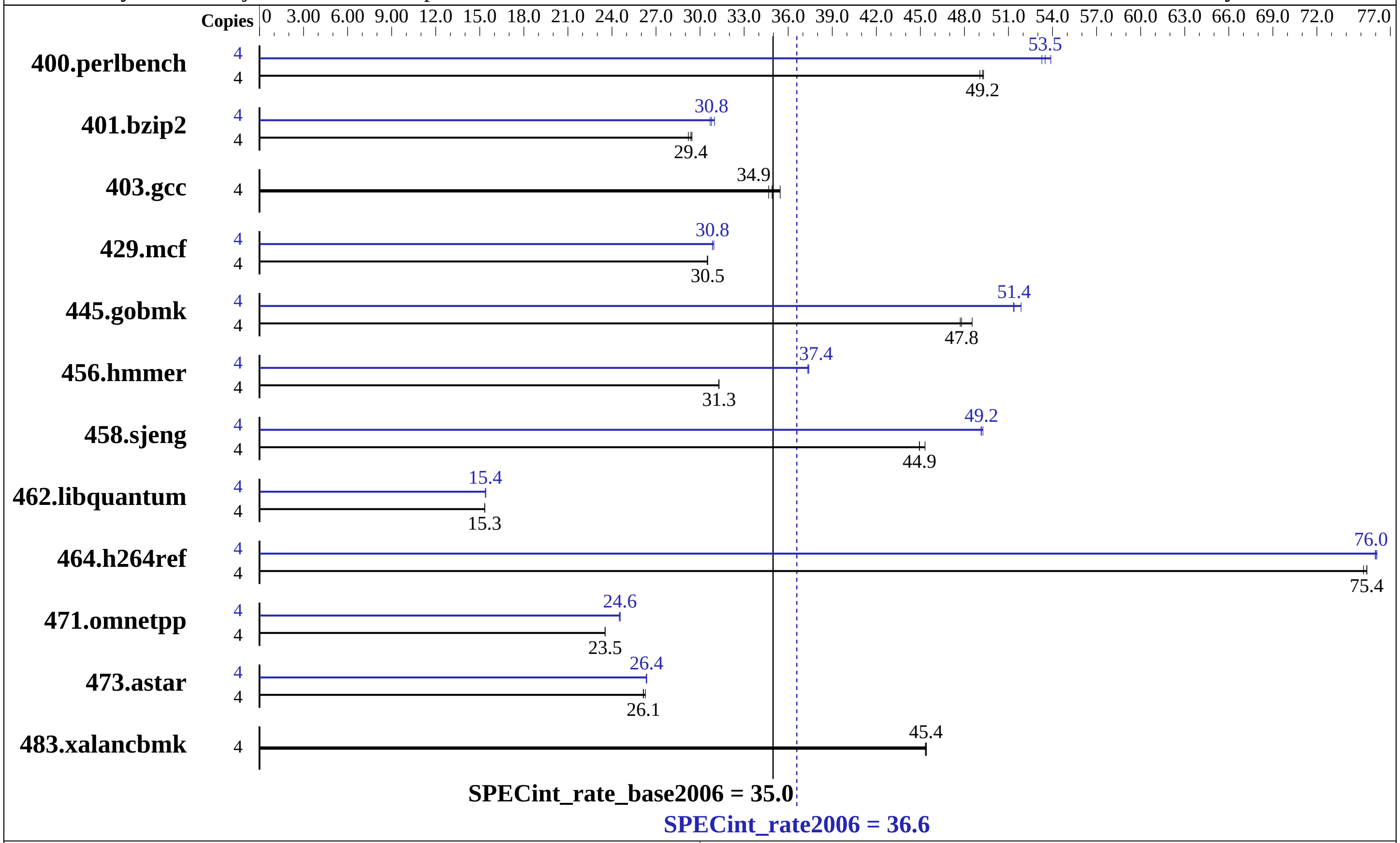
Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor E5320, 1.86 GHz

SPECint®_rate2006 = 36.6

SPECint_rate_base2006 = 35.0

CPU2006 license: 22		**Test date:**	May-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Nov-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Mar-2007



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (36GB 10000 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	4	793	49.3	**794**	**49.2**	797	49.1	4	**730**	**53.5**	725	53.9	734	53.3
401.bzip2	4	**1314**	**29.4**	1311	29.5	1322	29.2	4	1246	31.0	1258	30.7	**1254**	**30.8**
403.gcc	4	**923**	**34.9**	929	34.7	908	35.5	4	**923**	**34.9**	929	34.7	908	35.5
429.mcf	4	1195	30.5	**1195**	**30.5**	1197	30.5	4	1179	30.9	1183	30.8	**1183**	**30.8**
445.gobmk	4	**878**	**47.8**	865	48.5	879	47.7	4	**817**	**51.4**	817	51.3	809	51.9
456.hmmer	4	1193	31.3	**1193**	**31.3**	1193	31.3	4	**998**	**37.4**	998	37.4	1000	37.3
458.sjeng	4	**1077**	**44.9**	1077	44.9	1068	45.3	4	**985**	**49.2**	982	49.3	985	49.1
462.libquantum	4	**5405**	**15.3**	5403	15.3	5406	15.3	4	**5386**	**15.4**	5389	15.4	5378	15.4
464.h264ref	4	1174	75.4	**1174**	**75.4**	1178	75.2	4	**1164**	**76.0**	1165	76.0	1163	76.1
471.omnetpp	4	1062	23.5	**1062**	**23.5**	1063	23.5	4	1018	24.6	**1018**	**24.6**	1020	24.5
473.astar	4	**1074**	**26.1**	1075	26.1	1069	26.3	4	**1065**	**26.4**	1066	26.3	1065	26.4
483.xalancbmk	4	608	45.4	**608**	**45.4**	609	45.3	4	608	45.4	**608**	**45.4**	609	45.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries
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Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc -I/opt/intel/cce/9.1.047/include -L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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