SPEC® CINT2006 Result

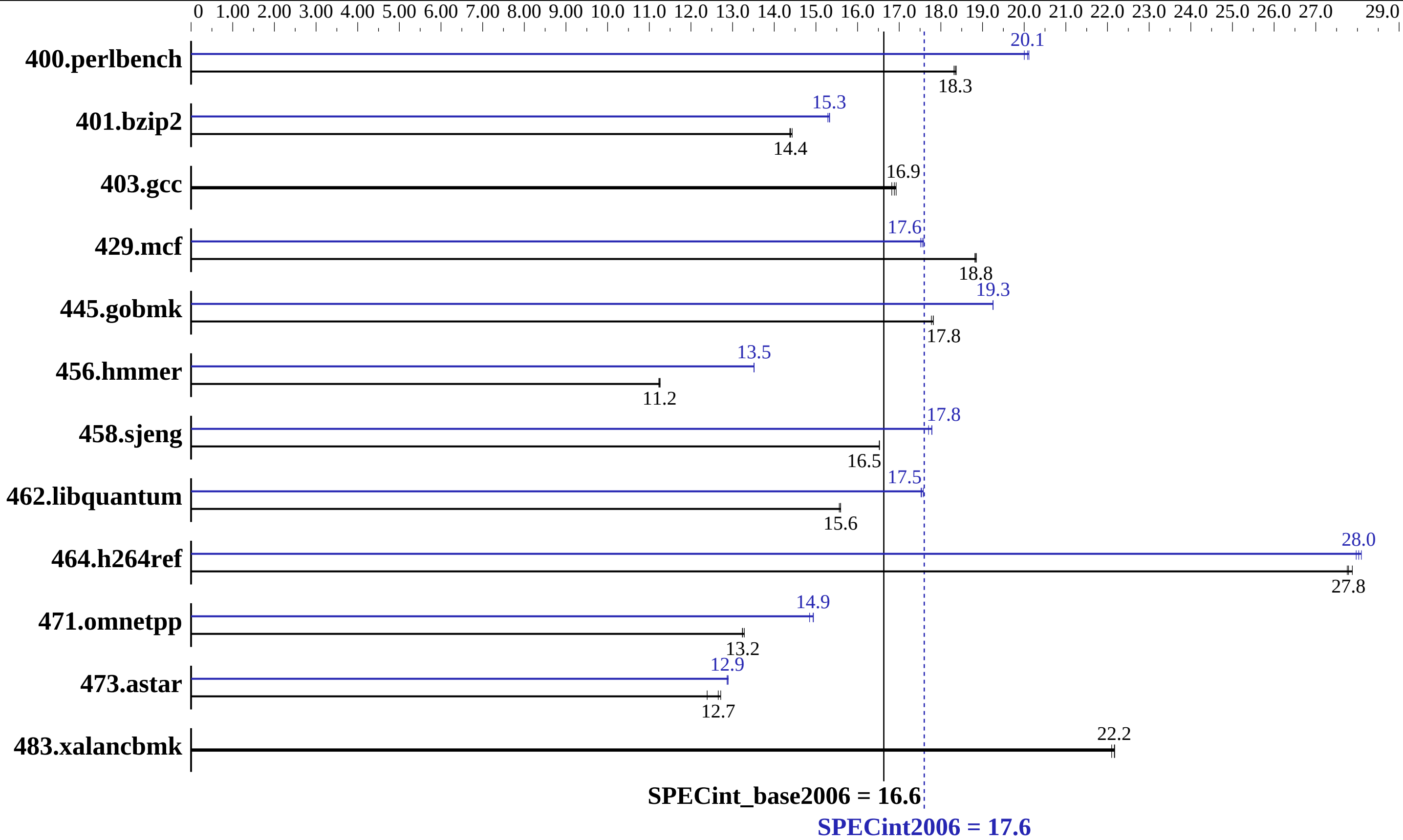
Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor 5150, 2.66 GHz

SPECint®2006 = 17.6

SPECint_base2006 = 16.6

CPU2006 license: 22		**Test date:**	May-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Jul-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Mar-2007



Hardware

CPU Name:	Intel Xeon 5150
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (73GB 15400 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**533**	**18.3**	532	18.4	533	18.3	486	20.1	488	20.0	**486**	**20.1**
401.bzip2	**671**	**14.4**	669	14.4	671	14.4	629	15.3	**630**	**15.3**	631	15.3
403.gcc	**477**	**16.9**	476	16.9	479	16.8	**477**	**16.9**	476	16.9	479	16.8
429.mcf	484	18.9	**484**	**18.8**	485	18.8	519	17.6	521	17.5	**520**	**17.6**
445.gobmk	590	17.8	589	17.8	**589**	**17.8**	545	19.3	545	19.3	**545**	**19.3**
456.hmmer	**830**	**11.2**	828	11.3	831	11.2	690	13.5	**690**	**13.5**	690	13.5
458.sjeng	732	16.5	732	16.5	**732**	**16.5**	680	17.8	**681**	**17.8**	683	17.7
462.libquantum	1332	15.6	1329	15.6	**1329**	**15.6**	1182	17.5	**1181**	**17.5**	1179	17.6
464.h264ref	797	27.8	794	27.9	**796**	**27.8**	791	28.0	788	28.1	**789**	**28.0**
471.omnetpp	**472**	**13.2**	471	13.3	472	13.2	418	14.9	421	14.8	**419**	**14.9**
473.astar	**555**	**12.7**	567	12.4	552	12.7	**545**	**12.9**	544	12.9	545	12.9
483.xalancbmk	312	22.1	**311**	**22.2**	311	22.2	312	22.1	**311**	**22.2**	311	22.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo` `-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast` `-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

Tested with SPEC CPU2006 v1.0.
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