# SPEC® CINT2006 Result



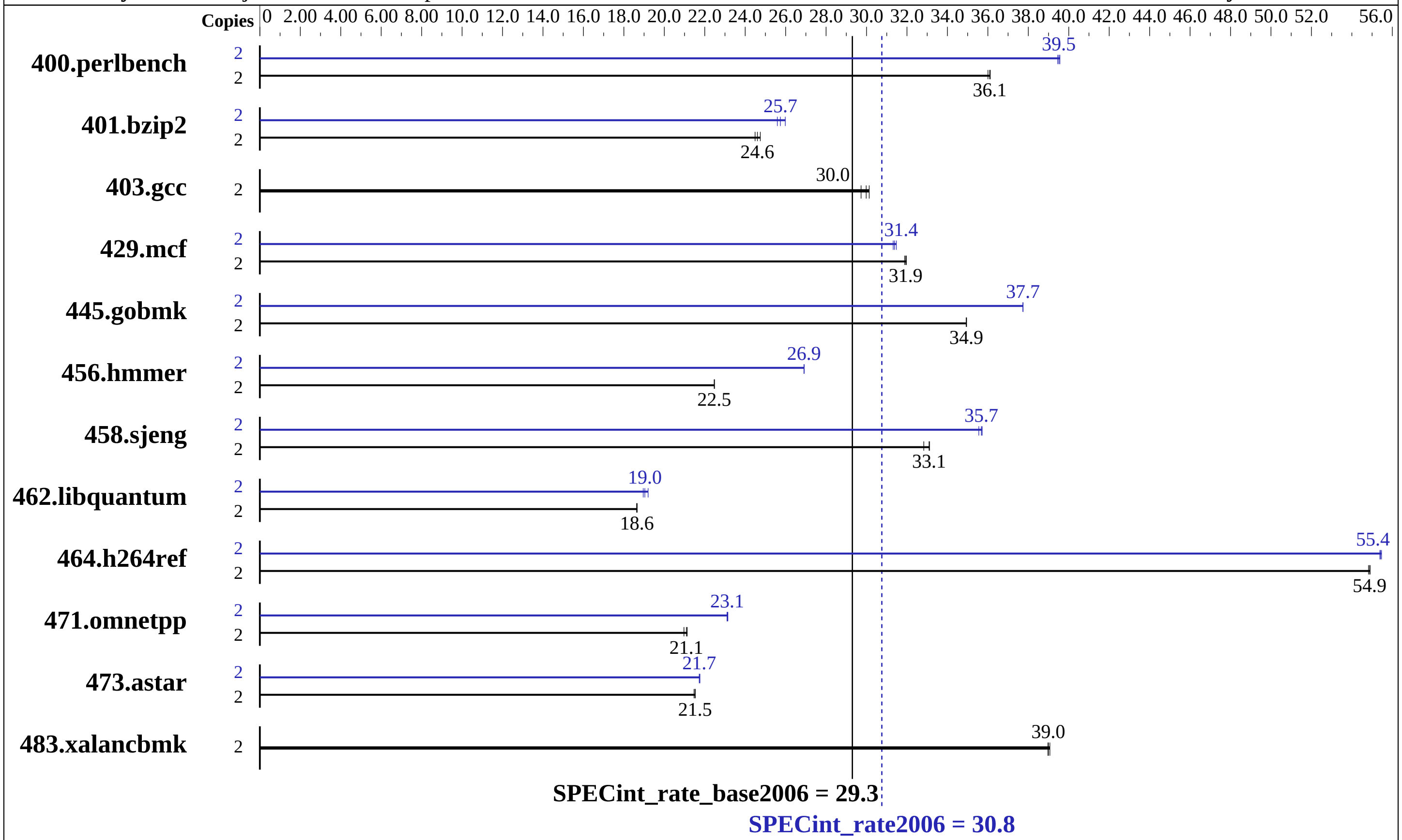
## Fujitsu Siemens Computers

PRIMERGY RX100 S4, Intel Xeon processor 3070, 2.67 GHz

SPECint®_rate2006 = 30.8

SPECint_rate_base2006 = 29.3

| | | | |
|---|---|---|---|
| **CPU2006 license:** 22 | | **Test date:** | May-2007 |
| **Test sponsor:** | Fujitsu Siemens Computers | **Hardware Availability:** | May-2007 |
| **Tested by:** | Fujitsu Siemens Computers | **Software Availability:** | Feb-2007 |




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon 3070 |
| CPU Characteristics: | 1067 MHz system bus |
| CPU MHz: | 2667 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 4 MB I+D on chip per chip |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC) |
| Disk Subsystem: | SATA (160 GB, 7200 rpm) |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64 |
| Compiler: | Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047 |
| Auto Parallel: | No |
| File System: | ReiserFS |
| System State: | Multiuser, Runlevel 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Smart Heap Library, Version 8.1 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 2 | 541 | 36.1 | **541** | **36.1** | 543 | 36.0 | 2 | 494 | 39.6 | 495 | 39.5 | **495** | **39.5** |
| 401.bzip2 | 2 | 780 | 24.7 | 788 | 24.5 | **784** | **24.6** | 2 | 743 | 26.0 | **750** | **25.7** | 754 | 25.6 |
| 403.gcc | 2 | 542 | 29.7 | 534 | 30.1 | **537** | **30.0** | 2 | 542 | 29.7 | 534 | 30.1 | **537** | **30.0** |
| 429.mcf | 2 | 571 | 32.0 | **571** | **31.9** | 572 | 31.9 | 2 | **581** | **31.4** | 580 | 31.5 | 582 | 31.3 |
| 445.gobmk | 2 | 600 | 34.9 | **601** | **34.9** | 601 | 34.9 | 2 | 556 | 37.7 | **556** | **37.7** | 556 | 37.7 |
| 456.hmmer | 2 | **830** | **22.5** | 830 | 22.5 | 830 | 22.5 | 2 | 693 | 26.9 | **693** | **26.9** | 694 | 26.9 |
| 458.sjeng | 2 | **731** | **33.1** | 737 | 32.8 | 731 | 33.1 | 2 | 678 | 35.7 | 681 | 35.6 | **678** | **35.7** |
| 462.libquantum | 2 | 2224 | 18.6 | **2222** | **18.6** | 2221 | 18.7 | 2 | **2177** | **19.0** | 2158 | 19.2 | 2186 | 19.0 |
| 464.h264ref | 2 | 806 | 54.9 | **806** | **54.9** | 807 | 54.8 | 2 | 798 | 55.5 | 799 | 55.4 | **799** | **55.4** |
| 471.omnetpp | 2 | 592 | 21.1 | 596 | 21.0 | **593** | **21.1** | 2 | 540 | 23.1 | 541 | 23.1 | **541** | **23.1** |
| 473.astar | 2 | 654 | 21.5 | **652** | **21.5** | 652 | 21.5 | 2 | 645 | 21.8 | 646 | 21.7 | **646** | **21.7** |
| 483.xalancbmk | 2 | 353 | 39.1 | 354 | 39.0 | **354** | **39.0** | 2 | 353 | 39.1 | 354 | 39.0 | **354** | **39.0** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

## General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries
```

## Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml



For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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