SPEC® CINT2006 Result

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Fujitsu Siemens Computers

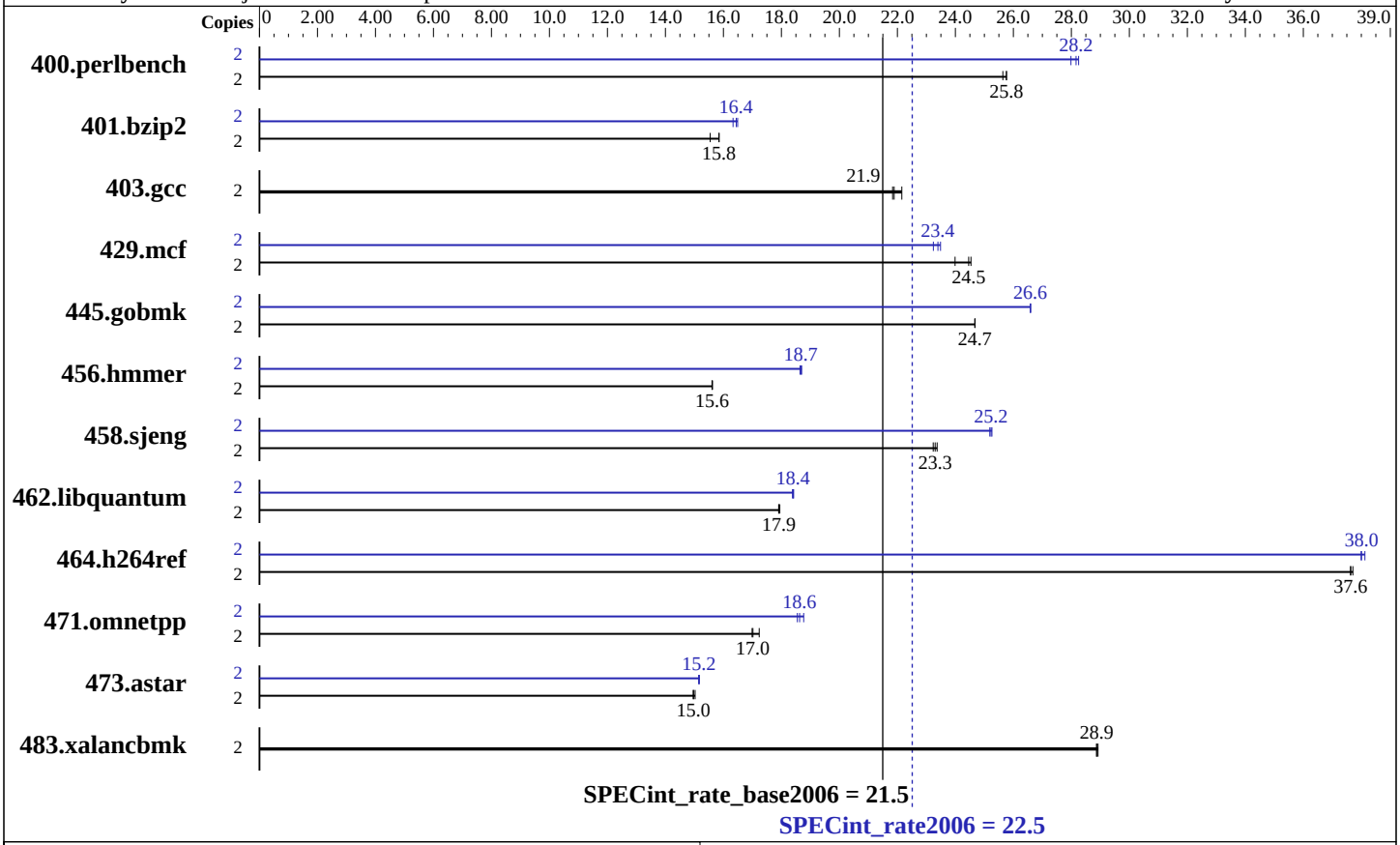
PRIMERGY TX150 S5, Intel Xeon processor 3040, 1.86 GHz

SPECint®_rate2006 = 22.5

SPECint_rate_base2006 = 21.5

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Sep-2006
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Xeon 3040
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem:	SATA (160 GB 7200 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ReiserFS
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**759**	**25.8**	758	25.8	762	25.6	2	698	28.0	**694**	**28.2**	692	28.2
401.bzip2	2	**1218**	**15.8**	1242	15.5	1218	15.9	2	1182	16.3	**1174**	**16.4**	1170	16.5
403.gcc	2	737	21.8	727	22.2	**736**	**21.9**	2	737	21.8	727	22.2	**736**	**21.9**
429.mcf	2	760	24.0	**746**	**24.5**	743	24.5	2	785	23.2	**780**	**23.4**	777	23.5
445.gobmk	2	**850**	**24.7**	850	24.7	850	24.7	2	**789**	**26.6**	789	26.6	789	26.6
456.hmmer	2	**1195**	**15.6**	1194	15.6	1196	15.6	2	998	18.7	1001	18.6	**999**	**18.7**
458.sjeng	2	1036	23.4	1042	23.2	**1039**	**23.3**	2	961	25.2	958	25.3	**959**	**25.2**
462.libquantum	2	**2313**	**17.9**	2309	17.9	2314	17.9	2	2255	18.4	**2251**	**18.4**	2249	18.4
464.h264ref	2	1176	37.6	**1176**	**37.6**	1174	37.7	2	1161	38.1	1165	38.0	**1164**	**38.0**
471.omnetpp	2	736	17.0	725	17.2	**735**	**17.0**	2	674	18.5	666	18.8	**671**	**18.6**
473.astar	2	939	15.0	**938**	**15.0**	934	15.0	2	927	15.1	**926**	**15.2**	926	15.2
483.xalancbmk	2	477	28.9	**478**	**28.9**	478	28.9	2	477	28.9	**478**	**28.9**	478	28.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

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http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`
`-L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo`
`-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`
`-L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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