SPEC® CINT2006 Result

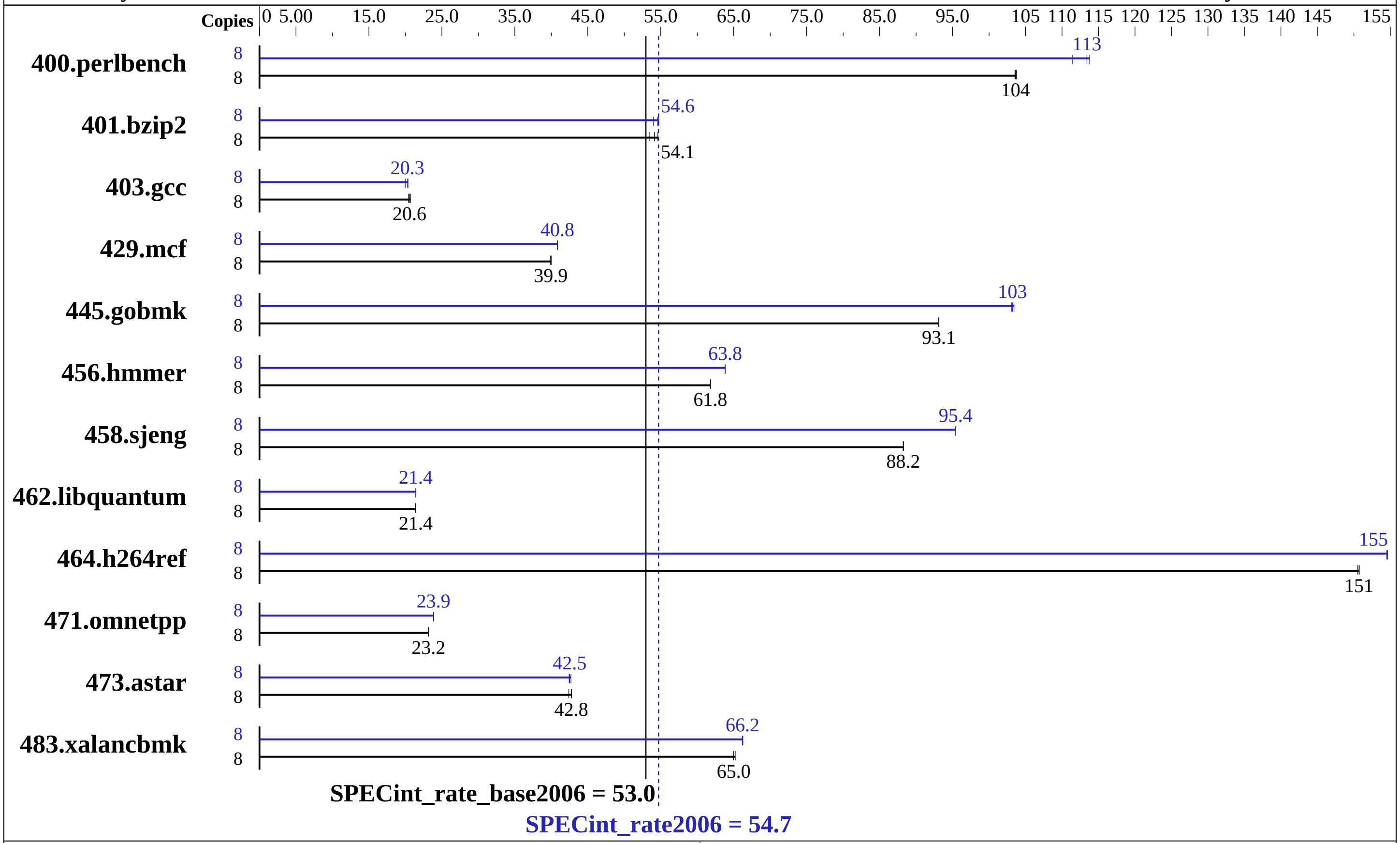
Bull SAS

NovaScale R460
(Intel Xeon processor E5320,1.86GHz)

SPECint®_rate2006 = 54.7

SPECint_rate_base2006 = 53.0

CPU2006 license:	20	**Test date:**	Apr-2007
Test sponsor:	Bull SAS	**Hardware Availability:**	Mar-2007
Tested by:	Bull SAS	**Software Availability:**	Dec-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1.86 GHz, 8 MB L2, 1066 MHz bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	12 GB (12x1 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem:	1x73 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise X64 Edition Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	753	104	**754**	**104**	755	104	8	702	111	**689**	**113**	687	114
401.bzip2	8	1445	53.4	1414	54.6	**1426**	**54.1**	8	1430	54.0	1411	54.7	**1415**	**54.6**
403.gcc	8	3113	20.7	**3132**	**20.6**	3156	20.4	8	3223	20.0	**3175**	**20.3**	3163	20.4
429.mcf	8	1825	40.0	1828	39.9	**1827**	**39.9**	8	1786	40.9	**1786**	**40.8**	1787	40.8
445.gobmk	8	901	93.1	**901**	**93.1**	901	93.1	8	811	103	814	103	**813**	**103**
456.hmmer	8	1208	61.8	1207	61.8	**1208**	**61.8**	8	1170	63.8	1169	63.8	**1169**	**63.8**
458.sjeng	8	1097	88.2	**1097**	**88.2**	1096	88.3	8	1014	95.4	**1014**	**95.4**	1015	95.3
462.libquantum	8	**7736**	**21.4**	7735	21.4	7740	21.4	8	7733	21.4	7734	21.4	**7733**	**21.4**
464.h264ref	8	1176	151	**1175**	**151**	1174	151	8	1146	154	1145	155	**1145**	**155**
471.omnetpp	8	2159	23.2	2156	23.2	**2158**	**23.2**	8	**2096**	**23.9**	2093	23.9	2096	23.8
473.astar	8	1325	42.4	**1314**	**42.8**	1313	42.8	8	1323	42.4	1315	42.7	**1321**	**42.5**
483.xalancbmk	8	**849**	**65.0**	849	65.0	847	65.2	8	833	66.3	**834**	**66.2**	834	66.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

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Other Configuration Notes

The NovaScale R440 and the NovaScale R460 models are
electronically equivalent.
The results have been measured on a NovaScale R460 model.
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Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`

Continued on next page

Base Portability Flags (Continued)

464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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