



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECint®\_rate2006 = 53.9

NovaScale T880 (3.0 GHz, Intel Xeon 7120M)

SPECint\_rate\_base2006 = 50.7

CPU2006 license: 20

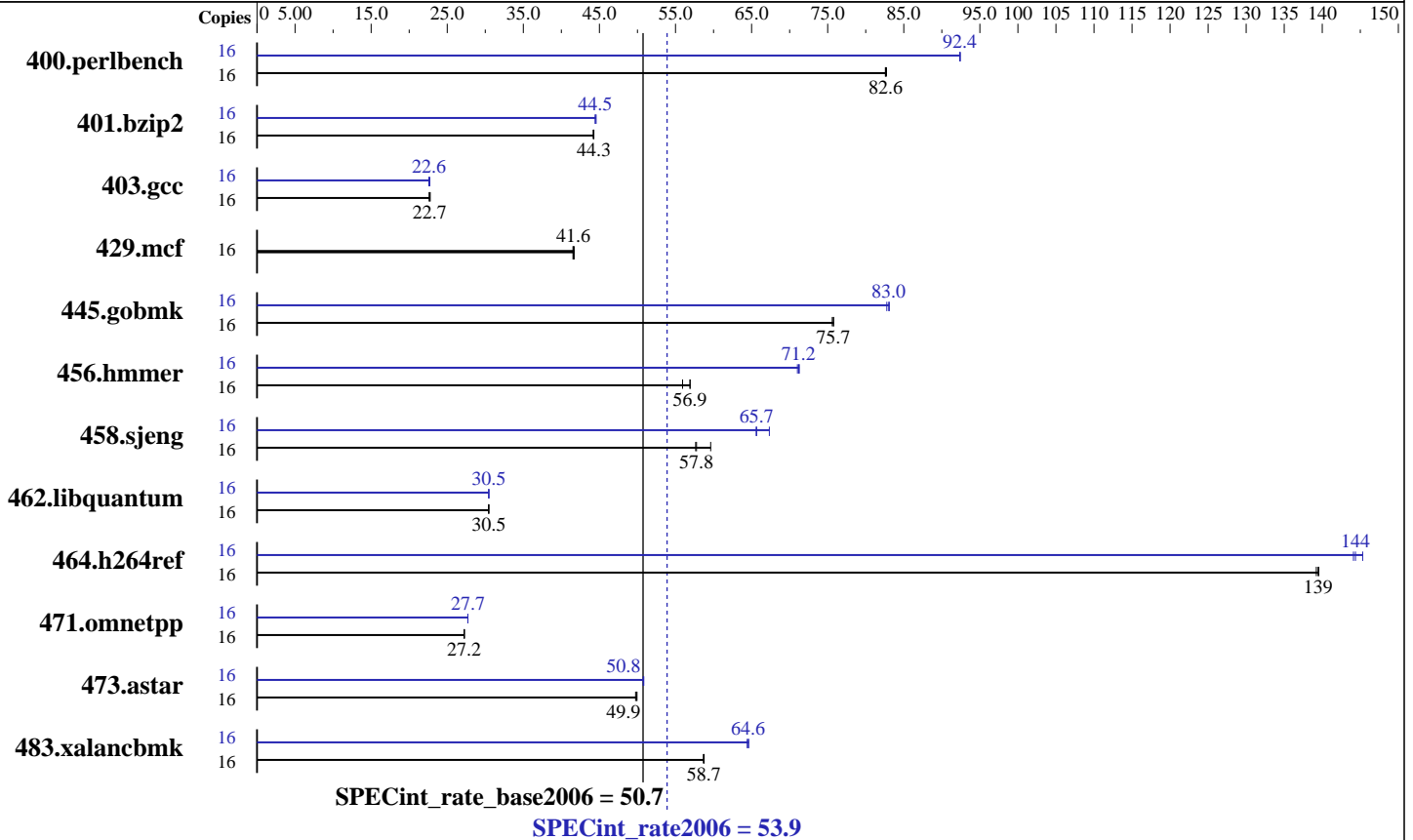
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: May-2007

Hardware Availability: Sep-2006

Software Availability: Nov-2006



### Hardware

CPU Name: Intel Xeon 7120M  
 CPU Characteristics: 3.0 GHz, 800 MHz bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16x2 GB) DDR2 400 PC2-3200R-333  
 Disk Subsystem: 2x36 GB SAS 15000 RPM  
 Other Hardware: None

### Software

Operating System: Windows Server 2003 Enterprise X64 Edition  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Package ID W\_CC\_C\_9.1.033 Build no 20061103Z  
 Microsoft Visual Studio .NET 2003 (lib & linker)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library 8.0 (shIW32M.lib)



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECint\_rate2006 = 53.9

NovaScale T880 (3.0 GHz, Intel Xeon 7120M)

SPECint\_rate\_base2006 = 50.7

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: Bull SAS

Test date: May-2007  
Hardware Availability: Sep-2006  
Software Availability: Nov-2006

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<b><u>1892</u></b>	<b><u>82.6</u></b>	1893	82.6	1890	82.7	16	1691	92.4	<b><u>1692</u></b>	<b><u>92.4</u></b>	1692	92.4
401.bzip2	16	3488	44.3	3495	44.2	<b><u>3488</u></b>	<b><u>44.3</u></b>	16	<b><u>3472</u></b>	<b><u>44.5</u></b>	3474	44.4	3464	44.6
403.gcc	16	5693	22.6	5658	22.8	<b><u>5679</u></b>	<b><u>22.7</u></b>	16	5670	22.7	<b><u>5693</u></b>	<b><u>22.6</u></b>	5702	22.6
429.mcf	16	<b><u>3509</u></b>	<b><u>41.6</u></b>	3501	41.7	3514	41.5	16	<b><u>3509</u></b>	<b><u>41.6</u></b>	3501	41.7	3514	41.5
445.gobmk	16	2220	75.6	2215	75.8	<b><u>2216</u></b>	<b><u>75.7</u></b>	16	2028	82.8	<b><u>2022</u></b>	<b><u>83.0</u></b>	2020	83.1
456.hammer	16	2669	55.9	<b><u>2624</u></b>	<b><u>56.9</u></b>	2622	56.9	16	2095	71.3	<b><u>2097</u></b>	<b><u>71.2</u></b>	2102	71.0
458.sjeng	16	3358	57.6	<b><u>3352</u></b>	<b><u>57.8</u></b>	3247	59.6	16	2875	67.3	<b><u>2948</u></b>	<b><u>65.7</u></b>	2951	65.6
462.libquantum	16	<b><u>10882</u></b>	<b><u>30.5</u></b>	10882	30.5	10894	30.4	16	<b><u>10883</u></b>	<b><u>30.5</u></b>	10882	30.5	10884	30.5
464.h264ref	16	2543	139	<b><u>2539</u></b>	<b><u>139</u></b>	2537	140	16	2457	144	<b><u>2453</u></b>	<b><u>144</u></b>	2437	145
471.omnetpp	16	3671	27.2	<b><u>3671</u></b>	<b><u>27.2</u></b>	3672	27.2	16	3608	27.7	<b><u>3608</u></b>	<b><u>27.7</u></b>	3609	27.7
473.astar	16	2258	49.8	<b><u>2252</u></b>	<b><u>49.9</u></b>	2249	49.9	16	<b><u>2210</u></b>	<b><u>50.8</u></b>	2213	50.8	2210	50.8
483.xalancbmk	16	<b><u>1880</u></b>	<b><u>58.7</u></b>	1879	58.8	1882	58.7	16	1714	64.4	1708	64.6	<b><u>1710</u></b>	<b><u>64.6</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

### Other Configuration Notes

The NovaScale T880 and the NovaScale R480 models are electronically equivalent.  
The results have been measured on a NovaScale R480 model.

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Bull SAS**

**SPECint\_rate2006 = 53.9**

NovaScale T880 (3.0 GHz, Intel Xeon 7120M)

**SPECint\_rate\_base2006 = 50.7**

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Bull SAS

**Test date:** May-2007  
**Hardware Availability:** Sep-2006  
**Software Availability:** Nov-2006

## Base Portability Flags (Continued)

464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

SPECint\_rate2006 = 53.9

NovaScale T880 (3.0 GHz, Intel Xeon 7120M)

SPECint\_rate\_base2006 = 50.7

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: May-2007

Hardware Availability: Sep-2006

Software Availability: Nov-2006

## Peak Optimization Flags (Continued)

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags.20090714.00.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 11:49:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 29 May 2007.