SPEC® CINT2006 Result

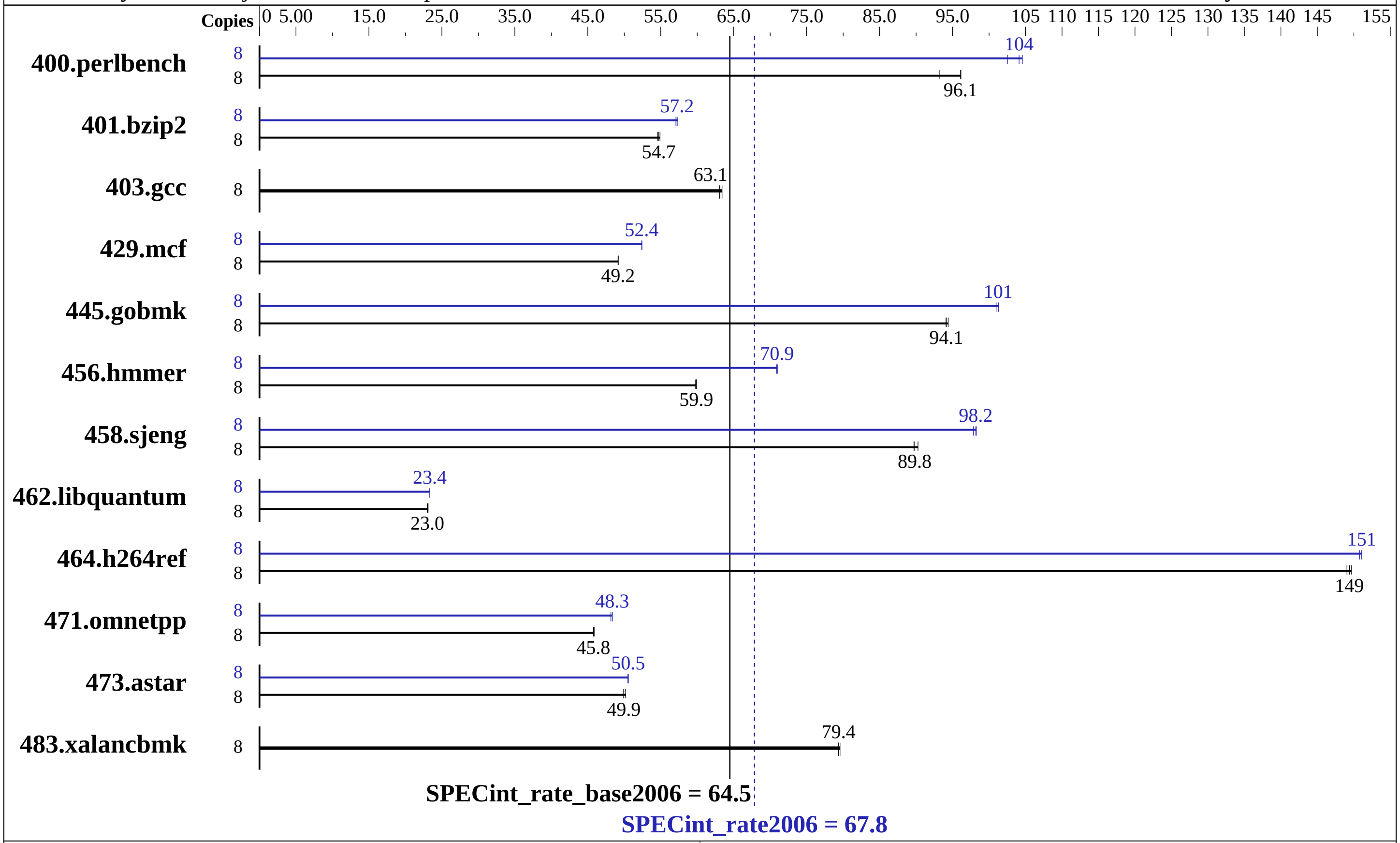
Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor E5320, 1.86 GHz

SPECint®_rate2006 = 67.8

SPECint_rate_base2006 = 64.5

CPU2006 license: 22		**Test date:**	Apr-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Nov-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (36GB 10000 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	838	93.3	813	96.1	**813**	**96.1**	8	747	105	**751**	**104**	762	103
401.bzip2	8	**1410**	**54.7**	1406	54.9	1414	54.6	8	1353	57.1	**1349**	**57.2**	1346	57.3
403.gcc	8	1021	63.1	1016	63.4	**1020**	**63.1**	8	1021	63.1	1016	63.4	**1020**	**63.1**
429.mcf	8	1484	49.2	**1484**	**49.2**	1484	49.2	8	1393	52.4	**1392**	**52.4**	1392	52.4
445.gobmk	8	**891**	**94.1**	889	94.4	892	94.1	8	831	101	**829**	**101**	828	101
456.hmmer	8	1250	59.7	**1247**	**59.9**	1246	59.9	8	1052	71.0	**1052**	**70.9**	1053	70.9
458.sjeng	8	**1078**	**89.8**	1072	90.3	1079	89.7	8	**986**	**98.2**	985	98.3	989	97.8
462.libquantum	8	**7200**	**23.0**	7201	23.0	7171	23.1	8	7096	23.4	7099	23.3	**7099**	**23.4**
464.h264ref	8	1183	150	1188	149	**1185**	**149**	8	1172	151	**1172**	**151**	1174	151
471.omnetpp	8	**1092**	**45.8**	1093	45.8	1090	45.9	8	**1034**	**48.3**	1034	48.4	1038	48.2
473.astar	8	1119	50.2	**1124**	**49.9**	1125	49.9	8	**1111**	**50.5**	1111	50.6	1112	50.5
483.xalancbmk	8	694	79.6	**695**	**79.4**	696	79.3	8	694	79.6	**695**	**79.4**	696	79.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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