Supermicro  
Motherboard PDSBA+  

**SPECint\_rate2006 = 18.7**  
**SPECint\_rate\_base2006 = 17.9**

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**Hardware**

- **CPU Name:** Intel Core 2 Duo E4300  
- **CPU Characteristics:** 1.8GHz, 800MHz bus  
- **CPU MHz:** 1800  
- **FPU:** Integrated  
- **CPU(s) enabled:** 2 cores, 1 chip, 2 cores/chip  
- **CPU(s) orderable:** 1 chip  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 2 MB I+D on chip per chip  
- **L3 Cache:** None  
- **Other Cache:** None  
- **Memory:** 2 GB (2X 1GB ECC, CL4, 533MHz, UnBuffer)  
- **Disk Subsystem:** 150GB SATA, 7200RPM  
- **Other Hardware:** None

**Software**

- **Operating System:** Windows XP Professional w/ SP2  
- **Compiler:** Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z  
- **Auto Parallel:** No  
- **File System:** NTFS  
- **System State:** Default  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32-bit  
- **Other Software:** SmartHeap Library Version 8.0 from http://www.microquill.com/
Results Table

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with SC733T-645 case. To ensure system stability, a 450W (minimum) ATX power supply [4-pin +12V AND (20 or 24-pin)] is required. Product description located as of [http://www.supermicro.com/products/motherboard/Core2Duo/965/PDSBA+.cfm](http://www.supermicro.com/products/motherboard/Core2Duo/965/PDSBA+.cfm)

The system bus runs at 800 MHz

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page
Supermicro
Motherboard PDSBA+

**SPECint_rate2006 = 18.7**
**SPECint_rate_base2006 = 17.9**

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**Base Optimization Flags (Continued)**

C++ benchmarks:
- `-fast -Qcxx_features /F5120000000 shlw32m.lib`
- `-link /FORCE:MULTIPLE`

**Base Other Flags**

C benchmarks:
- `403.gcc: -Dalloca=_alloca`

**Peak Compiler Invocation**

C benchmarks:
- `icl -Qvc7.1 -Qc99`

C++ benchmarks:
- `icl -Qvc7.1`

**Peak Portability Flags**

- `403.gcc: -DSPEC_CPU_WIN32`
- `464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32`

**Peak Optimization Flags**

C benchmarks:
- `400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F5120000000 shlw32m.lib -link /FORCE:MULTIPLE`
- `401.bzip2: Same as 400.perlbench`
- `403.gcc: Same as 400.perlbench`
- `429.mcf: basepeak = yes`
- `445.gobmk: Same as 400.perlbench`
- `456.hmmer: Same as 400.perlbench`
- `458.sjeng: Same as 400.perlbench`
Supermicro
Motherboard PDSBA+

SPECint_rate2006 = 18.7
SPECint_rate_base2006 = 17.9

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro
Test date: May-2007
Hardware Availability: Apr-2007
Software Availability: Apr-2007

Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo
-prec-div -Qunroll4 -Ob2 -Qsfalign16 -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

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For other inquiries, please contact webmaster@spec.org.

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