SPEC® CINT2006 Result

Supermicro Motherboard X7DB8

$SPECint^®\_rate2006 =$ 81.5

SPECint_rate_base2006 =   79.1

CPU2006 license: 001176   **Test date:** Apr-2007
Test sponsor: Supermicro   **Hardware Availability:** May-2007
Tested by: Supermicro   **Software Availability:** Apr-2007

Benchmark	Copies	Peak	Base
400.perlbench	8	160	145
401.bzip2	8	77.8	77.6
403.gcc	8	28.6	28.4
429.mcf	8		63.8
445.gobmk	8	147	135
456.hmmer	8	90.9	89.0
458.sjeng	8	137	129
462.libquantum	8	30.5	30.6
464.h264ref	8	220	215
471.omnetpp	8	46.6	45.4
473.astar	8	68.3	65.1
483.xalancbmk	8	97.6	97.5

SPECint_rate_base2006 = 79.1

SPECint_rate2006 = 81.5

Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66GHz, 4x2 MB L2 shared, 1333 MHz bus
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 X 2GB ECC, CL5, FBDIMM)
Disk Subsystem:	750GB IDE, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition W/ SP1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

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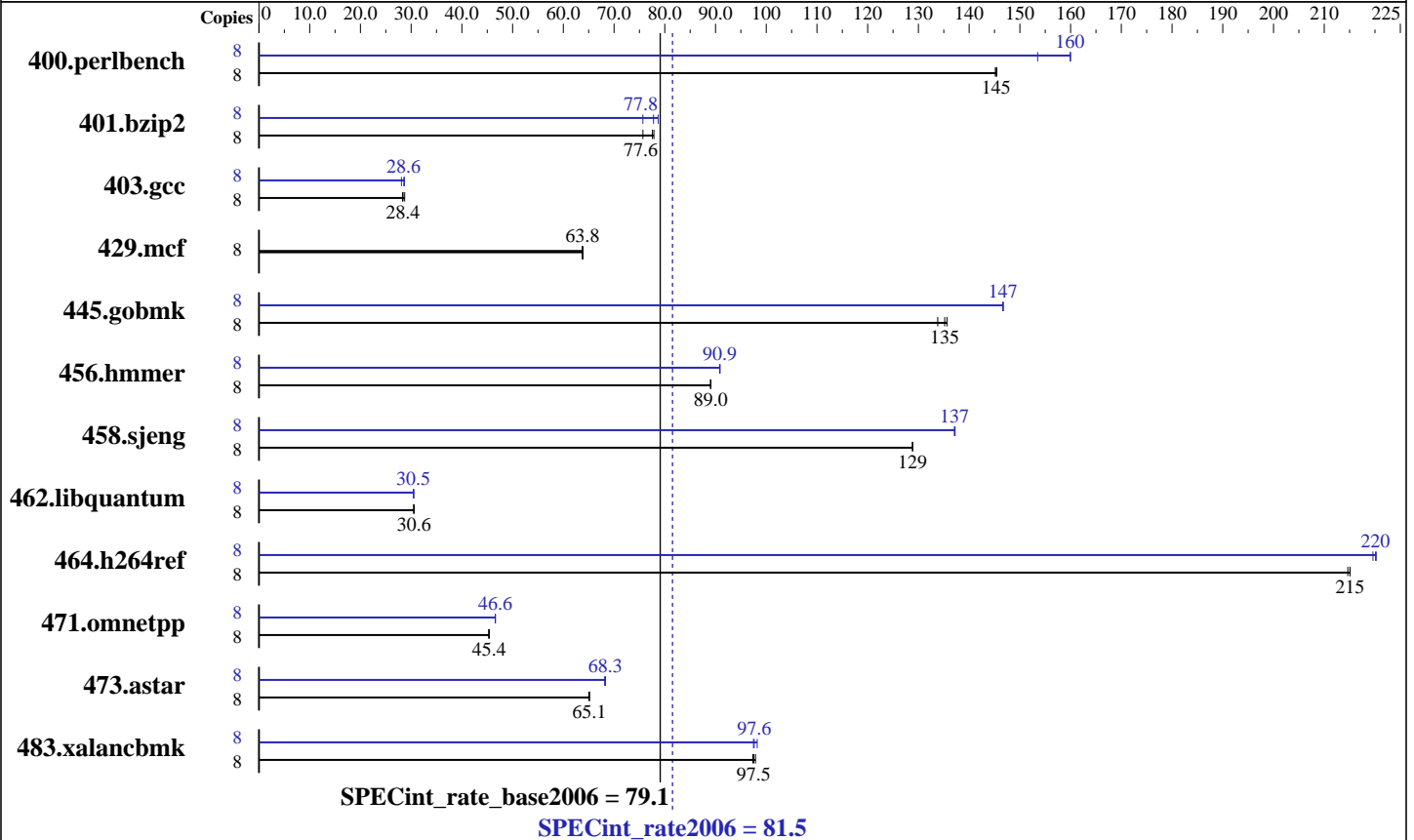
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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**_538_**	**_145_**	538	145	537	145	8	509	153	488	160	**_489_**	**_160_**
401.bzip2	8	1020	75.7	**_995_**	**_77.6_**	992	77.9	8	1020	75.7	**_993_**	**_77.8_**	981	78.7
403.gcc	8	**_2267_**	**_28.4_**	2242	28.7	2276	28.3	8	2246	28.7	2293	28.1	**_2255_**	**_28.6_**
429.mcf	8	1144	63.8	1143	63.9	**_1144_**	**_63.8_**	8	1144	63.8	1143	63.9	**_1144_**	**_63.8_**
445.gobmk	8	627	134	619	136	**_621_**	**_135_**	8	572	147	572	147	**_572_**	**_147_**
456.hmmer	8	**_838_**	**_89.0_**	838	89.1	839	89.0	8	**_821_**	**_90.9_**	822	90.8	821	90.9
458.sjeng	8	**_751_**	**_129_**	751	129	751	129	8	706	137	705	137	**_706_**	**_137_**
462.libquantum	8	5436	30.5	**_5424_**	**_30.6_**	5422	30.6	8	5427	30.5	5448	30.4	**_5443_**	**_30.5_**
464.h264ref	8	825	215	823	215	**_823_**	**_215_**	8	806	220	804	220	**_804_**	**_220_**
471.omnetpp	8	1101	45.4	1104	45.3	**_1102_**	**_45.4_**	8	**_1073_**	**_46.6_**	1072	46.6	1074	46.6
473.astar	8	864	65.0	862	65.1	**_862_**	**_65.1_**	8	822	68.3	**_823_**	**_68.3_**	824	68.2
483.xalancbmk	8	564	97.9	**_566_**	**_97.5_**	567	97.4	8	562	98.2	566	97.5	**_565_**	**_97.6_**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with SC816S-R700 case,
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DBR-8.cfm
The system bus runs at 1333 MHz

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Continued on next page

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
            shlw32m.lib               -link /FORCE:MULTIPLE

   401.bzip2: Same as 400.perlbench

    403.gcc: Same as 400.perlbench

    429.mcf: basepeak = yes

  445.gobmk: Same as 400.perlbench

  456.hmmer: Same as 400.perlbench

   458.sjeng: Same as 400.perlbench
```

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast` `-Qcxx_features`
`/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE`

473.astar: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxP` `-O2` `-Qipo`
`-Qprec-div-` `-Qunroll4` `-Ob2` `-Qsfalign16` `-Qcxx_features`
`/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE`

483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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