## SPECint® _rate2006_ = 75.1

### SPECint_rate_base2006 = 68.1

**Acer Incorporated**

**Acer Altos R910 (Intel Xeon 7140M, 3.4GHz)**

**CPU2006 license:** 97

**Test date:** Apr-2007

**Test sponsor:** Acer Incorporated

**Hardware Availability:** Aug-2006

**Tested by:** Acer Incorporated

**Software Availability:** Jul-2006

### SPECint® Result

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECint_rate2006</th>
<th>SPECint_rate_base2006</th>
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<tr>
<td>400.perlbench</td>
<td>91.7</td>
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<tr>
<td>401.bzip2</td>
<td>27.5</td>
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<td>403.gcc</td>
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<td>429.mcf</td>
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<tr>
<td>445.gobmk</td>
<td>89.8</td>
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</tr>
<tr>
<td>483.xalancbmk</td>
<td>85.9</td>
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</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon 7140M
- **CPU Characteristics:** 800MHz system bus
- **CPU MHz:** 3400
- **FPU:** Integrated
- **CPU(s) enabled:** 8 cores, 4 chips, 2 cores/chip, 2 threads/core
- **CPU(s) orderable:** 1,2,3,4 chips
- **Primary Cache:** 12 K micro-ops I + 16 KB D on chip per core
- **Secondary Cache:** 1 MB I+D on chip per core
- **L3 Cache:** 16 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 16 GB (16 x 1024MB ECC DDR2-400 SDRAM CL3-3-3)
- **Disk Subsystem:** 1 x 73GB 10000RPM SAS HDD
- **Other Hardware:** None

### Software

- **Operating System:** Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
- **Compiler:** Intel C++ Compiler for IA32 version 9.1
  - Package ID W_CC_C_9.1.028 Build 20060706Z
- **Auto Parallel:** No
- **File System:** NTFS
- **System State:** Default
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32-bit
- **Other Software:** MicroQuill Smart Heap Library, Version 7.4
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### Results Table

<table>
<thead>
<tr>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### General Notes

Disable hardware prefetching and disable adjacent cache line prefetch BIOS setting.

### Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

### Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```
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Base Optimization Flags

C benchmarks:
- fast /F512000000 shlw32m.lib

C++ benchmarks:
- fast -Qcxx_features /F512000000 shlw32m.lib

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
- link /FORCE:MULTIPLE

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

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Peak Optimization Flags (Continued)

456.hmmer: Same as 400.perlbench
458.sjeng: Same as 400.perlbench
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Acer-CPU2006-ic91-flags.20090714.00.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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