Bull SAS
NovaScale R460
(Intel Xeon processor E5310, 1.60GHz)

SPEClnt_rate2006 = 50.7
SPEClnt_rate_base2006 = 48.9

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Hardware
CPU Name: Intel Xeon E5310
CPU Characteristics: 1.60 GHz, 8MB L2, 1066MHz bus
CPU MHZ: 1600
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1 to 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 12 GB (1GB DIMMx12, FB-DIMM PC2-5300F ECC CL5)
Disk Subsystem: 73 GB SAS, 10000RPM
Other Hardware: None

Software
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.033 Build no 20061103Z
Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

Test date: Apr-2007
Hardware Availability: Mar-2007
Software Availability: Dec-2006
Bull SAS

NovaScale R460
(Intel Xeon processor E5310, 1.60GHz)

CPU2006 license: 20
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SPECint_rate2006 = 50.7
SPECint_rate_base2006 = 48.9

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Results Table

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The NovaScale R440 and the NovaScale R460 models are electronically equivalent.
The results have been measured on a NovaScale R460 model.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
SPEC CINT2006 Result

Bull SAS
NovaScale R460
(Intel Xeon processor E5310, 1.60GHz)

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SPECint_rate_base2006 = 48.9

CPU2006 license: 20
Test sponsor: Bull SAS
Test date: Apr-2007
Test by: Bull SAS
Hardware Availability: Mar-2007
Software Availability: Dec-2006

Base Optimization Flags

C benchmarks:
-`-fast /F512000000 shlw32m.lib`  

C++ benchmarks:
-`-fast -Qcxx_features /F512000000 shlw32m.lib`  
  -`-link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:
-`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
-`icl -Qvc7.1 -Qc99`

C++ benchmarks:
-`icl -Qvc7.1`

Peak Portability Flags

-`403.gcc: -DSPEC_CPU_WIN32`
-`464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
-`-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib`  
  -`-link /FORCE:MULTIPLE`

C++ benchmarks:
-`-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features`  
  `/F512000000 shlw32m.lib`  
  -`-link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

Continued on next page
## Bull SAS

**NovaScale R460**  
(Intel Xeon processor E5310, 1.60GHz)

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**SPECint** rate of Bull SAS on NovaScale R460 with Intel Xeon processor E5310 running at 1.60GHz.

### Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link: http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

### Details

- **CPU2006 license:** 20
- **Test sponsor:** Bull SAS
- **Tested by:** Bull SAS
- **Test date:** Apr-2007
- **Hardware Availability:** Mar-2007
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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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