SPEC® CINT2006 Result

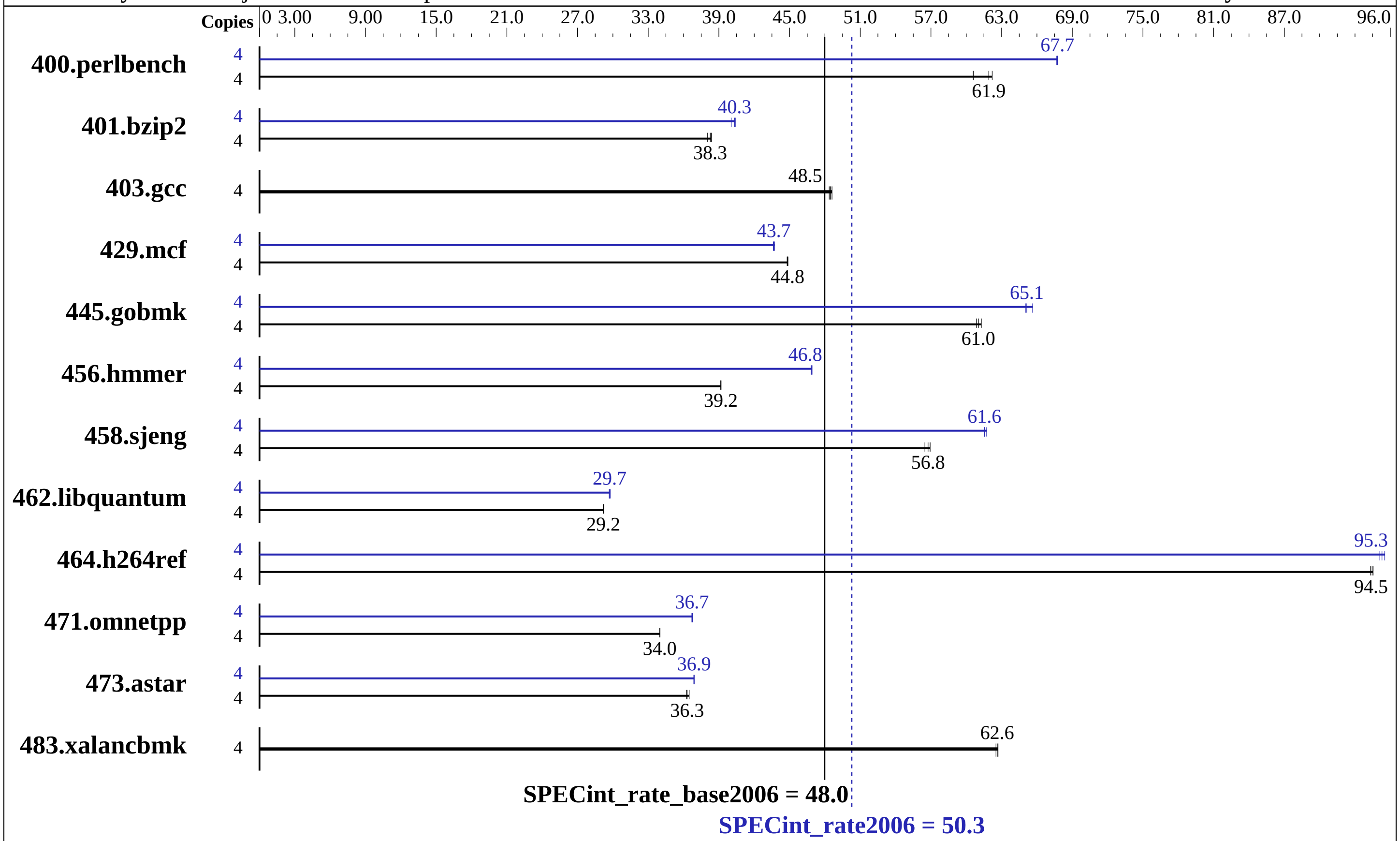
Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor 5140, 2.33 GHz

SPECint®_rate2006 = 50.3

SPECint_rate_base2006 = 48.0

CPU2006 license: 22		**Test date:**	Apr-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Jul-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Xeon 5140
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (73GB 15400 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	645	60.6	628	62.2	**631**	**61.9**	4	577	67.7	**577**	**67.7**	578	67.6
401.bzip2	4	1006	38.4	**1009**	**38.3**	1015	38.0	4	**957**	**40.3**	964	40.0	956	40.4
403.gcc	4	**664**	**48.5**	663	48.6	666	48.4	4	**664**	**48.5**	663	48.6	666	48.4
429.mcf	4	814	44.8	**814**	**44.8**	813	44.9	4	834	43.7	836	43.6	**835**	**43.7**
445.gobmk	4	685	61.3	689	60.9	**688**	**61.0**	4	639	65.6	645	65.1	**644**	**65.1**
456.hmmer	4	954	39.1	952	39.2	**953**	**39.2**	4	796	46.9	**797**	**46.8**	797	46.8
458.sjeng	4	850	56.9	**853**	**56.8**	857	56.5	4	786	61.5	784	61.7	**786**	**61.6**
462.libquantum	4	2836	29.2	**2839**	**29.2**	2840	29.2	4	2784	29.8	**2788**	**29.7**	2792	29.7
464.h264ref	4	936	94.5	**937**	**94.5**	938	94.3	4	**929**	**95.3**	931	95.1	927	95.5
471.omnetpp	4	736	34.0	735	34.0	**736**	**34.0**	4	680	36.8	681	36.7	**681**	**36.7**
473.astar	4	**774**	**36.3**	770	36.5	775	36.2	4	761	36.9	**761**	**36.9**	762	36.9
483.xalancbmk	4	**441**	**62.6**	441	62.5	440	62.7	4	**441**	**62.6**	441	62.5	440	62.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

401.bzip2: `-fast`

403.gcc: `basepeak = yes`

429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -L/opt/SmartHeap_8_1/lib -lsmartheap`

445.gobmk: `Same as 429.mcf`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 429.mcf`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 429.mcf`

C++ benchmarks:

471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap`

473.astar: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -L/opt/SmartHeap_8_1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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