



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor 5130, 2.0 GHz

SPECint®\_rate2006 = 45.9

SPECint\_rate\_base2006 = 43.7

CPU2006 license: 22

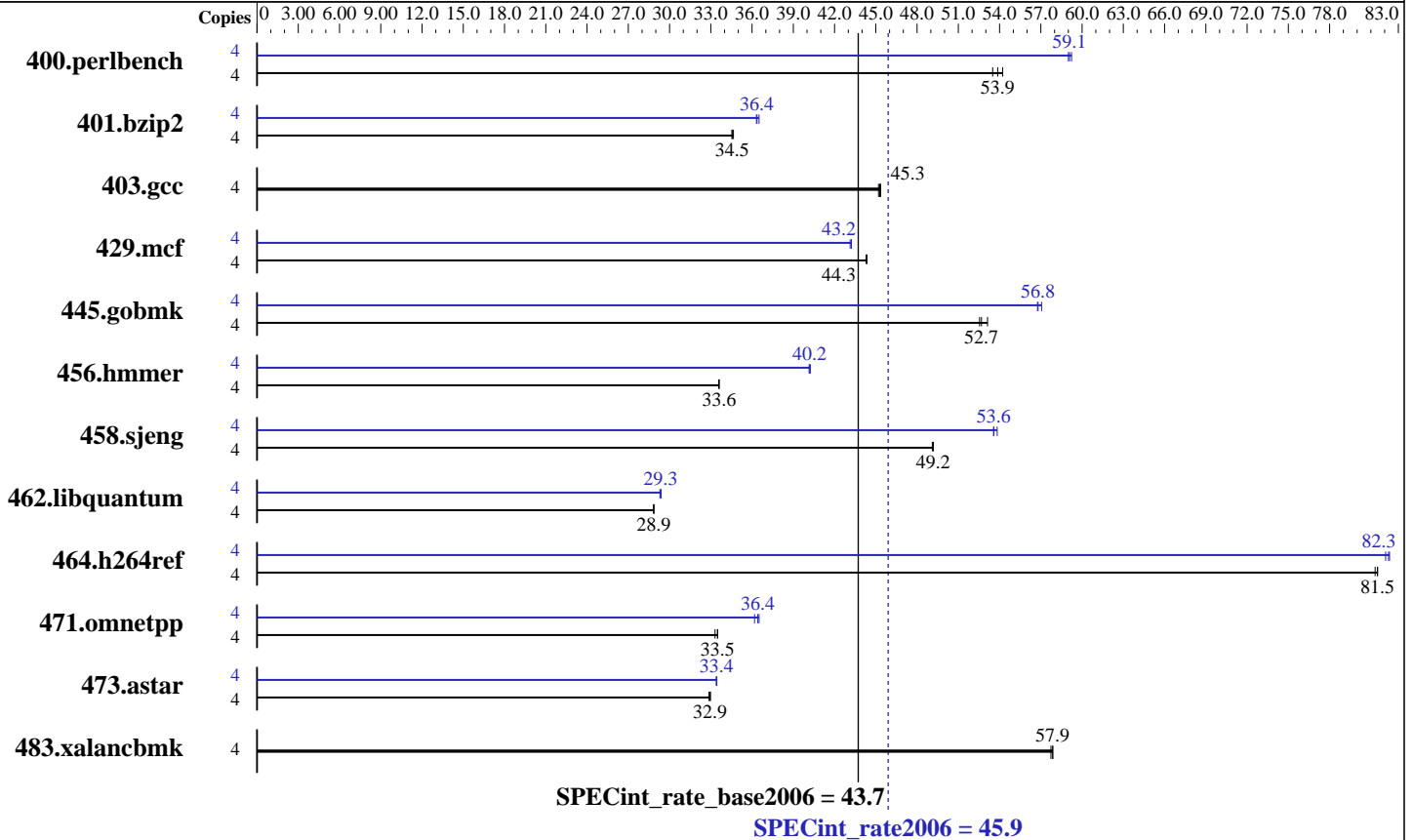
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Apr-2007

Hardware Availability: Jul-2006

Software Availability: Feb-2007



### Hardware

CPU Name: Intel Xeon 5130  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: SAS (36GB 10000 rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	731	53.5	<b><u>726</u></b>	<b><u>53.9</u></b>	721	54.2	4	662	59.0	660	59.2	<b><u>661</u></b>	<b><u>59.1</u></b>
401.bzip2	4	<b><u>1117</u></b>	<b><u>34.5</u></b>	1114	34.6	1117	34.5	4	<b><u>1062</u></b>	<b><u>36.4</u></b>	1057	36.5	1063	36.3
403.gcc	4	710	45.4	<b><u>711</u></b>	<b><u>45.3</u></b>	712	45.2	4	710	45.4	<b><u>711</u></b>	<b><u>45.3</u></b>	712	45.2
429.mcf	4	822	44.4	824	44.3	<b><u>823</u></b>	<b><u>44.3</u></b>	4	844	43.2	<b><u>844</u></b>	<b><u>43.2</u></b>	846	43.1
445.gobmk	4	799	52.5	<b><u>796</u></b>	<b><u>52.7</u></b>	790	53.1	4	<b><u>739</u></b>	<b><u>56.8</u></b>	739	56.8	735	57.1
456.hmmer	4	1111	33.6	1111	33.6	<b><u>1111</u></b>	<b><u>33.6</u></b>	4	<b><u>929</u></b>	<b><u>40.2</u></b>	927	40.2	930	40.1
458.sjeng	4	984	49.2	986	49.1	<b><u>984</u></b>	<b><u>49.2</u></b>	4	904	53.6	899	53.8	<b><u>903</u></b>	<b><u>53.6</u></b>
462.libquantum	4	2872	28.9	2873	28.8	<b><u>2873</u></b>	<b><u>28.9</u></b>	4	2820	29.4	2829	29.3	<b><u>2824</u></b>	<b><u>29.3</u></b>
464.h264ref	4	1089	81.3	1086	81.5	<b><u>1086</u></b>	<b><u>81.5</u></b>	4	<b><u>1076</u></b>	<b><u>82.3</u></b>	1079	82.1	1075	82.4
471.omnetpp	4	<b><u>747</u></b>	<b><u>33.5</u></b>	751	33.3	746	33.5	4	691	36.2	685	36.5	<b><u>687</u></b>	<b><u>36.4</u></b>
473.astar	4	855	32.9	<b><u>852</u></b>	<b><u>32.9</u></b>	852	33.0	4	841	33.4	<b><u>840</u></b>	<b><u>33.4</u></b>	840	33.4
483.xalancbmk	4	478	57.7	477	57.9	<b><u>477</u></b>	<b><u>57.9</u></b>	4	478	57.7	477	57.9	<b><u>477</u></b>	<b><u>57.9</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

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## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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