SPEC® CINT2006 Result

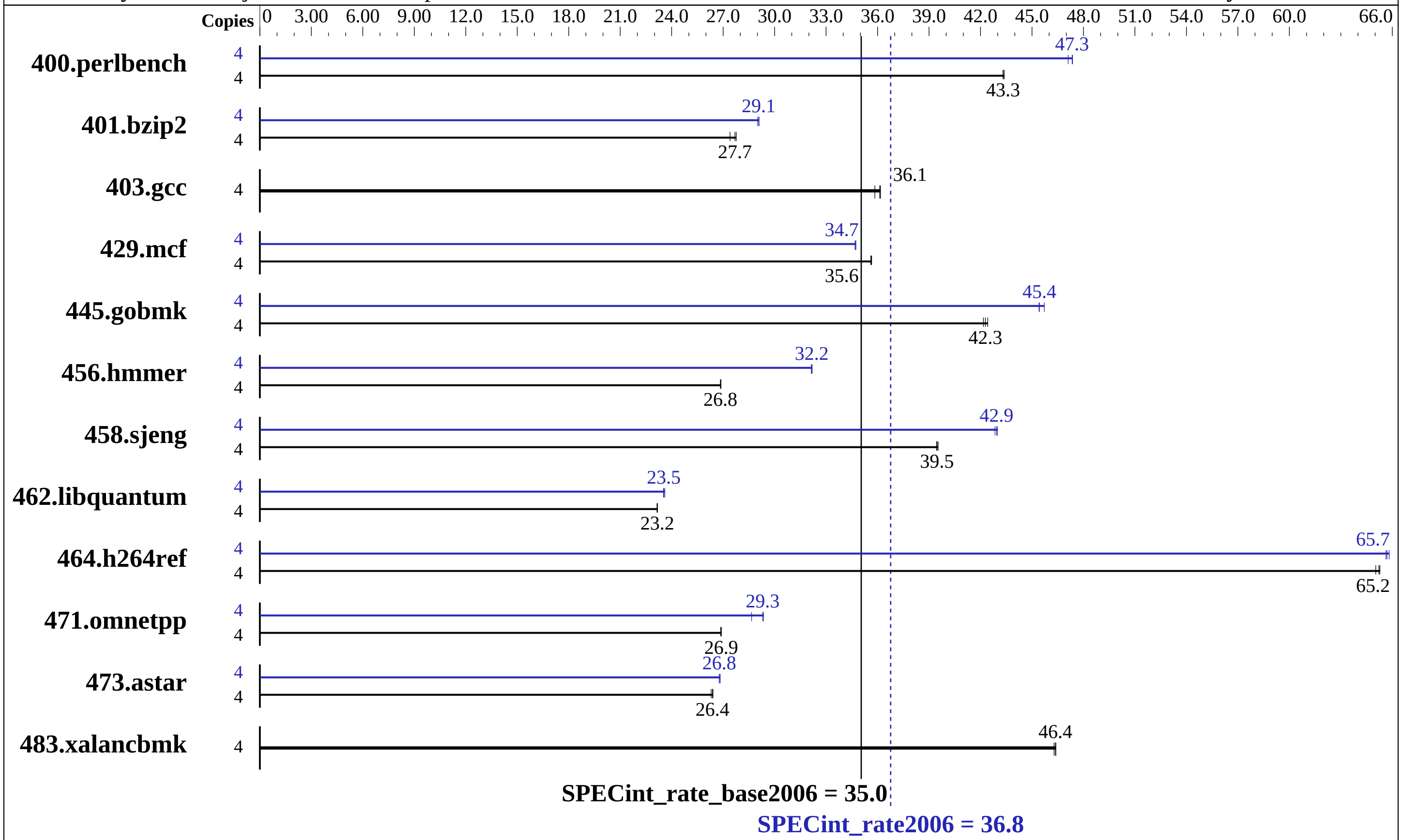
Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor 5110, 1.60 GHz

SPECint®_rate2006 = 36.8

SPECint_rate_base2006 = 35.0

CPU2006 license: 22		**Test date:**	Apr-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Jul-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Xeon 5110
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (36GB 10000 rpm)
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1

Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	4	**902**	**43.3**	902	43.3	901	43.4	4	825	47.4	830	47.1	**826**	**47.3**
401.bzip2	4	1390	27.8	**1394**	**27.7**	1409	27.4	4	1331	29.0	1327	29.1	**1328**	**29.1**
403.gcc	4	890	36.2	898	35.8	**891**	**36.1**	4	890	36.2	898	35.8	**891**	**36.1**
429.mcf	4	1023	35.7	**1024**	**35.6**	1024	35.6	4	1050	34.7	1052	34.7	**1051**	**34.7**
445.gobmk	4	995	42.2	**992**	**42.3**	989	42.4	4	924	45.4	**924**	**45.4**	918	45.7
456.hmmer	4	1389	26.9	1390	26.8	**1390**	**26.8**	4	1159	32.2	**1160**	**32.2**	1161	32.1
458.sjeng	4	1224	39.5	**1226**	**39.5**	1227	39.4	4	**1127**	**42.9**	1126	43.0	1130	42.8
462.libquantum	4	3577	23.2	3581	23.1	**3577**	**23.2**	4	3523	23.5	**3520**	**23.5**	3511	23.6
464.h264ref	4	**1357**	**65.2**	1356	65.3	1361	65.0	4	**1348**	**65.7**	1349	65.6	1345	65.8
471.omnetpp	4	**930**	**26.9**	930	26.9	931	26.9	4	852	29.4	872	28.7	**853**	**29.3**
473.astar	4	**1065**	**26.4**	1068	26.3	1063	26.4	4	1047	26.8	**1049**	**26.8**	1049	26.8
483.xalancbmk	4	595	46.4	596	46.3	**595**	**46.4**	4	595	46.4	596	46.3	**595**	**46.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
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General Notes

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The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

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http://www.fujitsu-siemens.com/countries
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Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -L/opt/SmartHeap_8_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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