



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7140M, 3.40 GHz

SPECint®_rate2006 = 45.5

SPECint_rate_base2006 = 42.5

CPU2006 license: 22

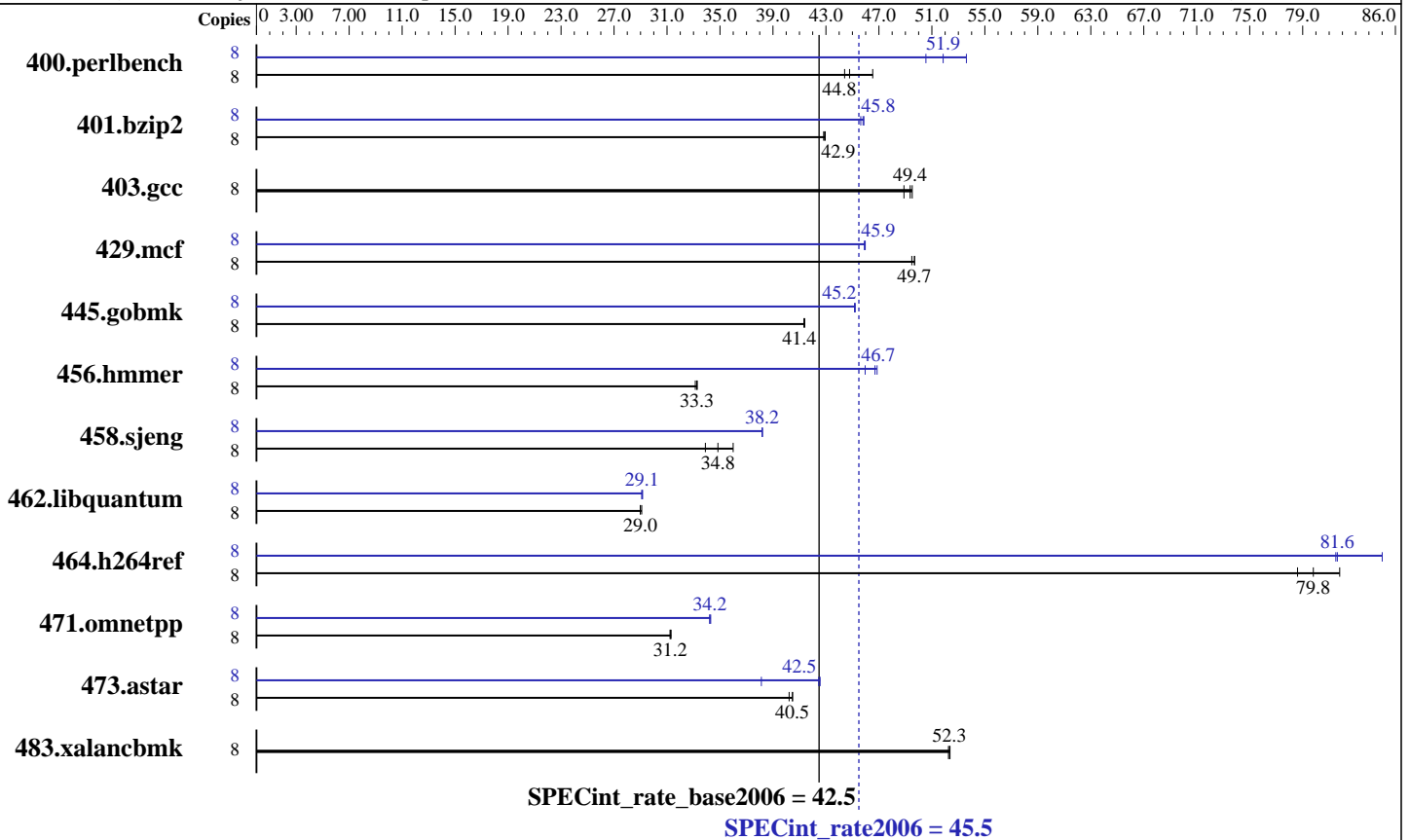
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Apr-2007

Hardware Availability: Dec-2006

Software Availability: Feb-2007



Hardware

CPU Name: Intel Xeon 7140M
 CPU Characteristics: 800 MHz system bus
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (16x2 GB DDR2 PC2-3200R, 2 rank, CAS 3-3-3, with ECC)
 Disk Subsystem: Fujitsu MAS3735NC (SCSI 73GB 15 krpm)
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
 Auto Parallel: No
 File System: ReiserFS
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	1760	44.4	1745	44.8	1680	46.5	8	1546	50.5	1458	53.6	1507	51.9
401.bzip2	8	1797	42.9	1803	42.8	1800	42.9	8	1692	45.6	1683	45.9	1686	45.8
403.gcc	8	1301	49.5	1317	48.9	1305	49.4	8	1301	49.5	1317	48.9	1305	49.4
429.mcf	8	1468	49.7	1474	49.5	1468	49.7	8	1587	46.0	1589	45.9	1589	45.9
445.gobmk	8	2027	41.4	2030	41.3	2029	41.4	8	1857	45.2	1858	45.2	1856	45.2
456.hmmmer	8	2245	33.3	2243	33.3	2253	33.1	8	1598	46.7	1593	46.9	1624	46.0
458.sjeng	8	2778	34.8	2689	36.0	2856	33.9	8	2534	38.2	2534	38.2	2535	38.2
462.libquantum	8	5718	29.0	5698	29.1	5719	29.0	8	5685	29.2	5698	29.1	5693	29.1
464.h264ref	8	2218	79.8	2252	78.6	2164	81.8	8	2169	81.6	2083	85.0	2172	81.5
471.omnetpp	8	1597	31.3	1600	31.2	1601	31.2	8	1458	34.3	1460	34.2	1461	34.2
473.astar	8	1387	40.5	1388	40.5	1396	40.2	8	1320	42.6	1323	42.5	1473	38.1
483.xalancbmk	8	1055	52.3	1054	52.4	1056	52.3	8	1055	52.3	1054	52.4	1056	52.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 800 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Hardware Prefetch = Enable

This result was measured on the PRIMERGY RX600 S3. The PRIMERGY RX600 S3 and
the PRIMERGY TX600 S3 are electronically equivalent.

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<http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmarheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc
-I/opt/intel/cce/9.1.047/include
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmarheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmarheap

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmarheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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