



SPEC® CINT2006 Result

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Bull SAS

SPECint®_rate2006 = 63.1

NovaScale R480 (3.20 GHz, Intel Xeon 7130M)

SPECint_rate_base2006 = 59.3

CPU2006 license: 20

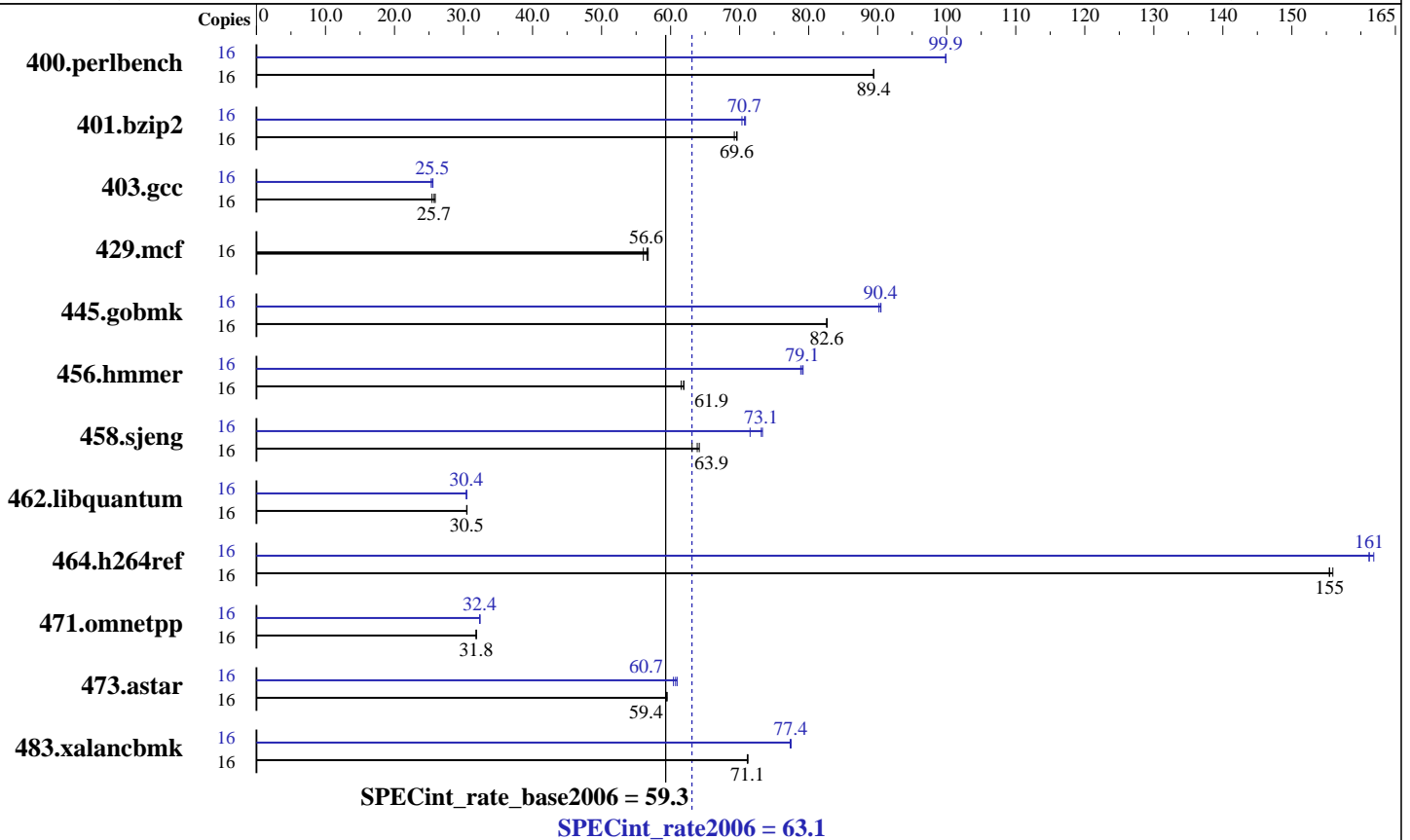
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Mar-2007

Hardware Availability: Sep-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon 7130M
 CPU Characteristics: 3.2GHz, 800MHz bus
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (667 MHz ECC CL5 DDR2 FB-DIMM)
 Disk Subsystem: 2x36GB SAS 15000 rpm
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition
 Compiler: Intel C++ Compiler 9.1 for 32-bit
 Build 20061103Z Package ID: W_CC_P_9.1.033
 Microsoft Visual Studio .NET 2003 (libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0 (shIW32M.lib)



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>1748</u>	<u>89.4</u>	1748	89.4	1749	89.4	16	1565	99.9	<u>1565</u>	<u>99.9</u>	1566	99.8
401.bzip2	16	<u>2219</u>	<u>69.6</u>	2218	69.6	2230	69.2	16	2179	70.9	2195	70.3	<u>2183</u>	<u>70.7</u>
403.gcc	16	4972	25.9	<u>5004</u>	<u>25.7</u>	5064	25.4	16	<u>5053</u>	<u>25.5</u>	5089	25.3	5038	25.6
429.mcf	16	<u>2579</u>	<u>56.6</u>	2571	56.8	2603	56.0	16	<u>2579</u>	<u>56.6</u>	2571	56.8	2603	56.0
445.gobmk	16	2032	82.6	<u>2031</u>	<u>82.6</u>	2030	82.7	16	1855	90.5	<u>1856</u>	<u>90.4</u>	1861	90.2
456.hammer	16	<u>2412</u>	<u>61.9</u>	2411	61.9	2426	61.5	16	1893	78.9	1885	79.2	<u>1887</u>	<u>79.1</u>
458.sjeng	16	3068	63.1	3018	64.2	<u>3032</u>	<u>63.9</u>	16	2707	71.5	<u>2647</u>	<u>73.1</u>	2641	73.3
462.libquantum	16	10883	30.5	10868	30.5	<u>10879</u>	<u>30.5</u>	16	10903	30.4	<u>10896</u>	<u>30.4</u>	10882	30.5
464.h264ref	16	<u>2278</u>	<u>155</u>	2278	155	2271	156	16	<u>2196</u>	<u>161</u>	2187	162	2197	161
471.omnetpp	16	3142	31.8	<u>3141</u>	<u>31.8</u>	3139	31.9	16	3090	32.4	3090	32.4	<u>3090</u>	<u>32.4</u>
473.astar	16	<u>1890</u>	<u>59.4</u>	1888	59.5	1892	59.4	16	1859	60.4	1843	60.9	<u>1850</u>	<u>60.7</u>
483.xalancbmk	16	1553	71.1	<u>1552</u>	<u>71.1</u>	1550	71.2	16	<u>1427</u>	<u>77.4</u>	1428	77.3	1425	77.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Other Configuration Notes

The NovaScale T880 and the NovaScale R480 models are electronically equivalent.
The results have been measured on a NovaScale R480 model.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

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Base Portability Flags (Continued)

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

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Peak Optimization Flags (Continued)

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags.20090714.00.xml>

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