SPEC® CFP2006 Result

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Hewlett-Packard Company

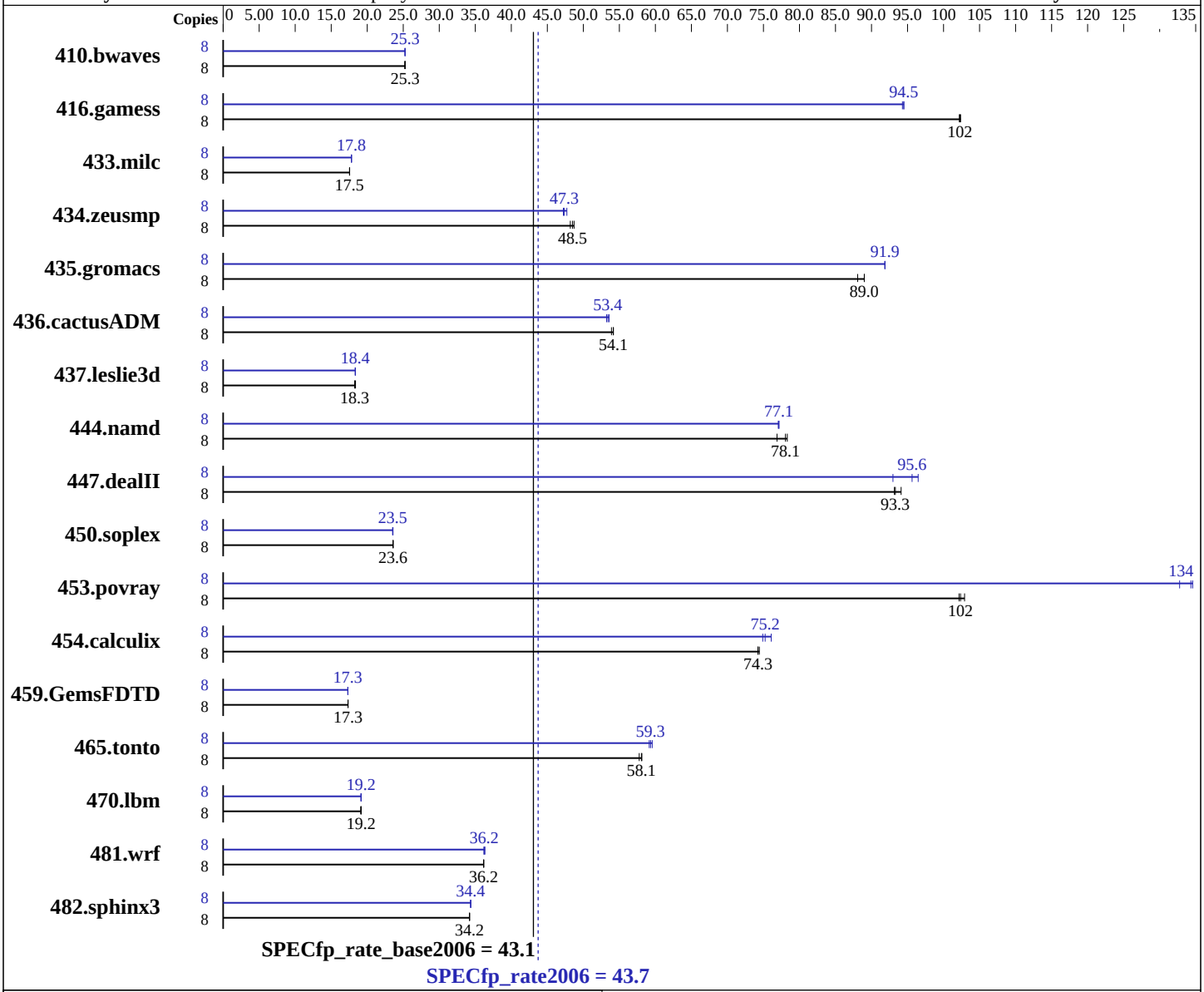
ProLiant BL480c
(1.86 GHz, Intel Xeon processor E5320)

SPECfp®_rate2006 = 43.7

SPECfp_rate_base2006 = 43.1

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_cc_c_9.1.045 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_fc_c_9.1.040
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300F CL5)	Base Pointers:	64-bit
Disk Subsystem:	1x72 GB 10k SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4303	25.3	**4304**	**25.3**	4313	25.2	8	**4304**	**25.3**	4304	25.3	4309	25.2
416.gamess	8	**1532**	**102**	1530	102	1533	102	8	1661	94.3	1658	94.5	**1658**	**94.5**
433.milc	8	4182	17.6	**4185**	**17.5**	4186	17.5	8	4122	17.8	4124	17.8	**4122**	**17.8**
434.zeusmp	8	1511	48.2	1495	48.7	**1501**	**48.5**	8	1526	47.7	1542	47.2	**1538**	**47.3**
435.gromacs	8	642	89.0	649	88.1	**642**	**89.0**	8	622	91.9	622	91.9	**622**	**91.9**
436.cactusADM	8	**1766**	**54.1**	1765	54.2	1774	53.9	8	**1790**	**53.4**	1784	53.6	1796	53.2
437.leslie3d	8	4092	18.4	**4113**	**18.3**	4116	18.3	8	4097	18.4	**4097**	**18.4**	4103	18.3
444.namd	8	**822**	**78.1**	834	76.9	819	78.3	8	**832**	**77.1**	833	77.1	832	77.2
447.dealII	8	972	94.1	**981**	**93.3**	982	93.2	8	984	93.0	**957**	**95.6**	949	96.5
450.soplex	8	**2830**	**23.6**	2826	23.6	2830	23.6	8	2836	23.5	2830	23.6	**2833**	**23.5**
453.povray	8	414	103	417	102	**416**	**102**	8	**317**	**134**	316	135	321	133
454.calculix	8	887	74.4	**889**	**74.3**	889	74.2	8	868	76.1	**877**	**75.2**	881	74.9
459.GemsFDTD	8	**4898**	**17.3**	4900	17.3	4893	17.3	8	4906	17.3	4900	17.3	**4900**	**17.3**
465.tonto	8	1354	58.1	**1356**	**58.1**	1363	57.8	8	1322	59.6	1331	59.1	**1327**	**59.3**
470.lbm	8	**5739**	**19.2**	5738	19.2	5756	19.1	8	5748	19.1	5737	19.2	**5737**	**19.2**
481.wrf	8	**2471**	**36.2**	2472	36.2	2471	36.2	8	2469	36.2	2459	36.3	**2465**	**36.2**
482.sphinx3	8	4558	34.2	4562	34.2	**4558**	**34.2**	8	**4537**	**34.4**	4540	34.3	4536	34.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Continued on next page

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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