SPEC® CFP2006 Result

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Hewlett-Packard Company

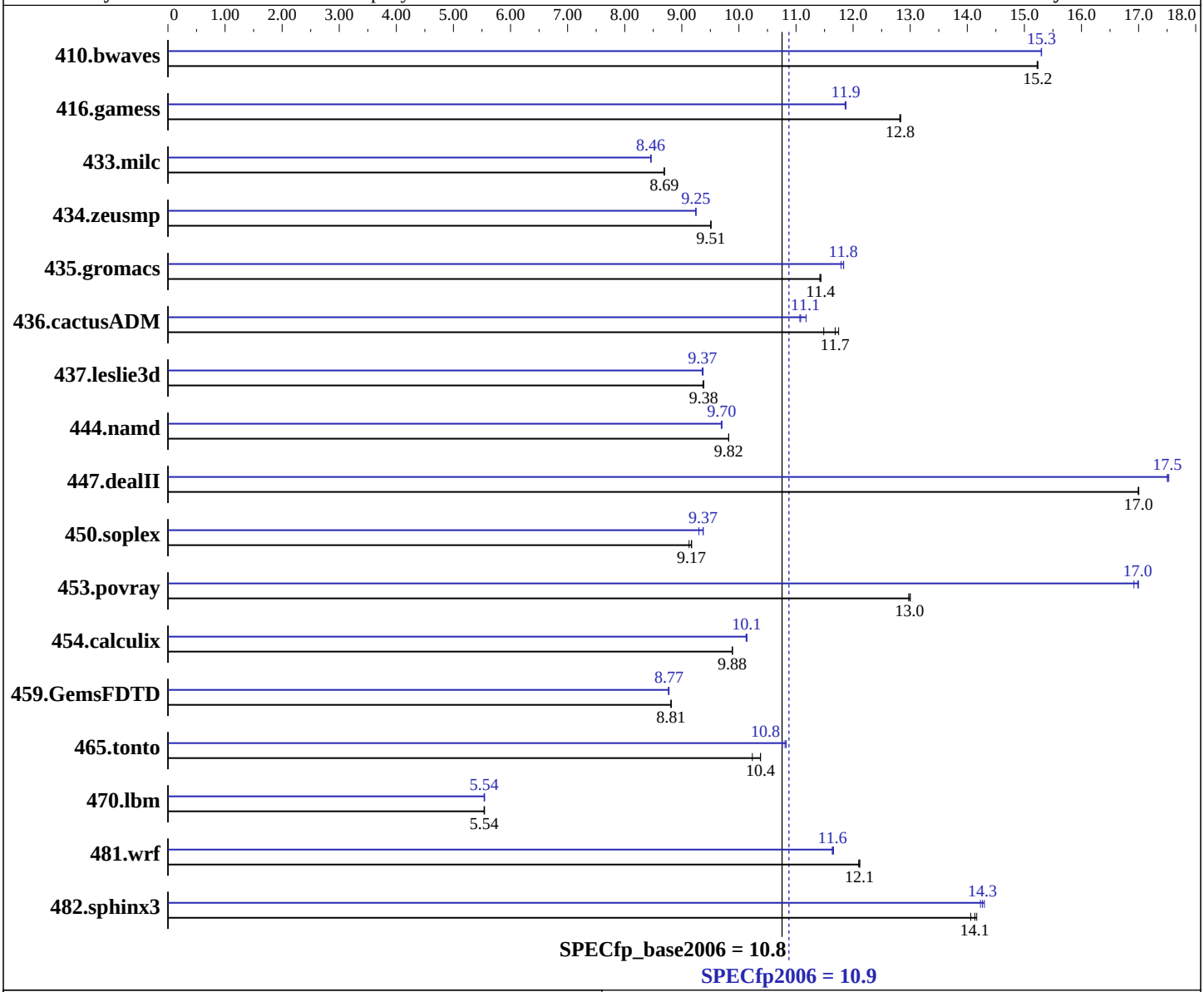
ProLiant ML370 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp®2006 = 10.9

SPECfp_base2006 = 10.8

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Nov-2006
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-default
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_cc_c_9.1.045 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_fc_c_9.1.040
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300F CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB 10 K SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	892	15.2	**892**	**15.2**	893	15.2	889	15.3	888	15.3	**888**	**15.3**
416.gamess	1528	12.8	1526	12.8	**1527**	**12.8**	**1649**	**11.9**	1649	11.9	1651	11.9
433.milc	1056	8.69	**1056**	**8.69**	1056	8.69	**1085**	**8.46**	1085	8.46	1086	8.46
434.zeusmp	957	9.51	957	9.51	**957**	**9.51**	**984**	**9.25**	984	9.25	984	9.25
435.gromacs	625	11.4	**625**	**11.4**	624	11.4	606	11.8	603	11.8	**604**	**11.8**
436.cactusADM	1017	11.7	1040	11.5	**1023**	**11.7**	1080	11.1	**1078**	**11.1**	1069	11.2
437.leslie3d	**1002**	**9.38**	1003	9.37	1002	9.39	1003	9.37	**1004**	**9.37**	1004	9.36
444.namd	**817**	**9.82**	817	9.82	816	9.82	827	9.70	828	9.69	**827**	**9.70**
447.dealII	673	17.0	673	17.0	**673**	**17.0**	653	17.5	653	17.5	**653**	**17.5**
450.soplex	909	9.17	914	9.13	**910**	**9.17**	**890**	**9.37**	897	9.30	890	9.38
453.povray	**410**	**13.0**	410	13.0	409	13.0	313	17.0	314	16.9	**313**	**17.0**
454.calculix	834	9.89	835	9.88	**835**	**9.88**	814	10.1	815	10.1	**814**	**10.1**
459.GemsFDTD	1204	8.81	**1204**	**8.81**	1204	8.81	1209	8.78	1211	8.76	**1209**	**8.77**
465.tonto	948	10.4	**948**	**10.4**	961	10.2	909	10.8	**911**	**10.8**	915	10.8
470.lbm	2478	5.55	2481	5.54	**2481**	**5.54**	2477	5.55	**2480**	**5.54**	2481	5.54
481.wrf	922	12.1	923	12.1	**922**	**12.1**	960	11.6	958	11.7	**960**	**11.6**
482.sphinx3	1376	14.2	**1379**	**14.1**	1386	14.1	**1366**	**14.3**	1363	14.3	1370	14.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
Single processor kernel used.
```

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:46:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 March 2007.