



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®_rate2006 = 26.0

ProLiant ML370 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate_base2006 = 25.6

CPU2006 license: 3

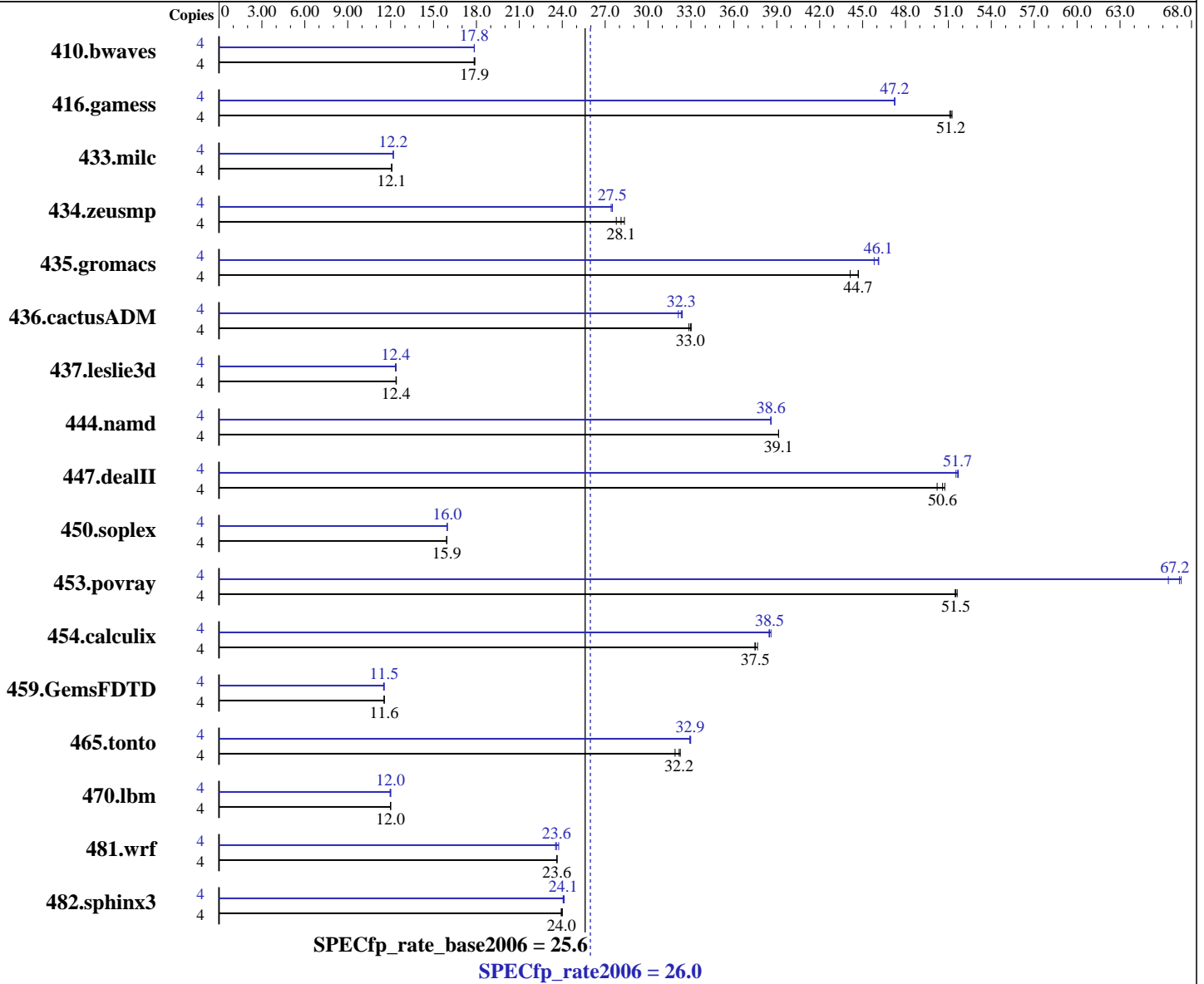
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64)
 kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_cc_c_9.1.045
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_fc_c_9.1.040
 Auto Parallel: No

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 2x72 GB 10 K SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	3039	17.9	3042	17.9	3047	17.8	4	3046	17.8	3044	17.9	3046	17.8
416.gamess	4	1528	51.3	1530	51.2	1533	51.1	4	1657	47.3	1658	47.2	1658	47.2
433.milc	4	3040	12.1	3039	12.1	3042	12.1	4	3012	12.2	3014	12.2	3013	12.2
434.zeusmp	4	1310	27.8	1284	28.3	1295	28.1	4	1323	27.5	1323	27.5	1327	27.4
435.gromacs	4	647	44.1	639	44.7	639	44.7	4	623	45.8	620	46.1	619	46.1
436.cactusADM	4	1455	32.9	1448	33.0	1450	33.0	4	1479	32.3	1476	32.4	1488	32.1
437.leslie3d	4	3031	12.4	3036	12.4	3038	12.4	4	3049	12.3	3035	12.4	3041	12.4
444.namd	4	820	39.1	820	39.1	820	39.1	4	831	38.6	831	38.6	831	38.6
447.dealII	4	904	50.6	911	50.2	902	50.7	4	888	51.5	885	51.7	886	51.7
450.soplex	4	2096	15.9	2093	15.9	2095	15.9	4	2089	16.0	2090	16.0	2092	15.9
453.povray	4	412	51.6	413	51.5	413	51.5	4	316	67.3	321	66.4	317	67.2
454.calculix	4	880	37.5	876	37.7	881	37.5	4	858	38.5	855	38.6	857	38.5
459.GemsFDTD	4	3676	11.5	3674	11.6	3672	11.6	4	3688	11.5	3674	11.6	3677	11.5
465.tonto	4	1223	32.2	1220	32.3	1234	31.9	4	1195	32.9	1195	32.9	1193	33.0
470.lbm	4	4582	12.0	4581	12.0	4581	12.0	4	4579	12.0	4579	12.0	4595	12.0
481.wrf	4	1892	23.6	1891	23.6	1890	23.6	4	1881	23.8	1893	23.6	1899	23.5
482.sphinx3	4	3247	24.0	3254	24.0	3258	23.9	4	3240	24.1	3238	24.1	3230	24.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks:

icc

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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