SPEC® CFP2006 Result

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Itautec

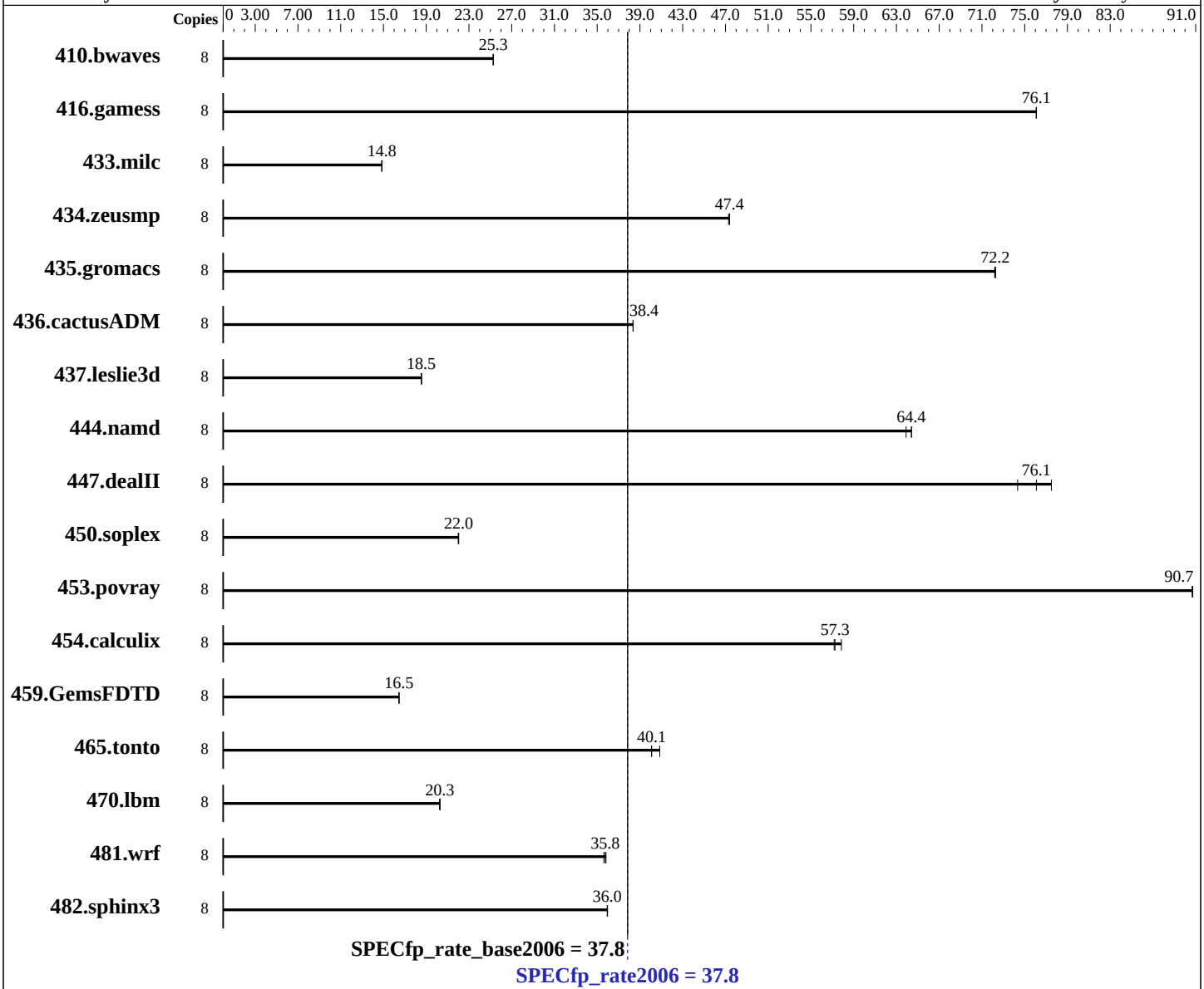
Servidor Itautec MX201 (Intel Xeon E5310)

SPECfp®_rate2006 = 37.8

SPECfp_rate_base2006 = 37.8

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1066MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Intel Fortran Compiler for IA32 version 9.1 Package ID W_FC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem: 73 GB SCSI, 10000RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**4301**	**25.3**	4299	25.3	4304	25.3	8	**4301**	**25.3**	4299	25.3	4304	25.3
416.gamess	8	2059	76.1	2058	76.1	**2059**	**76.1**	8	2059	76.1	2058	76.1	**2059**	**76.1**
433.milc	8	4948	14.8	4942	14.9	**4947**	**14.8**	8	4948	14.8	4942	14.9	**4947**	**14.8**
434.zeusmp	8	1536	47.4	**1537**	**47.4**	1539	47.3	8	1536	47.4	**1537**	**47.4**	1539	47.3
435.gromacs	8	**791**	**72.2**	790	72.3	791	72.2	8	**791**	**72.2**	790	72.3	791	72.2
436.cactusADM	8	2492	38.4	2493	38.3	**2492**	**38.4**	8	2492	38.4	2493	38.3	**2492**	**38.4**
437.leslie3d	8	4057	18.5	4050	18.6	**4055**	**18.5**	8	4057	18.5	4050	18.6	**4055**	**18.5**
444.namd	8	1004	63.9	**996**	**64.4**	996	64.4	8	1004	63.9	**996**	**64.4**	996	64.4
447.dealII	8	1181	77.5	**1203**	**76.1**	1231	74.3	8	1181	77.5	**1203**	**76.1**	1231	74.3
450.soplex	8	3027	22.0	3033	22.0	**3028**	**22.0**	8	3027	22.0	3033	22.0	**3028**	**22.0**
453.povray	8	**469**	**90.7**	469	90.7	469	90.7	8	**469**	**90.7**	469	90.7	469	90.7
454.calculix	8	1141	57.9	1154	57.2	**1153**	**57.3**	8	1141	57.9	1154	57.2	**1153**	**57.3**
459.GemsFDTD	8	5162	16.4	**5159**	**16.5**	5155	16.5	8	5162	16.4	**5159**	**16.5**	5155	16.5
465.tonto	8	1964	40.1	1927	40.9	**1964**	**40.1**	8	1964	40.1	1927	40.9	**1964**	**40.1**
470.lbm	8	5425	20.3	**5422**	**20.3**	5419	20.3	8	5425	20.3	**5422**	**20.3**	5419	20.3
481.wrf	8	**2499**	**35.8**	2495	35.8	2508	35.6	8	**2499**	**35.8**	2495	35.8	2508	35.6
482.sphinx3	8	4335	36.0	4339	35.9	**4337**	**36.0**	8	4335	36.0	4339	35.9	**4337**	**36.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
This result was measured on the Servidor Itautec MX221.
The Servidor Itautec MX221 and the Servidor Itautec MX201
are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: basepeak = yes

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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