SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

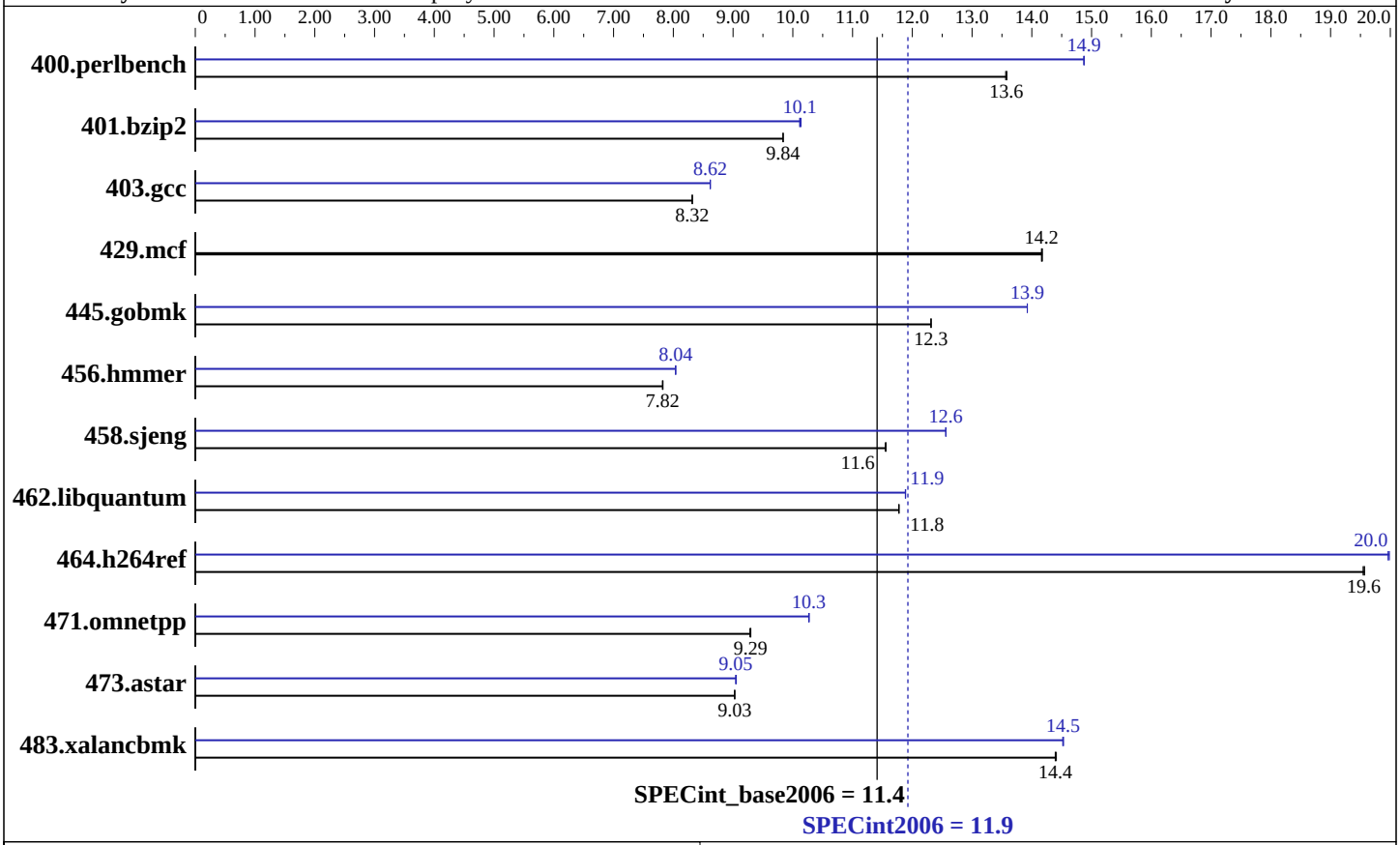
ProLiant BL460c
(1.86 GHz, Intel Xeon processor E5320)

SPECint®2006 = 11.9

SPECint_base2006 = 11.4

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300 CL5)
Disk Subsystem:	1x72 GB 10k SAS
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise x64 Edition SP1
Compiler:	Intel C++ Compiler for 32-bit applications, Version 9.1, Build 20061103Z Package ID: W_CC_C_9.1.033 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant BL460c
(1.86 GHz, Intel Xeon processor E5320)

SPECint2006 = 11.9

SPECint_base2006 = 11.4

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	720	13.6	719	13.6	**720**	**13.6**	657	14.9	**657**	**14.9**	657	14.9
401.bzip2	**981**	**9.84**	981	9.84	981	9.84	952	10.1	954	10.1	**953**	**10.1**
403.gcc	**968**	**8.32**	968	8.32	968	8.32	934	8.62	**934**	**8.62**	934	8.62
429.mcf	643	14.2	644	14.2	**644**	**14.2**	643	14.2	644	14.2	**644**	**14.2**
445.gobmk	852	12.3	**852**	**12.3**	852	12.3	753	13.9	**753**	**13.9**	753	13.9
456.hmmer	1192	7.83	1193	7.82	**1193**	**7.82**	**1161**	**8.04**	1161	8.04	1160	8.04
458.sjeng	**1047**	**11.6**	1048	11.5	1047	11.6	964	12.6	963	12.6	**963**	**12.6**
462.libquantum	**1760**	**11.8**	1760	11.8	1759	11.8	1744	11.9	1743	11.9	**1743**	**11.9**
464.h264ref	**1131**	**19.6**	1131	19.6	1132	19.5	1108	20.0	**1108**	**20.0**	1107	20.0
471.omnetpp	673	9.28	672	9.30	**672**	**9.29**	609	10.3	608	10.3	**609**	**10.3**
473.astar	777	9.03	778	9.03	**778**	**9.03**	**776**	**9.05**	775	9.05	776	9.04
483.xalancbmk	479	14.4	**479**	**14.4**	479	14.4	475	14.5	**475**	**14.5**	475	14.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
                -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

Continued on next page

Peak Optimization Flags (Continued)

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 400.perlbench`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features`
`/F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:41:10 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 March 2007.