SPEC® CINT2006 Result

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IBM Corporation

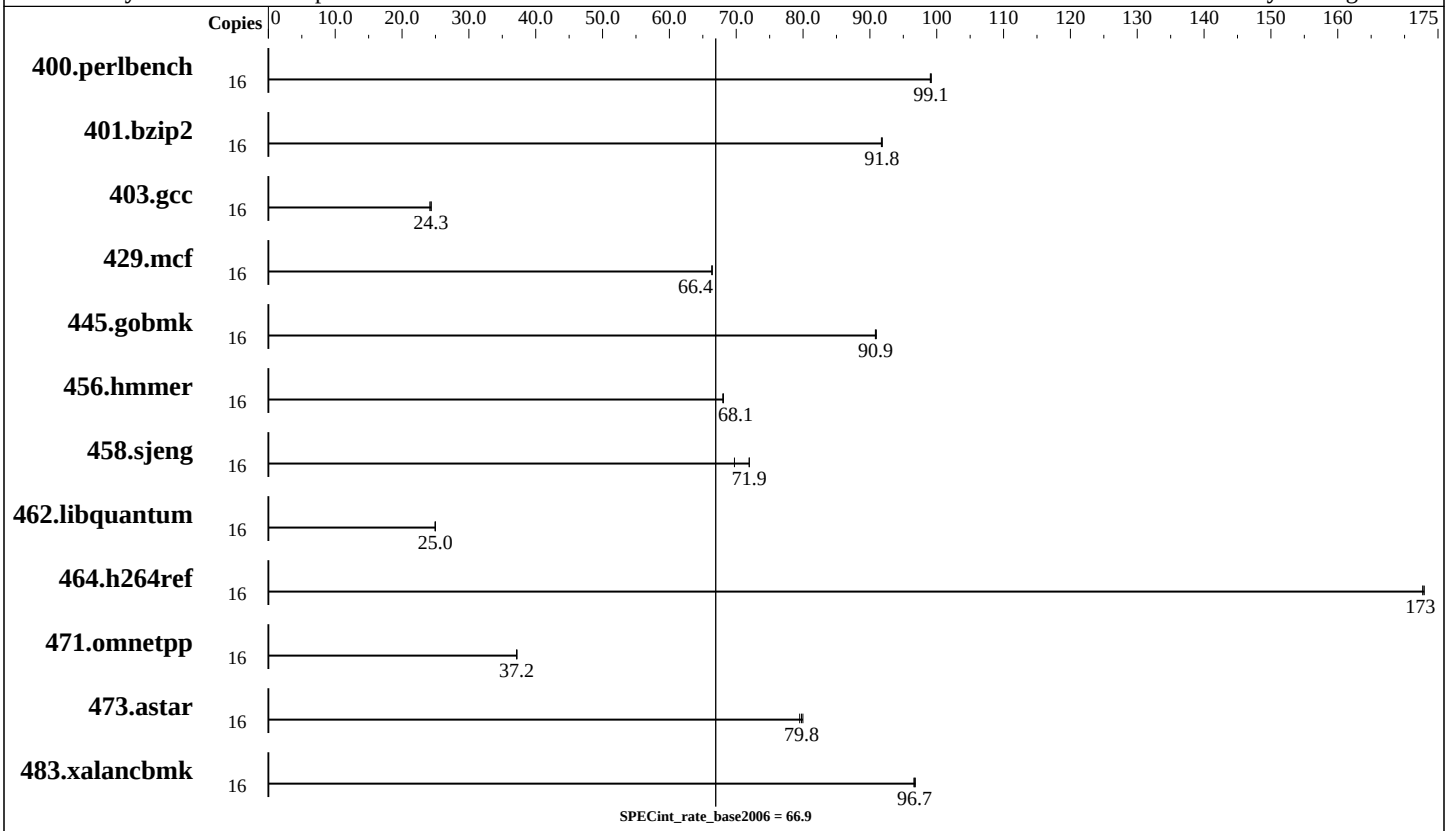
IBM System x3850 (Intel Xeon 7150N)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 66.9

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon 7150N
CPU Characteristics:	667 MHz bus
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	16 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16 x 2048 MB ECC PC2-3200)
Disk Subsystem:	73 GB SAS, 10k RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1578	99.0	**1577**	**99.1**	1576	99.2							
401.bzip2	16	1683	91.7	1681	91.8	**1681**	**91.8**							
403.gcc	16	5286	24.4	5332	24.2	**5296**	**24.3**							
429.mcf	16	2198	66.4	**2198**	**66.4**	2198	66.4							
445.gobmk	16	1848	90.8	**1847**	**90.9**	1845	91.0							
456.hmmer	16	2195	68.0	2193	68.1	**2193**	**68.1**							
458.sjeng	16	2691	71.9	2776	69.7	**2691**	**71.9**							
462.libquantum	16	**13276**	**25.0**	13279	25.0	13272	25.0							
464.h264ref	16	2050	173	**2050**	**173**	2047	173							
471.omnetpp	16	**2691**	**37.2**	2690	37.2	2692	37.2							
473.astar	16	1413	79.5	1404	80.0	**1408**	**79.8**							
483.xalancbmk	16	**1142**	**96.7**	1143	96.6	1141	96.8							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios Settings
  Hardware Prefetch enabled
  Memory Array set to High-Performance Memory Array
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml`

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