



# SPEC® CINT2006 Result

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## IBM Corporation

## SPECint®\_rate2006 = Not Run

## IBM System x3850 (Intel Xeon 7110N)

## SPECint\_rate\_base2006 = 46.1

CPU2006 license: 11

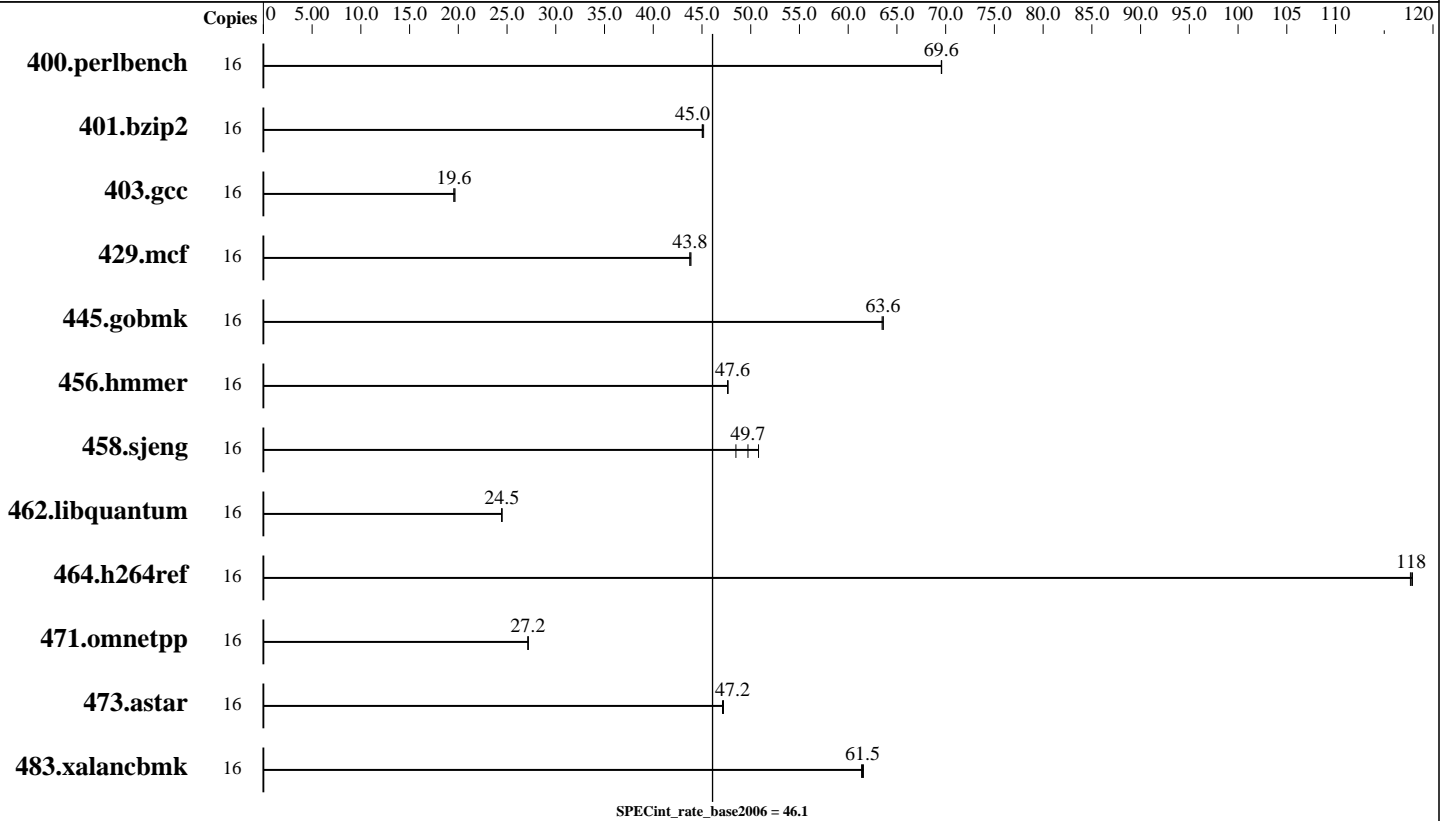
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Feb-2007

Hardware Availability: Oct-2006

Software Availability: Aug-2006



### Hardware

CPU Name: Intel Xeon 7110N  
 CPU Characteristics: 667 MHz bus  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)  
 Disk Subsystem: 73 GB SAS, 10k RPM  
 Other Hardware: None

### Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	16	2247	69.6	<u>2247</u>	<u>69.6</u>	2246	69.6									
401.bzip2	16	3428	45.0	<u>3428</u>	<u>45.0</u>	3418	45.2									
403.gcc	16	6588	19.6	<u>6584</u>	<u>19.6</u>	6551	19.7									
429.mcf	16	3335	43.8	3326	43.9	<u>3333</u>	<u>43.8</u>									
445.gobmk	16	2639	63.6	2644	63.5	<u>2640</u>	<u>63.6</u>									
456.hammer	16	3132	47.7	<u>3133</u>	<u>47.6</u>	3134	47.6									
458.sjeng	16	3993	48.5	<u>3894</u>	<u>49.7</u>	3811	50.8									
462.libquantum	16	13557	24.5	13546	24.5	<u>13548</u>	<u>24.5</u>									
464.h264ref	16	3009	118	3003	118	<u>3007</u>	<u>118</u>									
471.omnetpp	16	3685	27.1	3680	27.2	<u>3682</u>	<u>27.2</u>									
473.astar	16	<u>2380</u>	<u>47.2</u>	2380	47.2	2384	47.1									
483.xalancbmk	16	<u>1796</u>	<u>61.5</u>	1799	61.4	1793	61.6									

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

### Bios Settings

Hardware Prefetch enabled

Memory Array set to High-Performance Memory Array

## Base Compiler Invocation

### C benchmarks:

```
icl -Qvc7.1 -Qc99
```

### C++ benchmarks:

```
icl -Qvc7.1
```

## Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

## Base Optimization Flags

### C benchmarks:

```
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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