# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

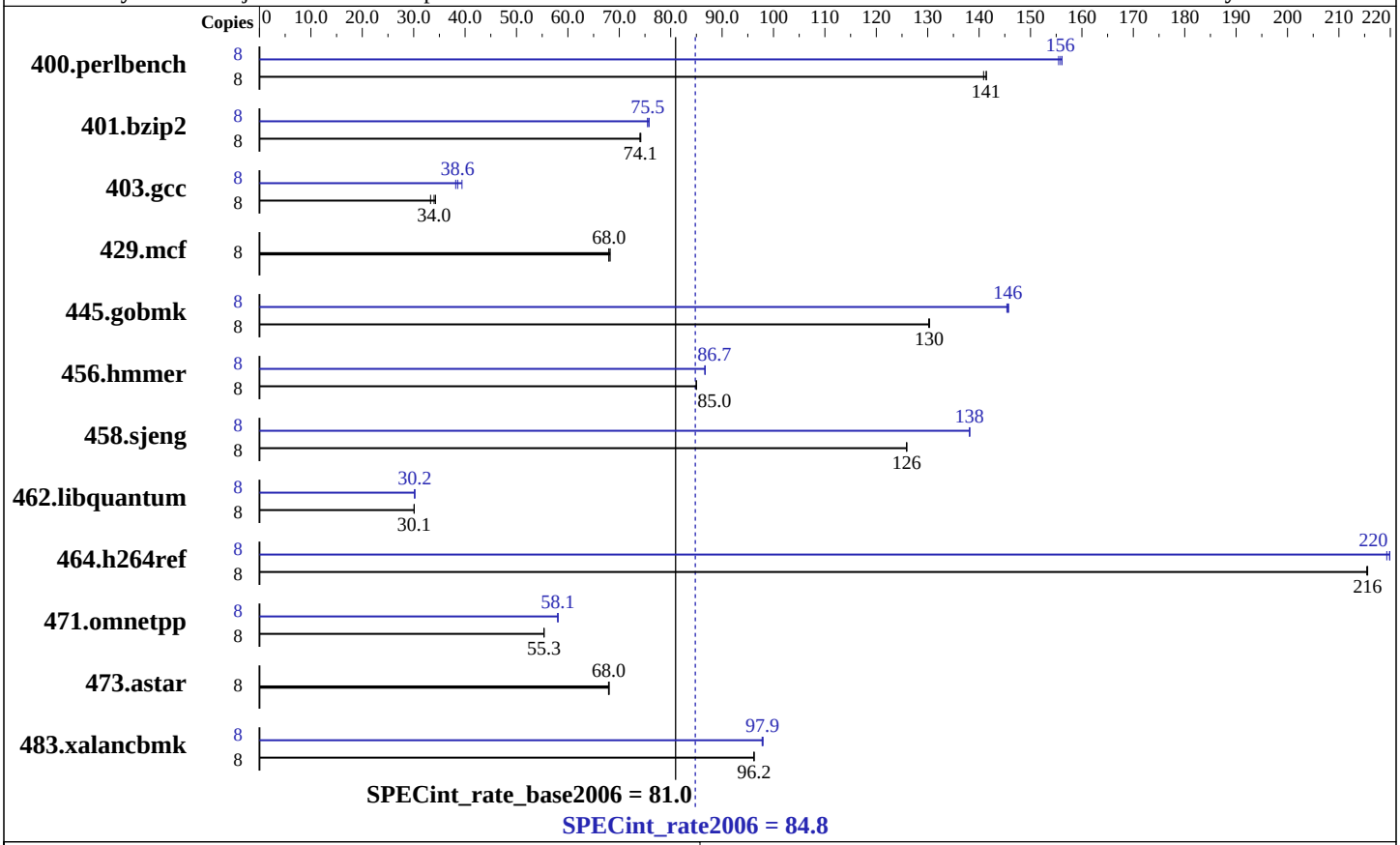
PRIMERGY TX300 S3, Intel Xeon processor X5355, 2.66 GHz

**SPECint®_rate2006 = 84.8**

**SPECint_rate_base2006 = 81.0**

**CPU2006 license:** 22
**Test sponsor:** Fujitsu Siemens Computers
**Tested by:** Fujitsu Siemens Computers

**Test date:** Feb-2007
**Hardware Availability:** Jan-2007
**Software Availability:** Nov-2006




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5355 |
| CPU Characteristics: | X5355 |
| CPU MHz: | 2667 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 8 MB I+D on chip per chip, 4 MB shared / 2 cores |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC) |
| Disk Subsystem: | SAS (73GB 15400 rpm) |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows Server 2003 x64 Enterprise Edition + SP1 (64-bit) |
| Compiler: | Intel C++ Compiler for IA32 version 9.1 - Build 20061103Z, Package-ID W_CC_C_9.1.033 Microsoft Visual Studio 2005 (libr. & linker) |
| Auto Parallel: | No |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32-bit |
| Other Software: | Smart Heap Library, Version 8 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 555 | 141 | **553** | **141** | 553 | 141 | 8 | 503 | 155 | **502** | **156** | 501 | 156 |
| 401.bzip2 | 8 | 1041 | 74.2 | 1043 | 74.0 | **1042** | **74.1** | 8 | **1022** | **75.5** | 1022 | 75.5 | 1018 | 75.8 |
| 403.gcc | 8 | 1935 | 33.3 | 1880 | 34.2 | **1896** | **34.0** | 8 | **1670** | **38.6** | 1687 | 38.2 | 1635 | 39.4 |
| 429.mcf | 8 | 1074 | 68.0 | **1073** | **68.0** | 1069 | 68.2 | 8 | 1074 | 68.0 | **1073** | **68.0** | 1069 | 68.2 |
| 445.gobmk | 8 | **644** | **130** | 645 | 130 | 644 | 130 | 8 | 577 | 145 | **576** | **146** | 576 | 146 |
| 456.hmmer | 8 | 879 | 85.0 | **878** | **85.0** | 878 | 85.0 | 8 | **861** | **86.7** | 861 | 86.7 | 861 | 86.7 |
| 458.sjeng | 8 | 769 | 126 | **769** | **126** | 769 | 126 | 8 | 701 | 138 | 701 | 138 | **701** | **138** |
| 462.libquantum | 8 | **5508** | **30.1** | 5510 | 30.1 | 5507 | 30.1 | 8 | 5491 | 30.2 | **5489** | **30.2** | 5488 | 30.2 |
| 464.h264ref | 8 | **821** | **216** | 821 | 216 | 822 | 215 | 8 | **805** | **220** | 807 | 219 | 805 | 220 |
| 471.omnetpp | 8 | 903 | 55.4 | **904** | **55.3** | 904 | 55.3 | 8 | 862 | 58.0 | **861** | **58.1** | 861 | 58.1 |
| 473.astar | 8 | 827 | 67.9 | **826** | **68.0** | 826 | 68.0 | 8 | 827 | 67.9 | **826** | **68.0** | 826 | 68.0 |
| 483.xalancbmk | 8 | 574 | 96.2 | **574** | **96.2** | 574 | 96.2 | 8 | 564 | 98.0 | 564 | 97.8 | **564** | **97.9** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
The system bus runs at 1333 MHz

BIOS configuration:
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
the PRIMERGY TX300 S3 are electronically equivalent.
For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```

## Base Compiler Invocation

C benchmarks:
`icl -Qvc8 -Qc99`

C++ benchmarks:
`icl -Qvc8`

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_NO_NEED_VA_COPY`
403.gcc: `-DSPEC_CPU_WIN32`

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## Base Portability Flags (Continued)

464.h264ref: `-DWIN32 -DSPEC_CPU_NO_INTTYPES`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

## Base Optimization Flags

C benchmarks:
`-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks:
`icl -Qvc8 -Qc99`

C++ benchmarks:
`icl -Qvc8`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_NO_NEED_VA_COPY`
403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DWIN32 -DSPEC_CPU_NO_INTTYPES`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

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# Peak Optimization Flags (Continued)

401.bzip2: Same as 400.perlbench

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000 -link -FORCE:MULTIPLE

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: Same as 471.omnetpp

# Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.09.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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