



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor X5355,
2.66 GHz

SPECfp®_rate2006 = 60.2

SPECfp_rate_base2006 = 58.9

CPU2006 license: 22

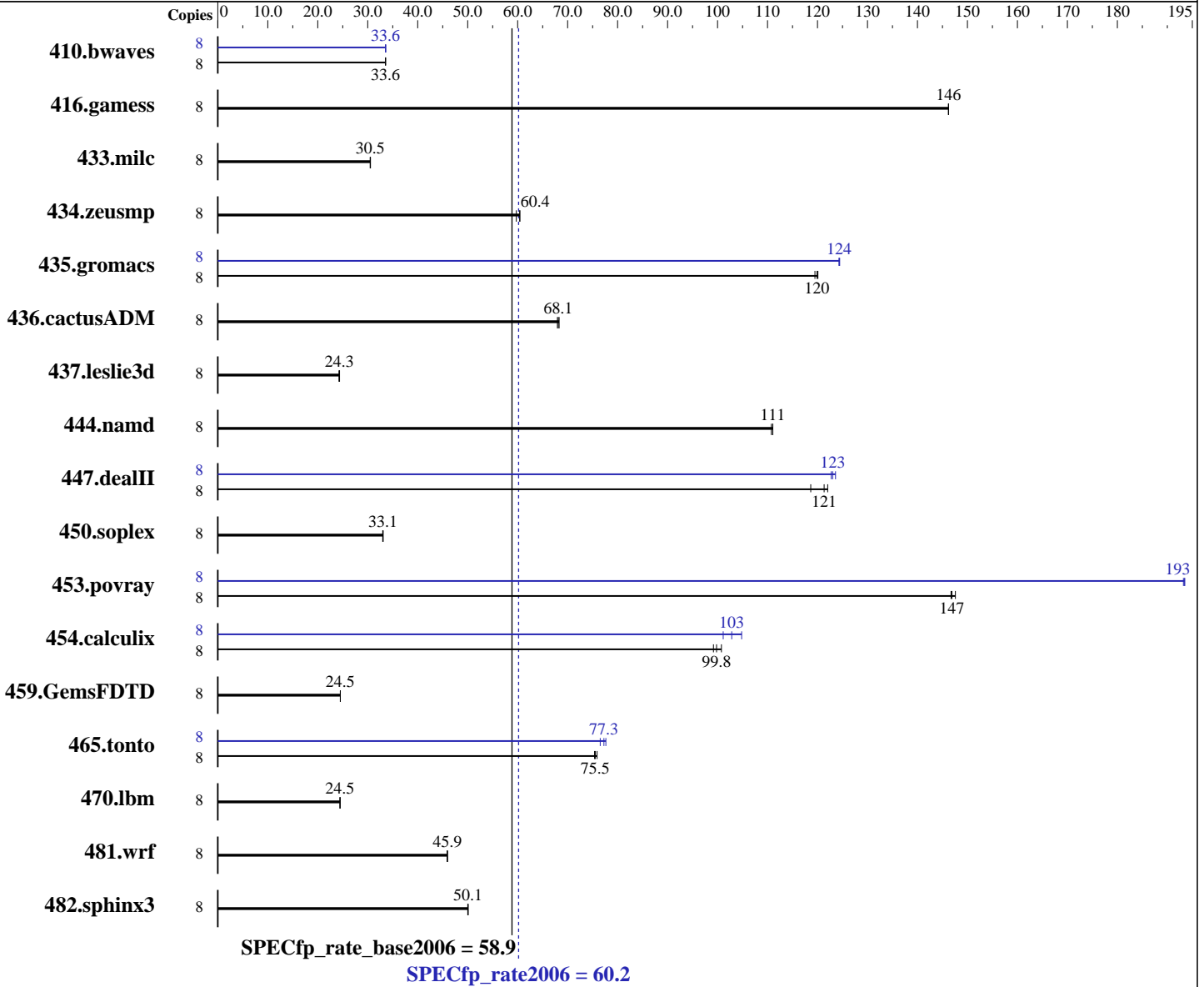
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2007

Hardware Availability: Jan-2007

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: X5355
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
 Compiler: Intel C++ Compiler for Linux64 EM64T version 9.1 Build 20061101, Package-ID l_cc_p_9.1.045
 Intel Fortran Compiler for Linux64 EM64T version 9.1 Build 20061101 Package ID: l_fc_p_9.1.040
 Auto Parallel: No
 File System: ReiserFS
 System State: Default

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: SAS (73GB 15400 rpm)
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	8	3240	33.6	3240	33.6	3238	33.6	8	3238	33.6	3238	33.6	3237	33.6		
416.gamess	8	1071	146	1071	146	1071	146	8	1071	146	1071	146	1071	146		
433.milc	8	2407	30.5	2403	30.6	2408	30.5	8	2407	30.5	2403	30.6	2408	30.5		
434.zeusmp	8	1204	60.4	1205	60.4	1219	59.7	8	1204	60.4	1205	60.4	1219	59.7		
435.gromacs	8	476	120	478	119	476	120	8	459	124	459	124	459	124		
436.cactusADM	8	1403	68.1	1407	68.0	1400	68.3	8	1403	68.1	1407	68.0	1400	68.3		
437.leslie3d	8	3086	24.4	3091	24.3	3101	24.3	8	3086	24.4	3091	24.3	3101	24.3		
444.namd	8	579	111	578	111	578	111	8	579	111	578	111	578	111		
447.dealII	8	754	121	771	119	750	122	8	740	124	746	123	744	123		
450.soplex	8	2018	33.1	2022	33.0	2017	33.1	8	2018	33.1	2022	33.0	2017	33.1		
453.povray	8	290	147	290	147	288	148	8	220	193	220	193	220	193		
454.calculix	8	665	99.2	661	99.8	655	101	8	630	105	653	101	642	103		
459.GemsFDTD	8	3461	24.5	3461	24.5	3461	24.5	8	3461	24.5	3461	24.5	3461	24.5		
465.tonto	8	1044	75.4	1037	75.9	1042	75.5	8	1019	77.3	1014	77.7	1029	76.5		
470.lbm	8	4493	24.5	4493	24.5	4493	24.5	8	4493	24.5	4493	24.5	4493	24.5		
481.wrf	8	1945	45.9	1948	45.9	1944	46.0	8	1945	45.9	1948	45.9	1944	46.0		
482.sphinx3	8	3112	50.1	3117	50.0	3115	50.1	8	3112	50.1	3117	50.0	3115	50.1		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

The system bus runs at 1333 MHz

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

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General Notes (Continued)

<http://www.fujitsu-siemens.com/countries>

For information about Fujitsu Siemens Computers in your country please see:

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

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Base Optimization Flags (Continued)

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof_gen(pass 1) -prof_use(pass 2) -fast

450.soplex: basepeak = yes

453.povray: Same as 447.dealII

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: `-prof_gen(pass 1) -prof_use(pass 2) -fast`

416.gamess: `basepeak = yes`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: Same as 410.bwaves

Benchmarks using both Fortran and C:

435.gromacs: `-prof_gen(pass 1) -prof_use(pass 2) -fast`

436.cactusADM: `basepeak = yes`

454.calculix: Same as 435.gromacs

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.18.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.18.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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