SPEC® CFP2006 Result

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Fujitsu Siemens Computers

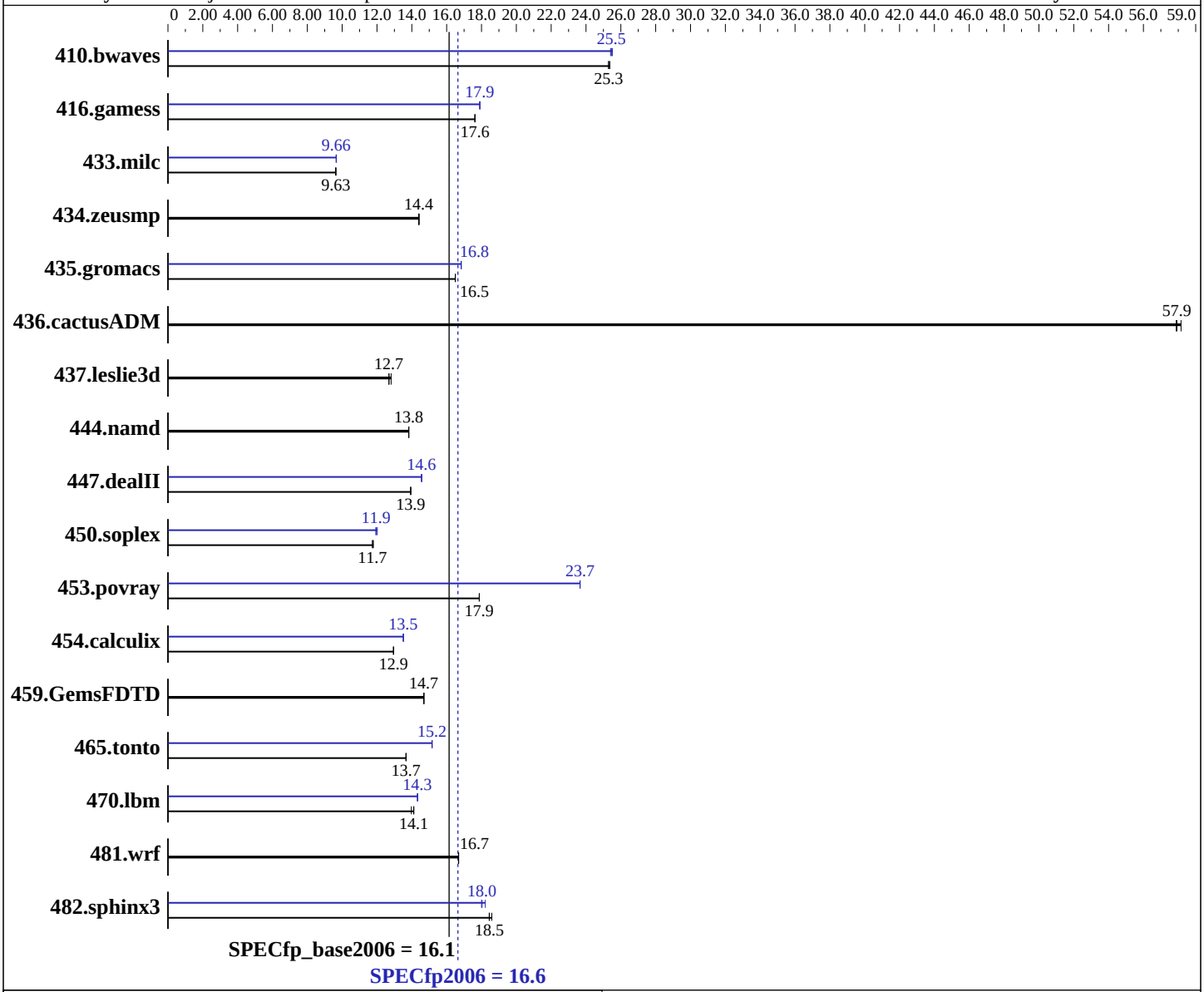
CELSIUS R540, Intel Xeon processor X5355

SPECfp®2006 = 16.6

SPECfp_base2006 = 16.1

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Feb-2007
Hardware Availability: Nov-2006
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	X5355
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Windows XP, 64 bit Edition
Compiler:	Intel C++ Compiler for EM64T-based applications - version 9.1, Build 20061104 Intel Fortran Compiler for EM64T-based applications - version 9.1, Build 20061104 Microsoft Visual Studio 2005 (for libraries)
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB DDR2 5300F, 2 rank, CL5-5-5,with ECC)
Disk Subsystem: SATA II 7200 rpm
Other Hardware: None

System State: Default
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	536	25.4	537	25.3	**537**	**25.3**	533	25.5	535	25.4	**534**	**25.5**
416.gamess	1110	17.6	**1111**	**17.6**	1111	17.6	**1094**	**17.9**	1094	17.9	1093	17.9
433.milc	952	9.65	954	9.63	**953**	**9.63**	951	9.66	**950**	**9.66**	949	9.67
434.zeusmp	631	14.4	**632**	**14.4**	633	14.4	631	14.4	**632**	**14.4**	633	14.4
435.gromacs	**433**	**16.5**	433	16.5	433	16.5	424	16.8	424	16.8	**424**	**16.8**
436.cactusADM	**206**	**57.9**	205	58.2	206	57.9	**206**	**57.9**	205	58.2	206	57.9
437.leslie3d	734	12.8	741	12.7	**741**	**12.7**	734	12.8	741	12.7	**741**	**12.7**
444.namd	**580**	**13.8**	580	13.8	580	13.8	**580**	**13.8**	580	13.8	580	13.8
447.dealII	**821**	**13.9**	821	13.9	821	13.9	**786**	**14.6**	786	14.6	786	14.6
450.soplex	710	11.7	707	11.8	**710**	**11.7**	694	12.0	**698**	**11.9**	698	11.9
453.povray	298	17.9	298	17.9	**298**	**17.9**	225	23.7	**225**	**23.7**	225	23.7
454.calculix	637	13.0	**637**	**12.9**	637	12.9	**611**	**13.5**	611	13.5	611	13.5
459.GemsFDTD	**722**	**14.7**	723	14.7	722	14.7	**722**	**14.7**	723	14.7	722	14.7
465.tonto	**720**	**13.7**	719	13.7	720	13.7	649	15.2	**649**	**15.2**	649	15.2
470.lbm	973	14.1	983	14.0	**974**	**14.1**	**959**	**14.3**	959	14.3	959	14.3
481.wrf	669	16.7	**670**	**16.7**	670	16.7	669	16.7	**670**	**16.7**	670	16.7
482.sphinx3	1048	18.6	**1056**	**18.5**	1057	18.4	1070	18.2	1083	18.0	**1081**	**18.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
BIOS default settings have been used, except:
Snoop Filter Enabled
Adjacent Cache Line Prefetch Enabled
```

General Notes

```
For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc8 -Qc99 ifort
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -D_Complex= -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -D_Complex= -DSPEC_CPU_P64
436.cactusADM: -D_Complex= -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -D_Complex= -DSPEC_CPU_P64 -DBOOST_NO_INTRINSIC_WCHAR_T -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -D_Complex= -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -D_Complex= -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -D_Complex= -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
```
-fast -Qparallel -F950000000
```

C++ benchmarks:
```
-fast -Qparallel -Qcxx-features -F950000000
```

Fortran benchmarks:
```
-fast -Qparallel -F950000000
```

Continued on next page

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-fast -Qparallel -F950000000`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc8 -Qc99`

C++ benchmarks:
`icl -Qvc8`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icl -Qvc8 -Qc99 ifort`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F950000000`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx-features -F950000000`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qparallel -F950000000`

416.gamess: `-fast -F950000000`

Continued on next page

Peak Optimization Flags (Continued)

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `Same as 410.bwaves`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F950000000`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.16.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.16.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:31:51 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 7 March 2007.