



SPEC® CINT2006 Result

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Itautec

SPECint®_rate2006 = 68.1

Servidor Itautec LX211 (Intel Xeon E5345)

SPECint_rate_base2006 = 68.1

CPU2006 license: 9001

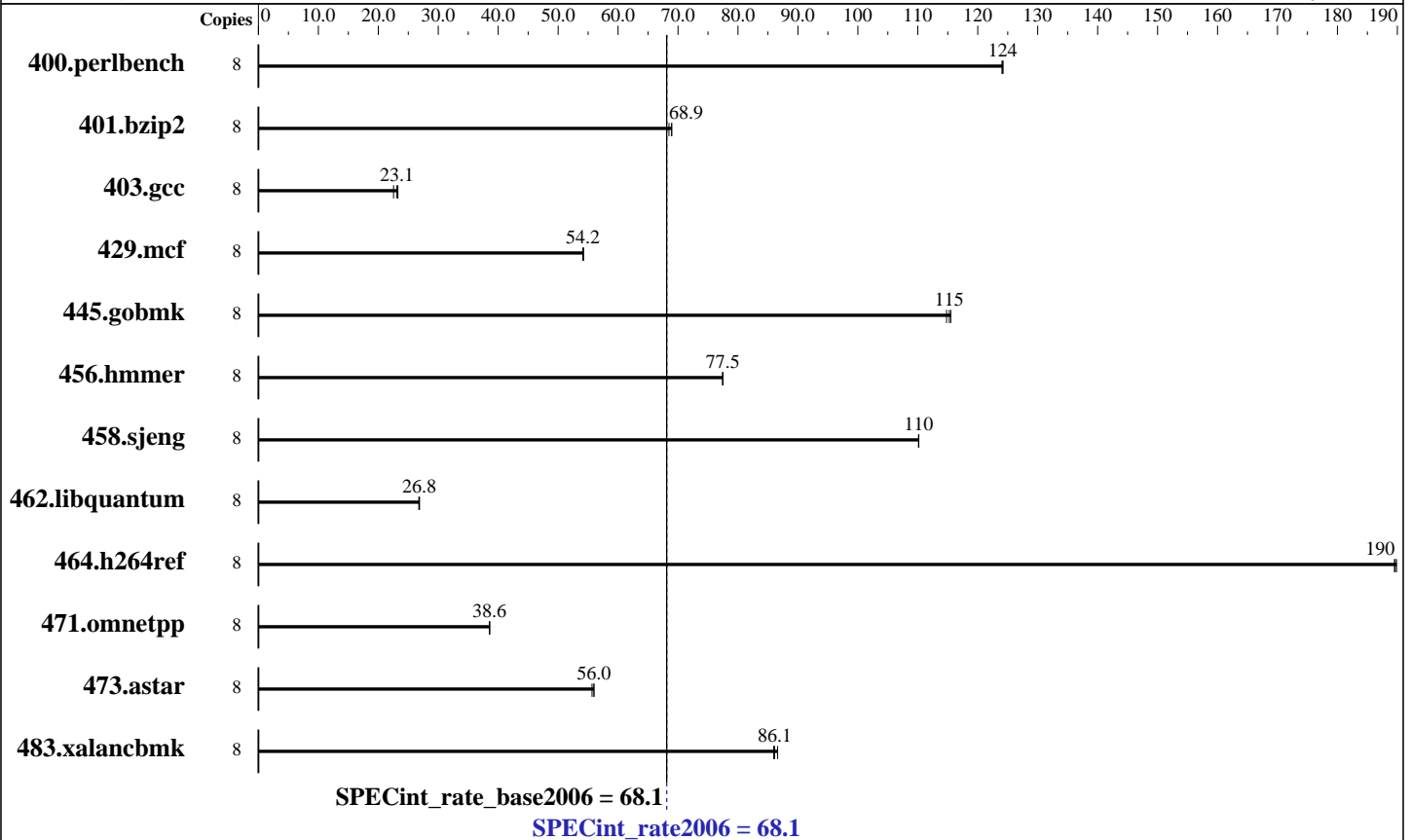
Test date: Feb-2007

Test sponsor: Itautec

Hardware Availability: Feb-2007

Tested by: Itautec

Software Availability: May-2006



Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2330
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1-2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
 Disk Subsystem: 80 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.025 Build no 20060519Z
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Microquill SmartHeap Library v.8.0 for SMP



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	629	124	630	124	629	124	8	629	124	630	124	629	124
401.bzip2	8	1127	68.5	1120	68.9	1120	68.9	8	1127	68.5	1120	68.9	1120	68.9
403.gcc	8	2857	22.5	2788	23.1	2768	23.3	8	2857	22.5	2788	23.1	2768	23.3
429.mcf	8	1347	54.2	1348	54.1	1345	54.2	8	1347	54.2	1348	54.1	1345	54.2
445.gobmk	8	731	115	727	116	728	115	8	731	115	727	116	728	115
456.hammer	8	964	77.4	963	77.5	964	77.5	8	964	77.4	963	77.5	964	77.5
458.sjeng	8	879	110	879	110	879	110	8	879	110	879	110	879	110
462.libquantum	8	6194	26.8	6169	26.9	6176	26.8	8	6194	26.8	6169	26.9	6176	26.8
464.h264ref	8	932	190	934	190	934	190	8	932	190	934	190	934	190
471.omnetpp	8	1297	38.6	1296	38.6	1297	38.5	8	1297	38.6	1296	38.6	1297	38.5
473.astar	8	1009	55.7	1003	56.0	1003	56.0	8	1009	55.7	1003	56.0	1003	56.0
483.xalancbmk	8	637	86.6	642	86.0	641	86.1	8	637	86.6	642	86.0	641	86.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlSMPMt.lib -link /FORCE:MULTIPLE
C++ benchmarks:
-fast -Qcxx_features /F512000000 shlSMPMt.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Itaotec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Itaotec-ic91-flags.20090714.xml>



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For other inquiries, please contact webmaster@spec.org.

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