SPEC® CINT2006 Result

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Itautec

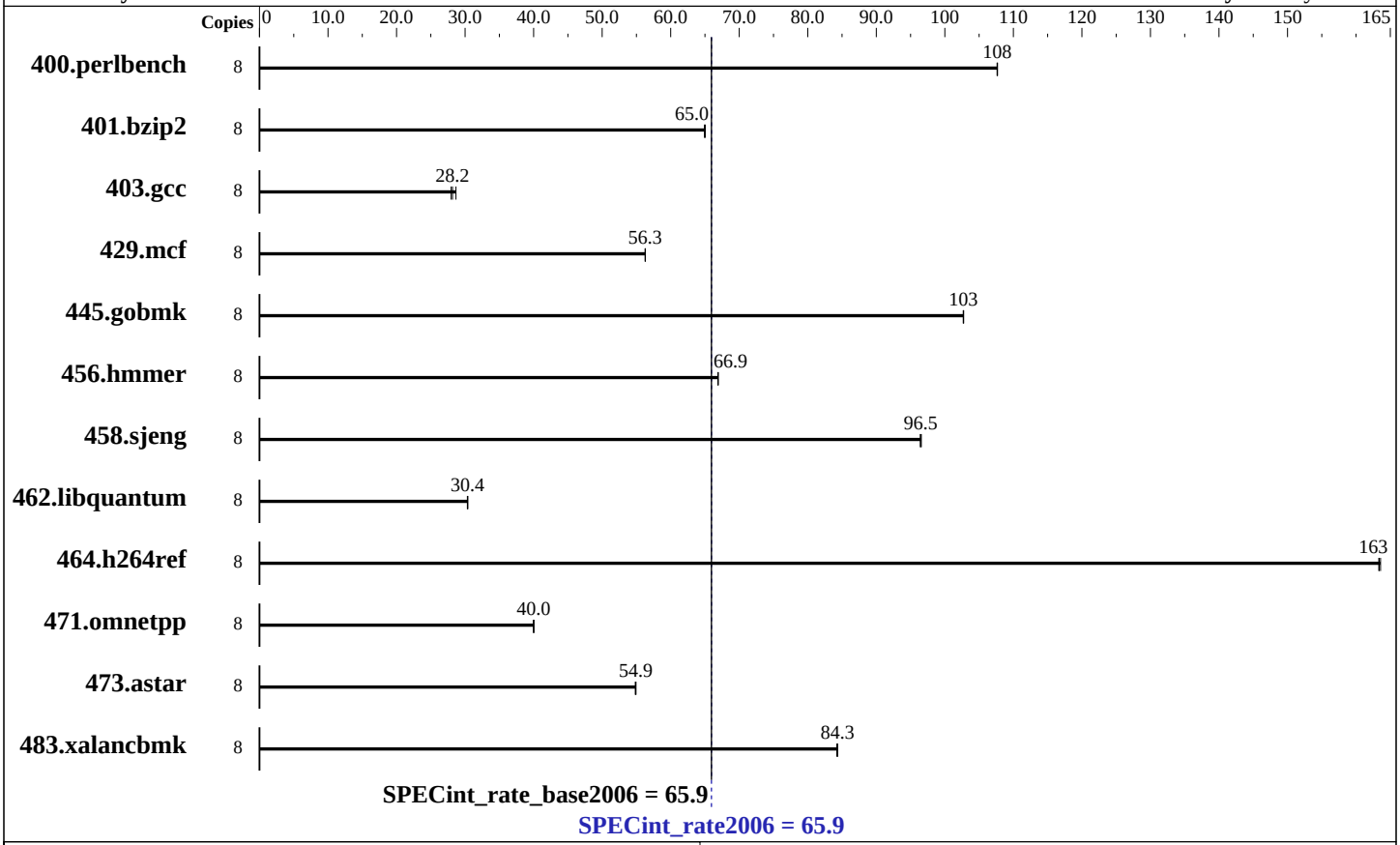
Servidor Itautec MX221 (Intel Xeon E5335)

SPECint®_rate2006 = 65.9

SPECint_rate_base2006 = 65.9

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon E5335
CPU Characteristics:	1333MHz system bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem:	73 GB SCSI, 10000RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	Microquill SmartHeap Library v.8.0 for SMP

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	726	108	**726**	**108**	726	108	8	726	108	**726**	**108**	726	108
401.bzip2	8	1189	64.9	**1188**	**65.0**	1187	65.0	8	1189	64.9	**1188**	**65.0**	1187	65.0
403.gcc	8	2246	28.7	**2287**	**28.2**	2303	28.0	8	2246	28.7	**2287**	**28.2**	2303	28.0
429.mcf	8	1296	56.3	**1296**	**56.3**	1296	56.3	8	1296	56.3	**1296**	**56.3**	1296	56.3
445.gobmk	8	817	103	**817**	**103**	817	103	8	817	103	**817**	**103**	817	103
456.hmmer	8	1115	66.9	1116	66.9	**1115**	**66.9**	8	1115	66.9	1116	66.9	**1115**	**66.9**
458.sjeng	8	1004	96.4	**1003**	**96.5**	1002	96.6	8	1004	96.4	**1003**	**96.5**	1002	96.6
462.libquantum	8	5456	30.4	5465	30.3	**5456**	**30.4**	8	5456	30.4	5465	30.3	**5456**	**30.4**
464.h264ref	8	**1084**	**163**	1082	164	1084	163	8	**1084**	**163**	1082	164	1084	163
471.omnetpp	8	**1249**	**40.0**	1251	40.0	1249	40.0	8	**1249**	**40.0**	1251	40.0	1249	40.0
473.astar	8	1022	54.9	**1023**	**54.9**	1024	54.8	8	1022	54.9	**1023**	**54.9**	1024	54.8
483.xalancbmk	8	654	84.3	**654**	**84.3**	655	84.2	8	654	84.3	**654**	**84.3**	655	84.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The Servidor Itautec MX221 and the Servidor Itautec MX201
are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlSMPMt.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlSMPMt.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:30:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 7 March 2007.