SPEC® CINT2006 Result

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Itautec

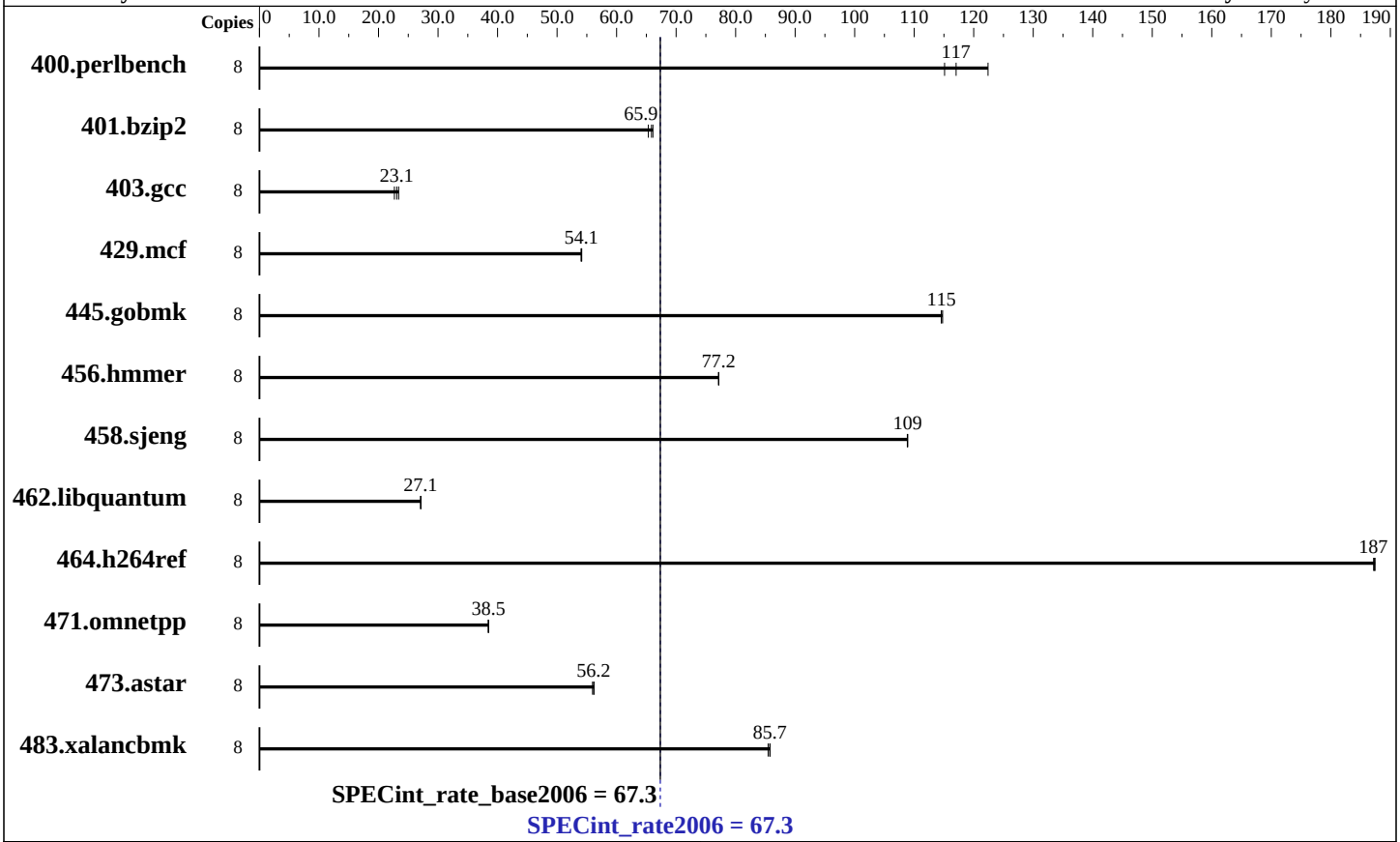
Servidor Itautec LX201 (Intel Xeon E5345)

SPECint®_rate2006 = 67.3

SPECint_rate_base2006 = 67.3

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon E5345
CPU Characteristics:	1333MHz system bus
CPU MHz:	2330
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem:	120 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	Microquill SmartHeap Library v.8.0 for SMP

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	639	122	679	115	**668**	**117**	8	639	122	679	115	**668**	**117**
401.bzip2	8	1182	65.3	**1172**	**65.9**	1168	66.1	8	1182	65.3	**1172**	**65.9**	1168	66.1
403.gcc	8	**2793**	**23.1**	2754	23.4	2841	22.7	8	**2793**	**23.1**	2754	23.4	2841	22.7
429.mcf	8	1350	54.1	1348	54.1	**1349**	**54.1**	8	1350	54.1	1348	54.1	**1349**	**54.1**
445.gobmk	8	732	115	731	115	**732**	**115**	8	732	115	731	115	**732**	**115**
456.hmmer	8	**967**	**77.2**	968	77.1	967	77.2	8	**967**	**77.2**	968	77.1	967	77.2
458.sjeng	8	889	109	**889**	**109**	889	109	8	889	109	**889**	**109**	889	109
462.libquantum	8	**6120**	**27.1**	6112	27.1	6121	27.1	8	**6120**	**27.1**	6112	27.1	6121	27.1
464.h264ref	8	**945**	**187**	945	187	946	187	8	**945**	**187**	945	187	946	187
471.omnetpp	8	**1300**	**38.5**	1299	38.5	1300	38.4	8	**1300**	**38.5**	1299	38.5	1300	38.4
473.astar	8	**1000**	**56.2**	999	56.2	1004	56.0	8	**1000**	**56.2**	999	56.2	1004	56.0
483.xalancbmk	8	646	85.5	643	85.8	**644**	**85.7**	8	646	85.5	643	85.8	**644**	**85.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlSMPMt.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlSMPMt.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Optimization Flags

C benchmarks:

400.perlbench: `basepeak = yes`

401.bzip2: `basepeak = yes`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

456.hmmer: `basepeak = yes`

458.sjeng: `basepeak = yes`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml`

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