SPEC® CINT2006 Result

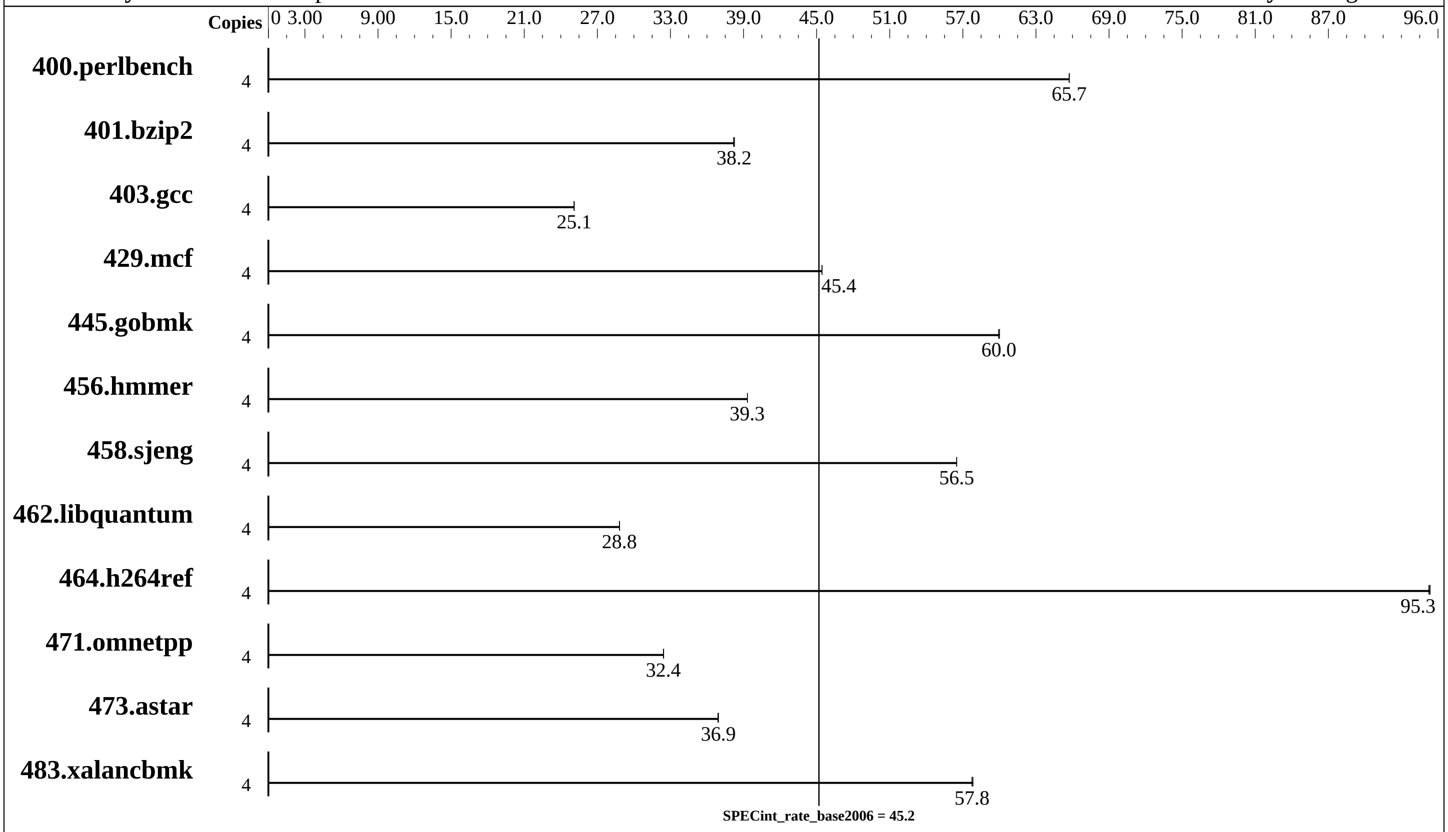
IBM Corporation

SPECint®_rate2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon 5140)

SPECint_rate_base2006 = 45.2

CPU2006 license:	11	**Test date:**	Jan-2007
Test sponsor:	IBM Corporation	**Hardware Availability:**	Feb-2007
Tested by:	IBM Corporation	**Software Availability:**	Aug-2006



Hardware

CPU Name:	Intel Xeon 5140
CPU Characteristics:	1333MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	4	594	65.7	**595**	**65.7**	595	65.7							
401.bzip2	4	**1010**	**38.2**	1011	38.2	1009	38.3							
403.gcc	4	1283	25.1	1284	25.1	**1283**	**25.1**							
429.mcf	4	**803**	**45.4**	803	45.4	803	45.4							
445.gobmk	4	**700**	**60.0**	700	59.9	699	60.0							
456.hmmer	4	949	39.3	**949**	**39.3**	949	39.3							
458.sjeng	4	857	56.5	857	56.5	**857**	**56.5**							
462.libquantum	4	2877	28.8	**2876**	**28.8**	2875	28.8							
464.h264ref	4	928	95.4	929	95.2	**929**	**95.3**							
471.omnetpp	4	770	32.4	**771**	**32.4**	771	32.4							
473.astar	4	**760**	**36.9**	761	36.9	760	36.9							
483.xalancbmk	4	477	57.8	**478**	**57.8**	478	57.8							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
           -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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