SPEC® CFP2006 Result

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IBM Corporation

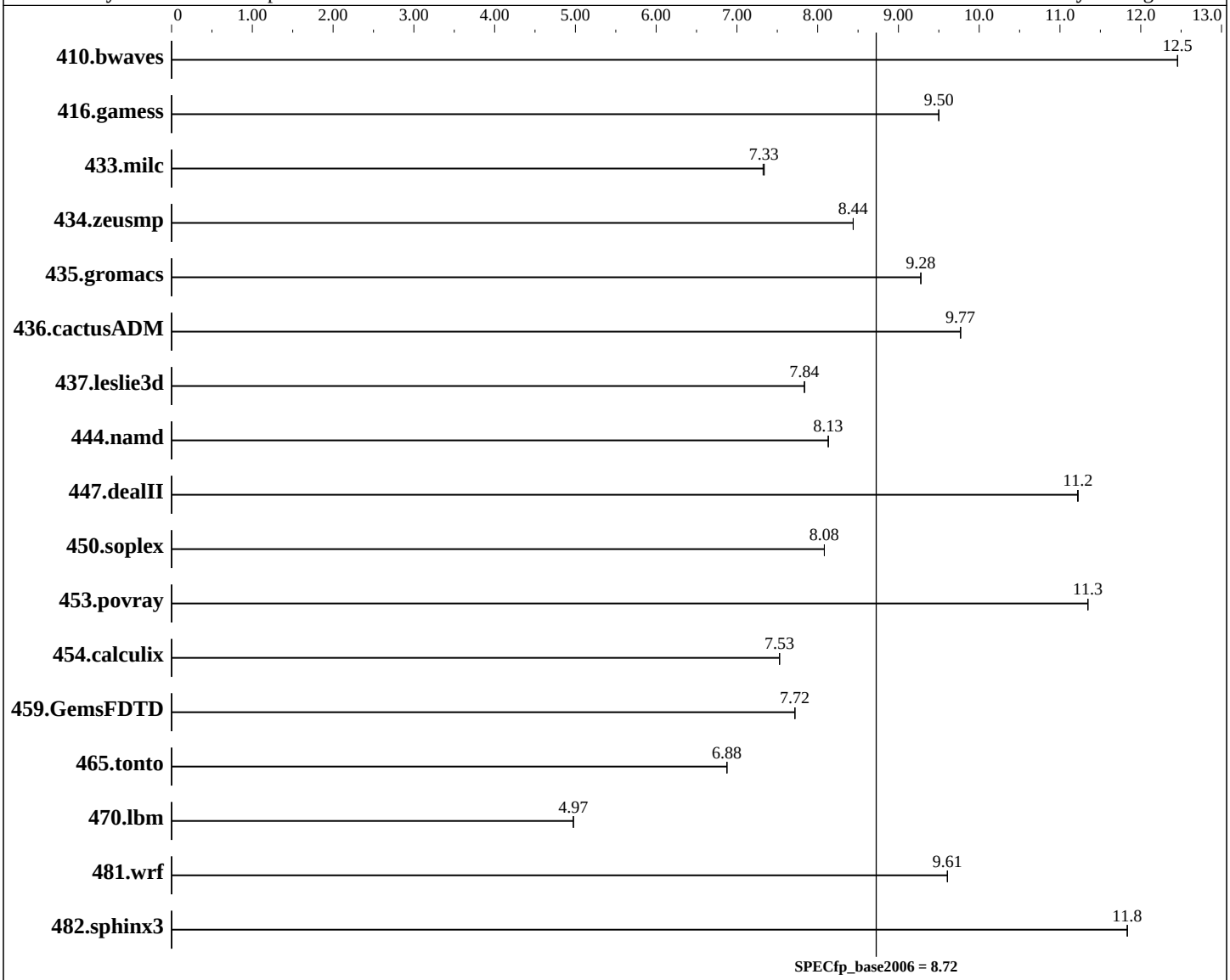
IBM BladeCenter HS21 XM (Intel Xeon E5310)

SPECfp®2006 = Not Run

SPECfp_base2006 = 8.72

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1066MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache:	None	System State:	Default
Other Cache:	None	Base Pointers:	32-bit
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)	Peak Pointers:	Not Applicable
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM	Other Software:	Smart Heap Library, Version 8
Other Hardware:	None		

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	1091	12.5	**1091**	**12.5**	1091	12.5						
416.gamess	2061	9.50	**2061**	**9.50**	2062	9.50						
433.milc	1251	7.34	**1252**	**7.33**	1253	7.32						
434.zeusmp	**1078**	**8.44**	1078	8.44	1078	8.44						
435.gromacs	770	9.28	**770**	**9.28**	770	9.28						
436.cactusADM	1223	9.77	1223	9.77	**1223**	**9.77**						
437.leslie3d	1200	7.84	1200	7.84	**1200**	**7.84**						
444.namd	**986**	**8.13**	986	8.13	986	8.13						
447.dealII	1019	11.2	**1019**	**11.2**	1020	11.2						
450.soplex	1032	8.08	1032	8.08	**1032**	**8.08**						
453.povray	469	11.3	**469**	**11.3**	469	11.3						
454.calculix	**1096**	**7.53**	1096	7.53	1096	7.53						
459.GemsFDTD	1375	7.72	1375	7.72	**1375**	**7.72**						
465.tonto	**1431**	**6.88**	1431	6.87	1431	6.88						
470.lbm	**2762**	**4.97**	2762	4.98	2762	4.97						
481.wrf	1163	9.61	1163	9.61	**1163**	**9.61**						
482.sphinx3	1647	11.8	1647	11.8	**1647**	**11.8**						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

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