SPEC® CFP2006 Result

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IBM Corporation

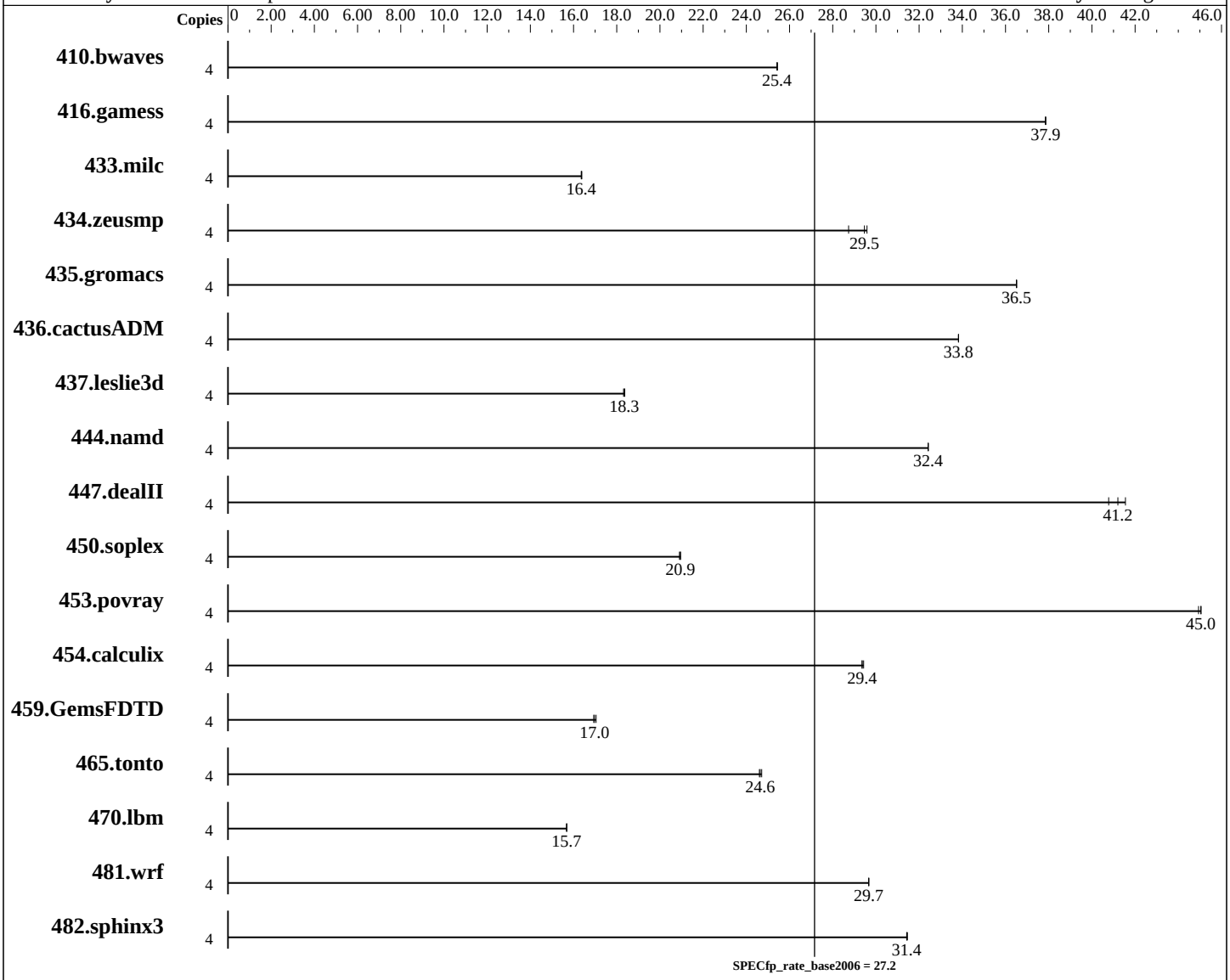
IBM BladeCenter HS21 XM (Intel Xeon 5110)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 27.2

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon 5110
CPU Characteristics:	1066MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM
Other Hardware:	None

System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	**2139**	**25.4**	2137	25.4	2139	25.4							
416.gamess	4	2069	37.9	2069	37.9	**2069**	**37.9**							
433.milc	4	2241	16.4	2245	16.4	**2244**	**16.4**							
434.zeusmp	4	1267	28.7	1230	29.6	**1235**	**29.5**							
435.gromacs	4	**782**	**36.5**	782	36.5	782	36.5							
436.cactusADM	4	**1413**	**33.8**	1413	33.8	1413	33.8							
437.leslie3d	4	2047	18.4	2053	18.3	**2049**	**18.3**							
444.namd	4	990	32.4	990	32.4	**990**	**32.4**							
447.dealII	4	1101	41.6	**1111**	**41.2**	1122	40.8							
450.soplex	4	1592	21.0	1596	20.9	**1593**	**20.9**							
453.povray	4	472	45.1	**472**	**45.0**	474	44.9							
454.calculix	4	1124	29.4	**1124**	**29.4**	1121	29.4							
459.GemsFDTD	4	**2500**	**17.0**	2490	17.0	2506	16.9							
465.tonto	4	1600	24.6	1593	24.7	**1597**	**24.6**							
470.lbm	4	3504	15.7	3505	15.7	**3505**	**15.7**							
481.wrf	4	1506	29.7	1506	29.7	**1506**	**29.7**							
482.sphinx3	4	**2481**	**31.4**	2481	31.4	2478	31.5							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

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