SPEC® CINT2006 Result

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IBM Corporation

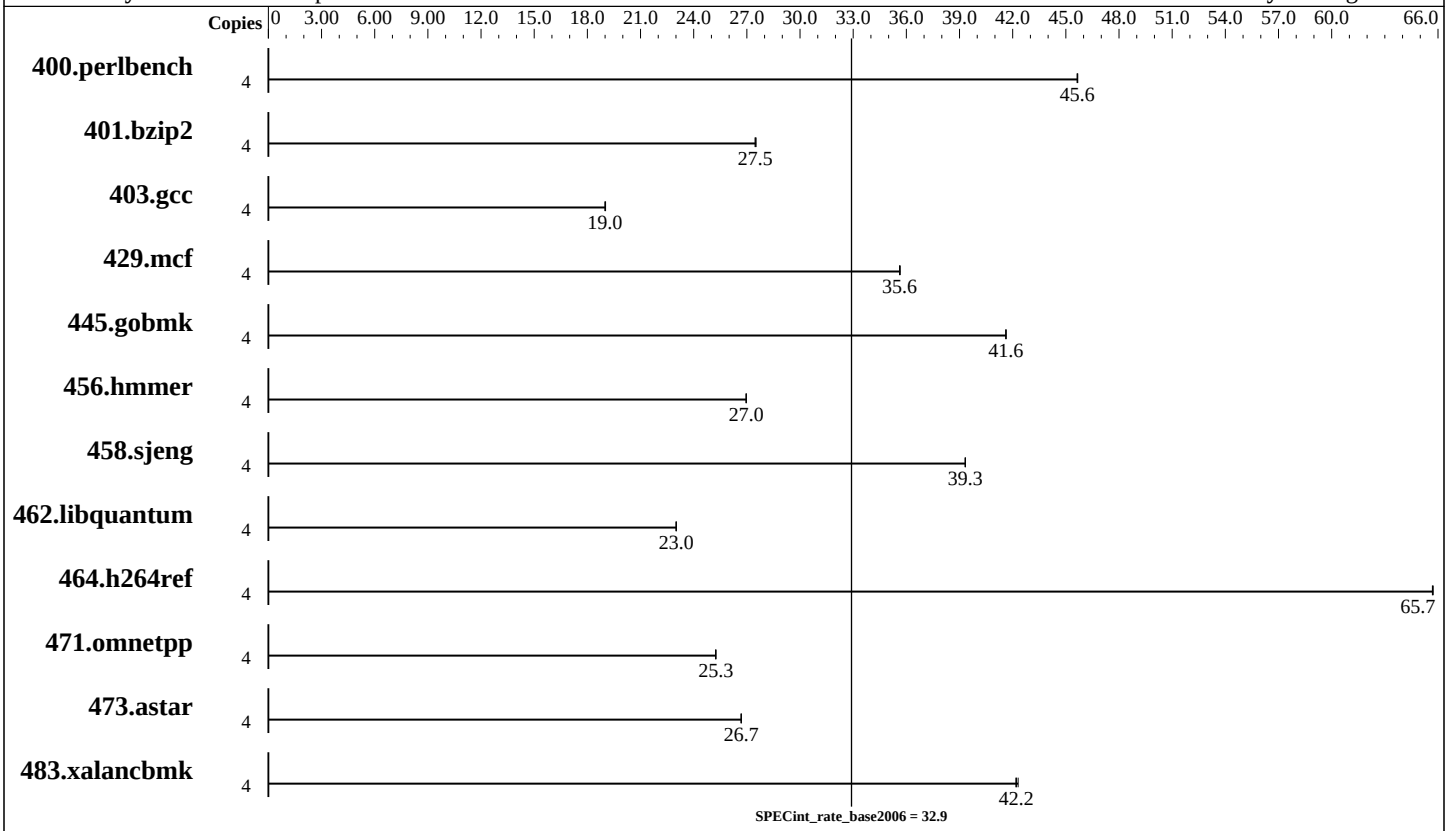
IBM BladeCenter HS21 XM (Intel Xeon 5110)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 32.9

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon 5110
CPU Characteristics:	1066MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	856	45.6	856	45.7	**856**	**45.6**							
401.bzip2	4	1405	27.5	**1405**	**27.5**	1403	27.5							
403.gcc	4	1692	19.0	**1695**	**19.0**	1695	19.0							
429.mcf	4	1024	35.6	1023	35.7	**1024**	**35.6**							
445.gobmk	4	1008	41.6	1008	41.6	**1008**	**41.6**							
456.hmmer	4	1384	27.0	1384	27.0	**1384**	**27.0**							
458.sjeng	4	1230	39.3	**1231**	**39.3**	1231	39.3							
462.libquantum	4	3602	23.0	3601	23.0	**3601**	**23.0**							
464.h264ref	4	1347	65.7	**1347**	**65.7**	1348	65.7							
471.omnetpp	4	991	25.2	990	25.3	**990**	**25.3**							
473.astar	4	1052	26.7	**1052**	**26.7**	1052	26.7							
483.xalancbmk	4	652	42.3	**654**	**42.2**	654	42.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml`

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For other inquiries, please contact webmaster@spec.org.

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