SPEC® CINT2006 Result

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Bull SAS

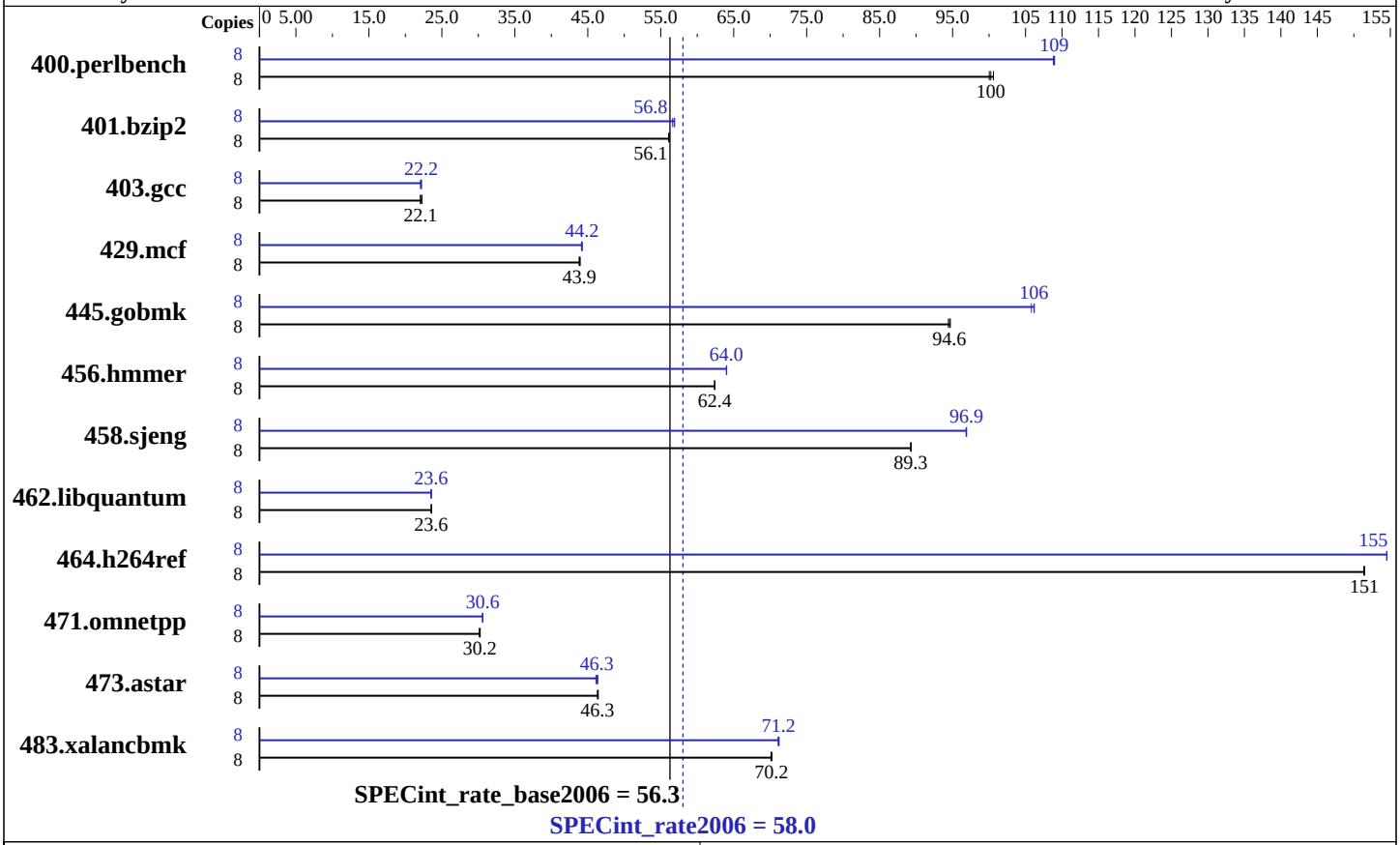
NovaScale B280 (Intel Xeon processor E5320,1.86GHz)

SPECint®_rate2006 = 58.0

SPECint_rate_base2006 = 56.3

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1.86 GHz, 8MB L2, 1066MHz bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (1GB DIMMx8, FB-DIMM PC2-5300F ECC CL5)
Disk Subsystem:	73 GB SAS, 10000RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	781	100	**780**	**100**	777	101	8	718	109	**718**	**109**	717	109
401.bzip2	8	1375	56.1	1376	56.1	**1376**	**56.1**	8	**1358**	**56.8**	1363	56.6	1357	56.9
403.gcc	8	2891	22.3	**2916**	**22.1**	2921	22.0	8	**2907**	**22.2**	2919	22.1	2898	22.2
429.mcf	8	1666	43.8	1660	44.0	**1663**	**43.9**	8	**1650**	**44.2**	1649	44.2	1653	44.1
445.gobmk	8	**887**	**94.6**	889	94.4	886	94.7	8	**790**	**106**	793	106	790	106
456.hmmer	8	1196	62.4	**1196**	**62.4**	1196	62.4	8	1166	64.0	1166	64.0	**1166**	**64.0**
458.sjeng	8	**1084**	**89.3**	1084	89.3	1085	89.3	8	**999**	**96.9**	999	96.9	1000	96.8
462.libquantum	8	7038	23.6	7039	23.5	**7038**	**23.6**	8	7059	23.5	7036	23.6	**7038**	**23.6**
464.h264ref	8	1169	151	**1169**	**151**	1170	151	8	**1146**	**155**	1146	155	1146	154
471.omnetpp	8	**1656**	**30.2**	1653	30.3	1660	30.1	8	**1634**	**30.6**	1633	30.6	1636	30.6
473.astar	8	**1212**	**46.3**	1209	46.4	1212	46.3	8	1211	46.4	**1214**	**46.3**	1218	46.1
483.xalancbmk	8	786	70.2	**787**	**70.2**	787	70.1	8	775	71.2	777	71.1	**776**	**71.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags.20090714.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags.20090714.00.xml`

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