SPEC® CINT2006 Result

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Hewlett-Packard Company

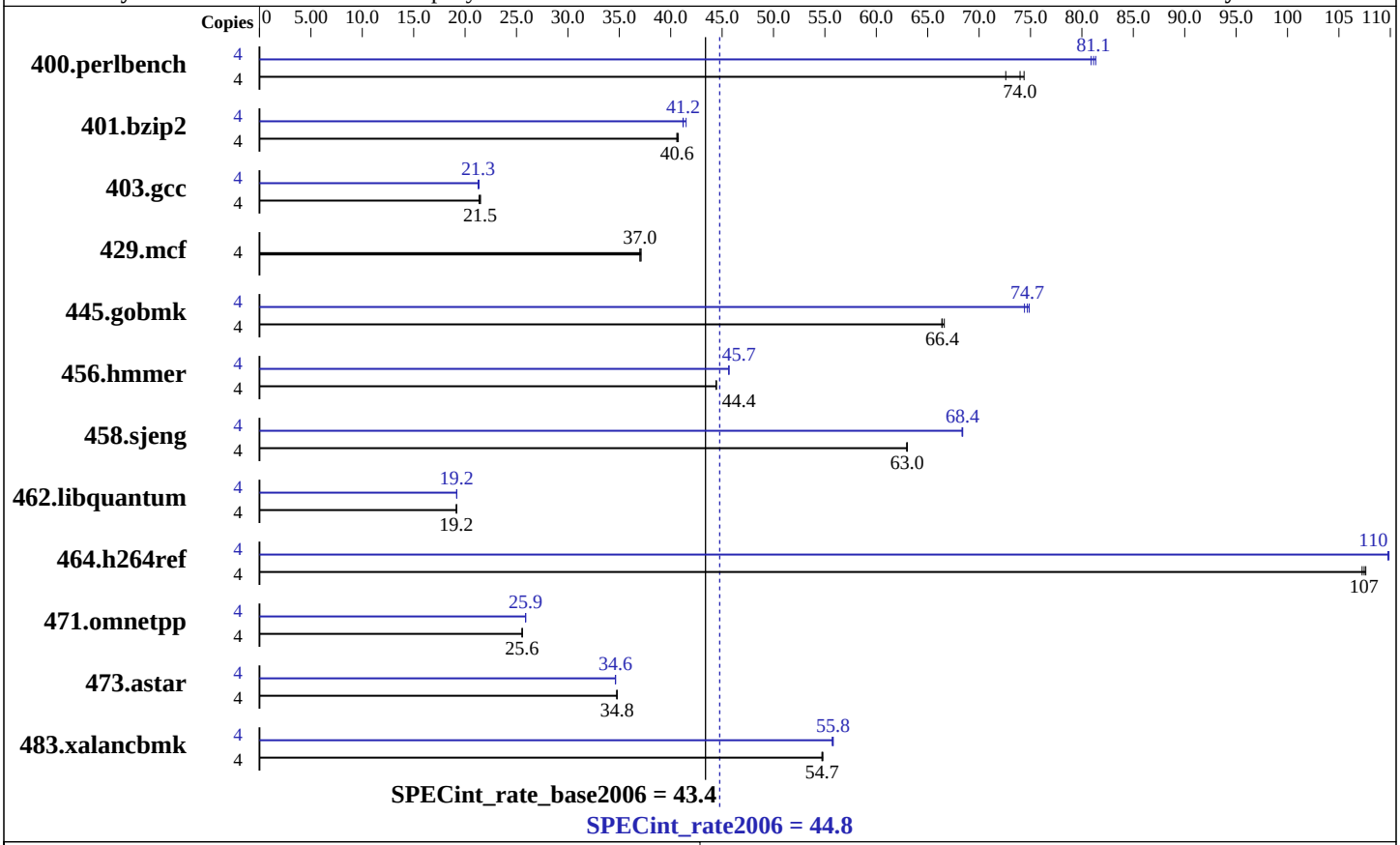
ProLiant BL20p G4
(2.66 GHz, Intel Xeon processor X5355)

SPECint®_rate2006 = 44.8

SPECint_rate_base2006 = 43.4

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 Mb L2 shared, 1333 MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300 CL5)
Disk Subsystem:	2x72 GB 10k SAS
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise X64 Edition
Compiler:	Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z Package ID: W_CC_P_9.1.020 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	538	72.6	**528**	**74.0**	525	74.4	4	480	81.3	483	80.9	**482**	**81.1**
401.bzip2	4	951	40.6	947	40.7	**950**	**40.6**	4	930	41.5	937	41.2	**936**	**41.2**
403.gcc	4	1498	21.5	**1500**	**21.5**	1508	21.4	4	1507	21.4	1514	21.3	**1513**	**21.3**
429.mcf	4	**985**	**37.0**	983	37.1	986	37.0	4	**985**	**37.0**	983	37.1	986	37.0
445.gobmk	4	**632**	**66.4**	630	66.6	632	66.4	4	**562**	**74.7**	560	74.9	564	74.4
456.hmmer	4	840	44.4	**840**	**44.4**	840	44.4	4	818	45.6	**817**	**45.7**	817	45.7
458.sjeng	4	769	63.0	**768**	**63.0**	768	63.0	4	708	68.3	708	68.4	**708**	**68.4**
462.libquantum	4	4325	19.2	4326	19.2	**4326**	**19.2**	4	4322	19.2	**4318**	**19.2**	4318	19.2
464.h264ref	4	825	107	823	108	**824**	**107**	4	806	110	**806**	**110**	806	110
471.omnetpp	4	**978**	**25.6**	978	25.6	979	25.5	4	966	25.9	**966**	**25.9**	966	25.9
473.astar	4	808	34.7	807	34.8	**807**	**34.8**	4	811	34.6	**811**	**34.6**	811	34.6
483.xalancbmk	4	504	54.7	**504**	**54.7**	504	54.8	4	495	55.7	495	55.8	**495**	**55.8**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib           -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

Continued on next page

Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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