



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 45.8

ProLiant ML370 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECint_rate_base2006 = 44.4

CPU2006 license: 3

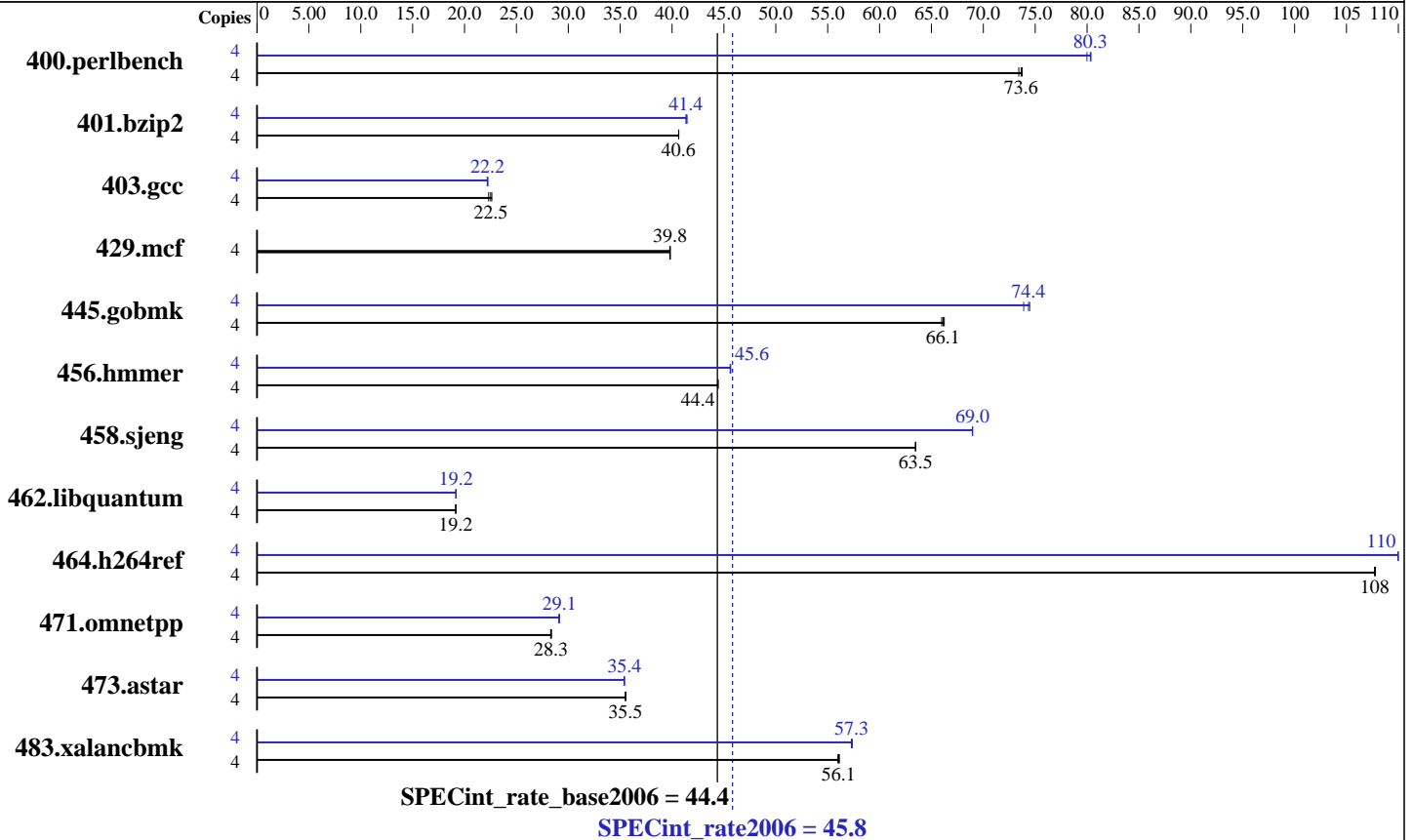
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66GHz, 4x2 MB L2 shared, 1333 MHz bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 2x72 GB 10 K SAS
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition
 Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z
 Package ID: W_CC_P_9.1.020
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	530	73.8	<u>531</u>	<u>73.6</u>	532	73.4	4	<u>487</u>	<u>80.3</u>	489	80.0	486	80.4
401.bzip2	4	950	40.6	950	40.6	<u>950</u>	<u>40.6</u>	4	933	41.4	<u>933</u>	<u>41.4</u>	931	41.4
403.gcc	4	1423	22.6	1442	22.3	<u>1430</u>	<u>22.5</u>	4	1451	22.2	<u>1448</u>	<u>22.2</u>	1448	22.2
429.mcf	4	<u>916</u>	<u>39.8</u>	916	39.8	917	39.8	4	<u>916</u>	<u>39.8</u>	916	39.8	917	39.8
445.gobmk	4	<u>635</u>	<u>66.1</u>	633	66.2	636	66.0	4	563	74.5	<u>564</u>	<u>74.4</u>	568	73.9
456.hammer	4	<u>840</u>	<u>44.4</u>	840	44.4	840	44.4	4	819	45.6	<u>818</u>	<u>45.6</u>	818	45.6
458.sjeng	4	<u>763</u>	<u>63.5</u>	763	63.5	763	63.5	4	702	68.9	702	69.0	<u>702</u>	<u>69.0</u>
462.libquantum	4	4326	19.2	4326	19.2	<u>4326</u>	<u>19.2</u>	4	<u>4323</u>	<u>19.2</u>	4323	19.2	4324	19.2
464.h264ref	4	821	108	<u>822</u>	<u>108</u>	822	108	4	805	110	<u>805</u>	<u>110</u>	805	110
471.omnetpp	4	881	28.4	883	28.3	<u>883</u>	<u>28.3</u>	4	858	29.1	<u>858</u>	<u>29.1</u>	859	29.1
473.astar	4	790	35.5	<u>791</u>	<u>35.5</u>	791	35.5	4	793	35.4	793	35.4	<u>793</u>	<u>35.4</u>
483.xalancbmk	4	<u>492</u>	<u>56.1</u>	493	56.0	492	56.1	4	481	57.4	482	57.3	<u>482</u>	<u>57.3</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

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