SPEC® CINT2006 Result

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IBM Corporation

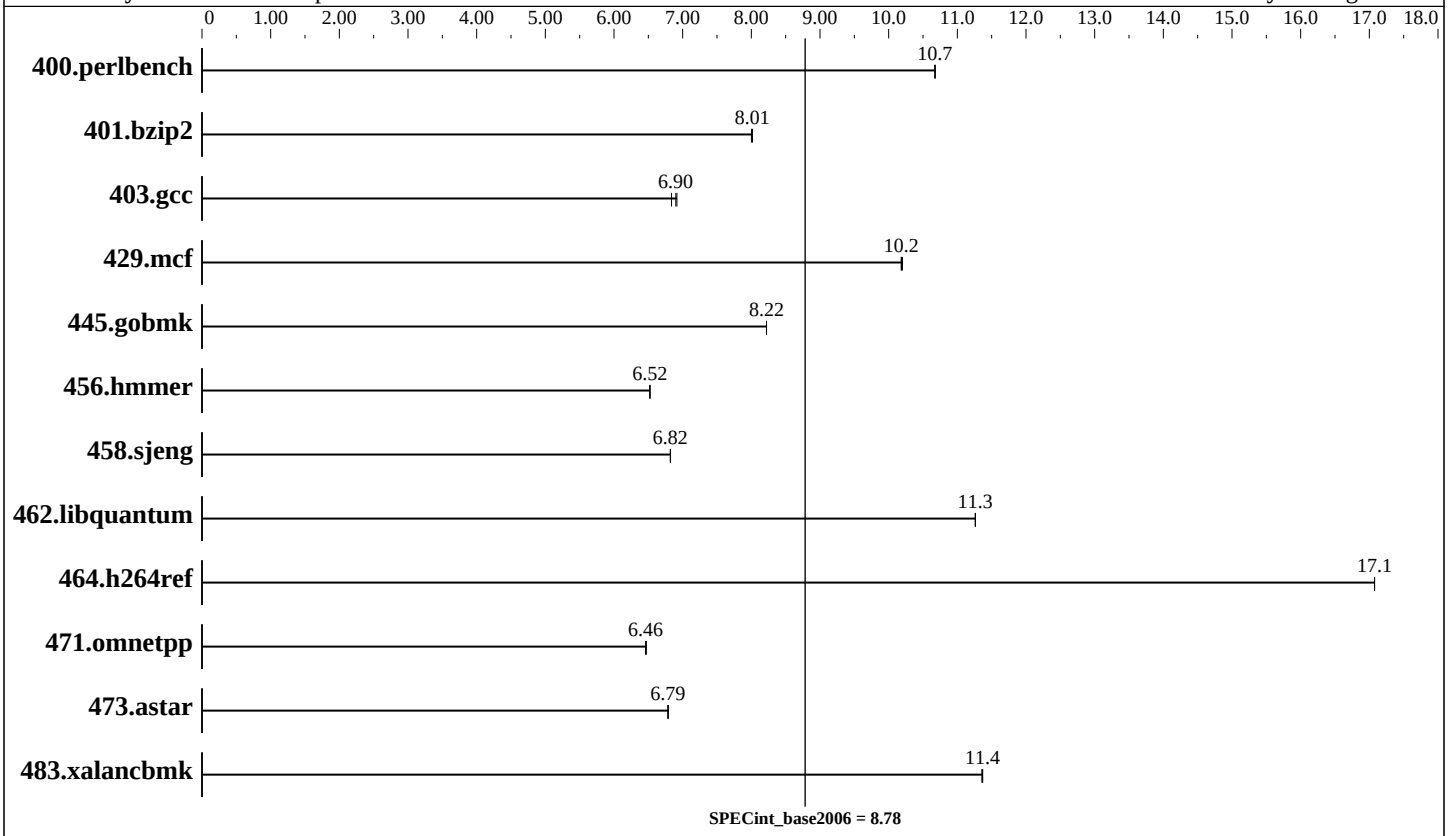
IBM System x3800 (Intel Xeon 7120N)

SPECint®2006 = Not Run

SPECint_base2006 = 8.78

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon 7120N
CPU Characteristics:	667 MHz bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16 x 2048 MB ECC PC2-3200)
Disk Subsystem:	73 GB SAS, 10k RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	915	10.7	**915**	**10.7**	915	10.7						
401.bzip2	1206	8.00	**1204**	**8.01**	1204	8.02						
403.gcc	**1167**	**6.90**	1164	6.92	1177	6.84						
429.mcf	**895**	**10.2**	894	10.2	895	10.2						
445.gobmk	**1276**	**8.22**	1276	8.22	1276	8.22						
456.hmmer	**1430**	**6.52**	1430	6.52	1430	6.52						
458.sjeng	1774	6.82	1774	6.82	**1774**	**6.82**						
462.libquantum	1840	11.3	**1839**	**11.3**	1839	11.3						
464.h264ref	1296	17.1	1296	17.1	**1296**	**17.1**						
471.omnetpp	967	6.46	966	6.47	**967**	**6.46**						
473.astar	**1034**	**6.79**	1035	6.78	1034	6.79						
483.xalancbmk	607	11.4	**607**	**11.4**	608	11.4						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios Settings
  Hardware Prefetch enabled
  Memory Array set to High-Performance Memory Arra2
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml`

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