SPEC® CINT2006 Result

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IBM Corporation

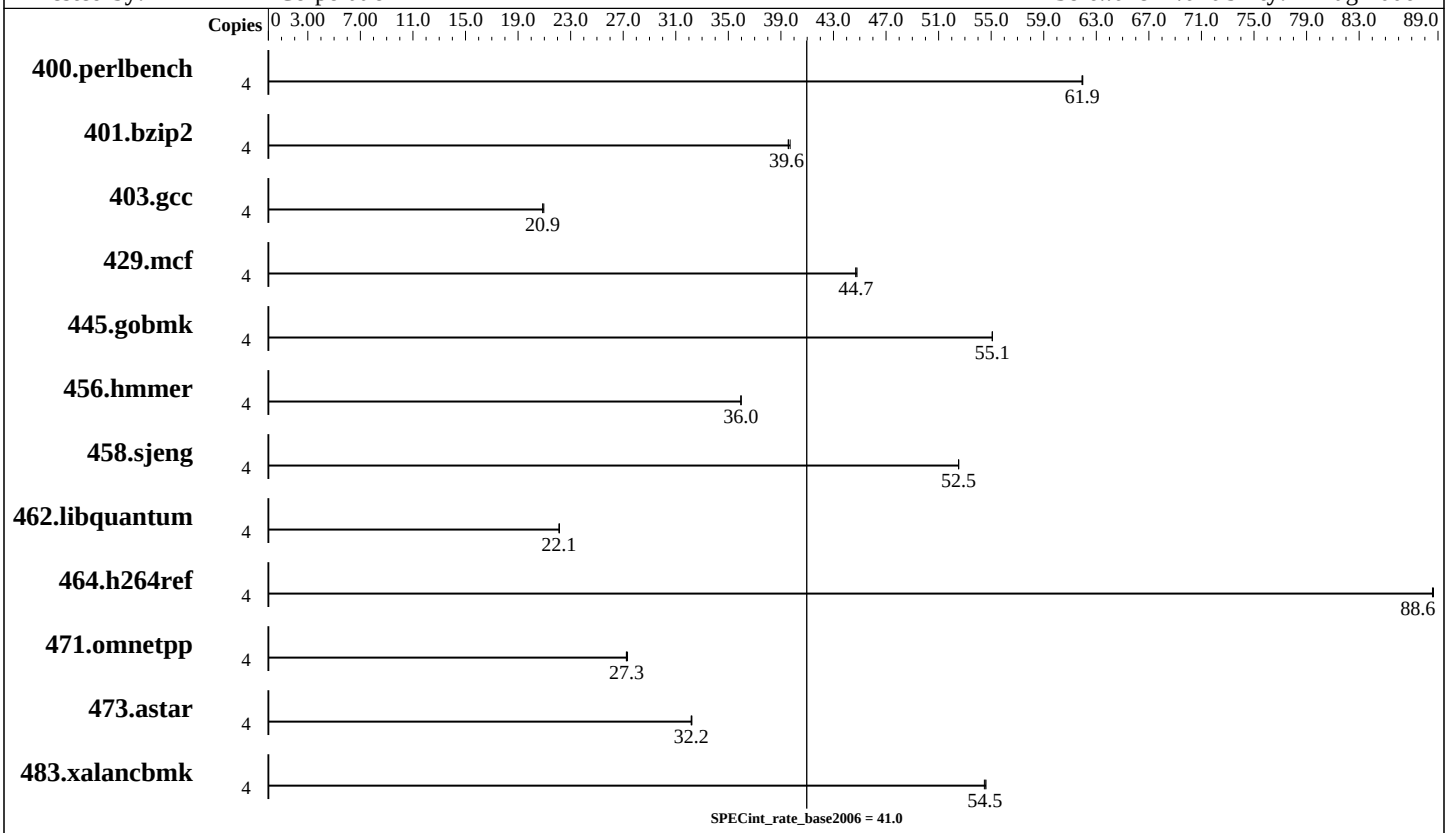
IBM System x3250 (Intel Xeon X3210)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 41.0

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon X3210
CPU Characteristics:	1066MHz system bus
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2GB PC2-5300 ECC)
Disk Subsystem:	1 x 73 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	631	61.9	**631**	**61.9**	631	62.0							
401.bzip2	4	976	39.6	**976**	**39.6**	972	39.7							
403.gcc	4	1536	21.0	1543	20.9	**1542**	**20.9**							
429.mcf	4	814	44.8	816	44.7	**816**	**44.7**							
445.gobmk	4	762	55.1	762	55.1	**762**	**55.1**							
456.hmmer	4	1038	36.0	1038	36.0	**1038**	**36.0**							
458.sjeng	4	921	52.5	**921**	**52.5**	922	52.5							
462.libquantum	4	**3746**	**22.1**	3747	22.1	3742	22.2							
464.h264ref	4	998	88.7	**999**	**88.6**	999	88.6							
471.omnetpp	4	915	27.3	**917**	**27.3**	918	27.2							
473.astar	4	873	32.2	871	32.2	**872**	**32.2**							
483.xalancbmk	4	505	54.6	507	54.5	**506**	**54.5**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml`

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