SPEC® CINT2006 Result

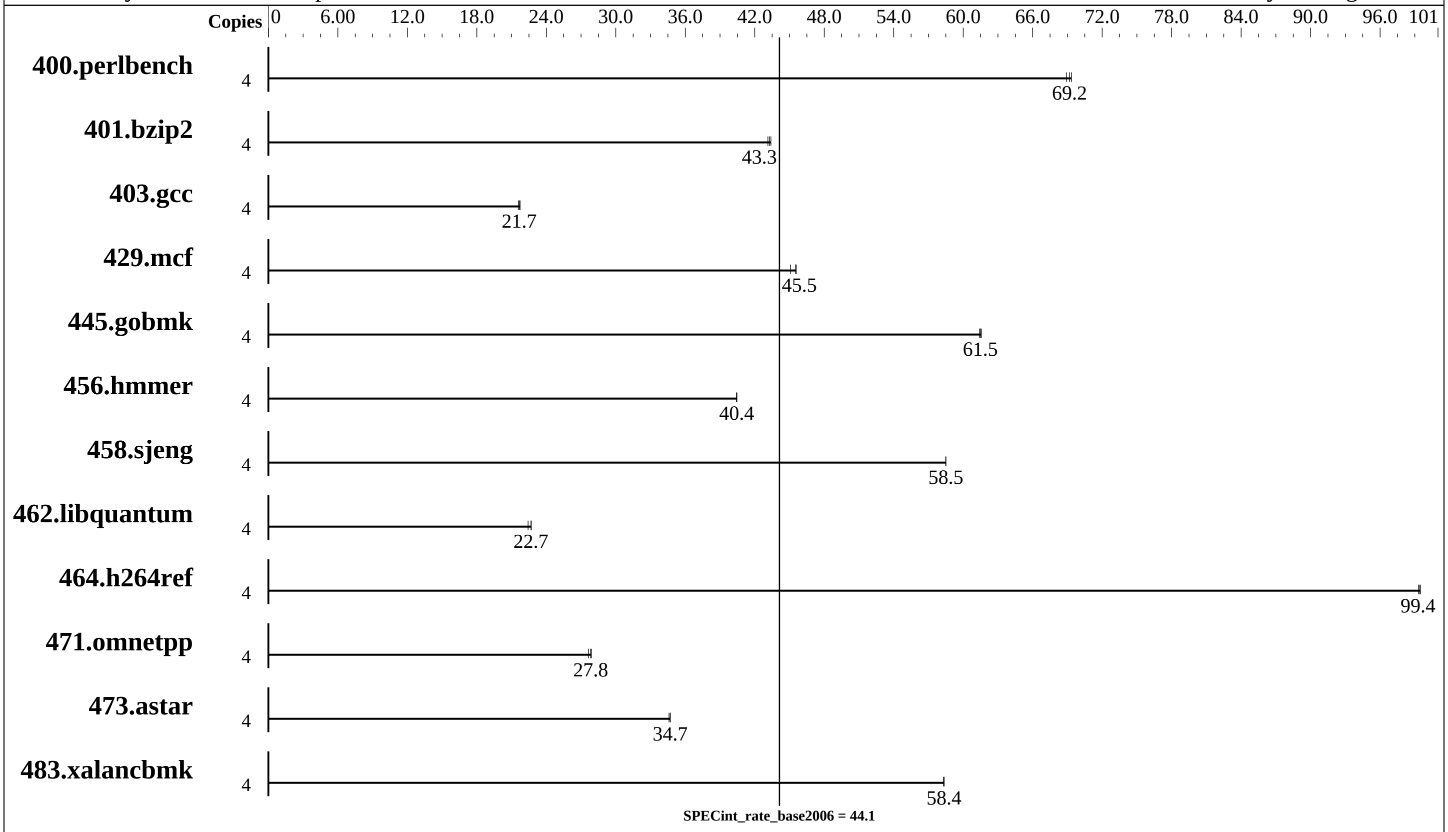
IBM Corporation

IBM System x3250 (Intel Xeon X3220)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 44.1

CPU2006 license:	11	**Test date:**	Jan-2007
Test sponsor:	IBM Corporation	**Hardware Availability:**	Feb-2007
Tested by:	IBM Corporation	**Software Availability:**	Aug-2006



Hardware

CPU Name:	Intel Xeon X3220
CPU Characteristics:	1066MHz system bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4 x 2GB PC2-5300 ECC)
Disk Subsystem:	1 x 73 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	4	567	68.9	**565**	**69.2**	564	69.3							
401.bzip2	4	895	43.1	890	43.4	**892**	**43.3**							
403.gcc	4	1492	21.6	1481	21.7	**1487**	**21.7**							
429.mcf	4	809	45.1	**801**	**45.5**	800	45.6							
445.gobmk	4	**682**	**61.5**	683	61.4	682	61.6							
456.hmmer	4	922	40.5	**923**	**40.4**	923	40.4							
458.sjeng	4	827	58.5	827	58.5	**827**	**58.5**							
462.libquantum	4	3696	22.4	3651	22.7	**3655**	**22.7**							
464.h264ref	4	891	99.3	**890**	**99.4**	890	99.5							
471.omnetpp	4	905	27.6	**898**	**27.8**	897	27.9							
473.astar	4	812	34.6	**809**	**34.7**	809	34.7							
483.xalancbmk	4	473	58.3	473	58.4	**473**	**58.4**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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