



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5160, 3.0 GHz

SPECint®_rate2006 = 55.2

SPECint_rate_base2006 = 52.2

CPU2006 license: 22

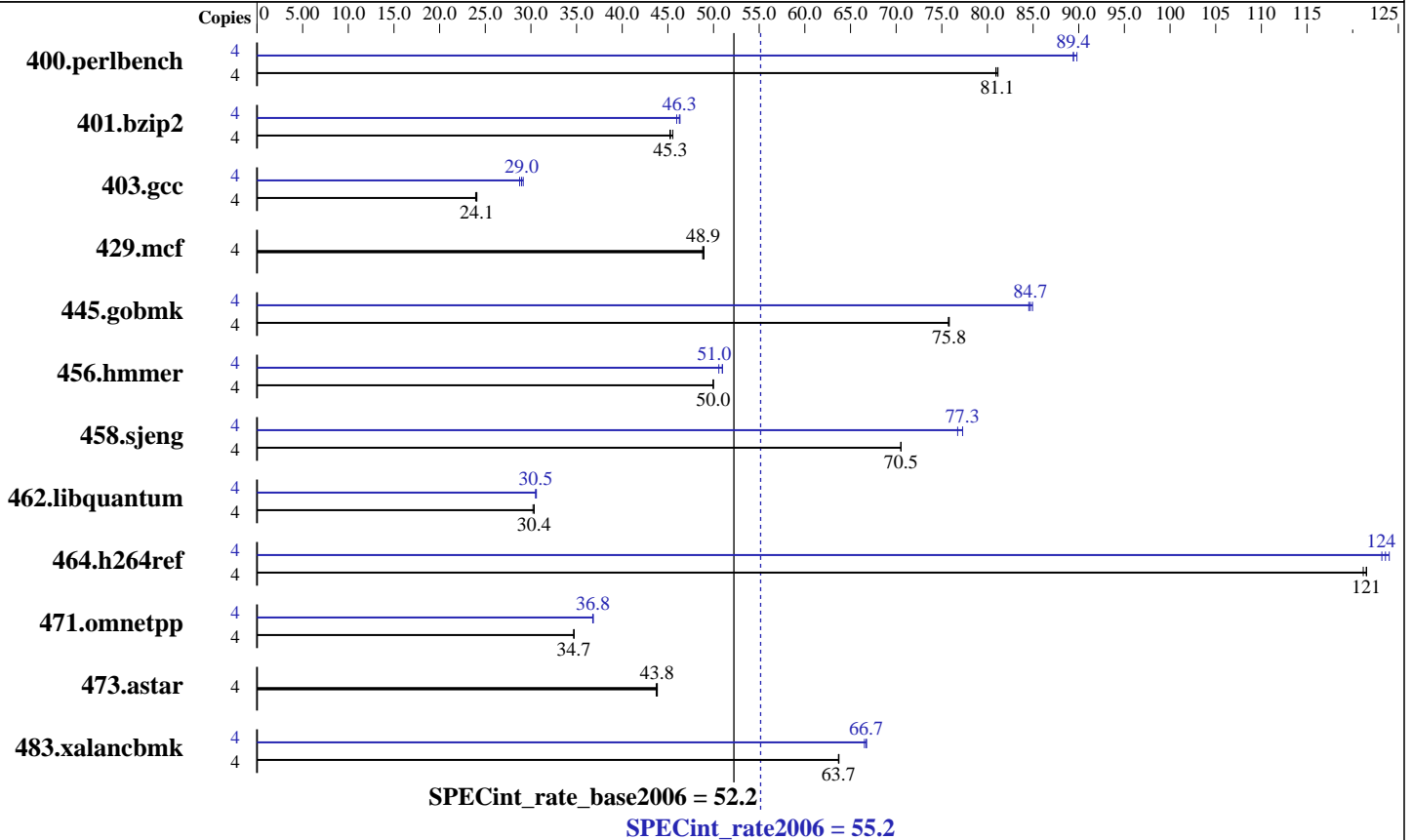
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: 5160
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: SAS (73GB 15400 rpm)
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 - Build 20061103Z, Package-ID W_CC_C_9.1.033 Microsoft Visual Studio 2005 (lib. & linker)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Smart Heap Library, Version 8



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5160, 3.0 GHz

SPECint_rate2006 = 55.2

SPECint_rate_base2006 = 52.2

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2006

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	483	80.9	482	81.1	482	81.2	4	435	89.8	437	89.4	437	89.4
401.bzip2	4	853	45.2	852	45.3	848	45.5	4	840	46.0	834	46.3	833	46.3
403.gcc	4	1337	24.1	1345	23.9	1338	24.1	4	1119	28.8	1104	29.2	1111	29.0
429.mcf	4	747	48.8	745	49.0	746	48.9	4	747	48.8	745	49.0	746	48.9
445.gobmk	4	554	75.8	554	75.7	554	75.8	4	495	84.7	496	84.5	494	84.9
456.hammer	4	747	50.0	747	50.0	747	50.0	4	732	51.0	732	51.0	738	50.6
458.sjeng	4	686	70.5	686	70.5	686	70.5	4	626	77.3	626	77.3	631	76.7
462.libquantum	4	2742	30.2	2730	30.4	2730	30.4	4	2716	30.5	2715	30.5	2709	30.6
464.h264ref	4	728	122	731	121	729	121	4	714	124	716	124	719	123
471.omnetpp	4	721	34.7	720	34.7	720	34.7	4	679	36.8	679	36.8	680	36.8
473.astar	4	642	43.7	641	43.8	640	43.8	4	642	43.7	641	43.8	640	43.8
483.xalancbmk	4	433	63.7	433	63.7	433	63.7	4	413	66.8	414	66.7	415	66.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

added /PAE switch to boot.ini

General Notes

The system bus runs at 1333 MHz

BIOS configuration:

Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see: <http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5160, 3.0 GHz

SPECint_rate2006 = 55.2

SPECint_rate_base2006 = 52.2

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2006

Base Portability Flags

400.perlbench: -DSPEC_CPU_NO_NEED_VA_COPY
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE
C++ benchmarks:
-fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99
C++ benchmarks:
icl -Qvc8

Peak Portability Flags

400.perlbench: -DSPEC_CPU_NO_NEED_VA_COPY
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
shlW32M.lib -link -FORCE:MULTIPLE

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5160, 3.0 GHz

SPECint_rate2006 = 55.2

SPECint_rate_base2006 = 52.2

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2006

Peak Optimization Flags (Continued)

401.bzip2: Same as 400.perlbench

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000
-link -FORCE:MULTIPLE

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F512000000 shlw32M.lib -link -FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.09.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.09.xml



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5160, 3.0 GHz

SPECint_rate2006 = 55.2

SPECint_rate_base2006 = 52.2

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2006

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:14:17 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 February 2007.