SPEC® CINT2006 Result

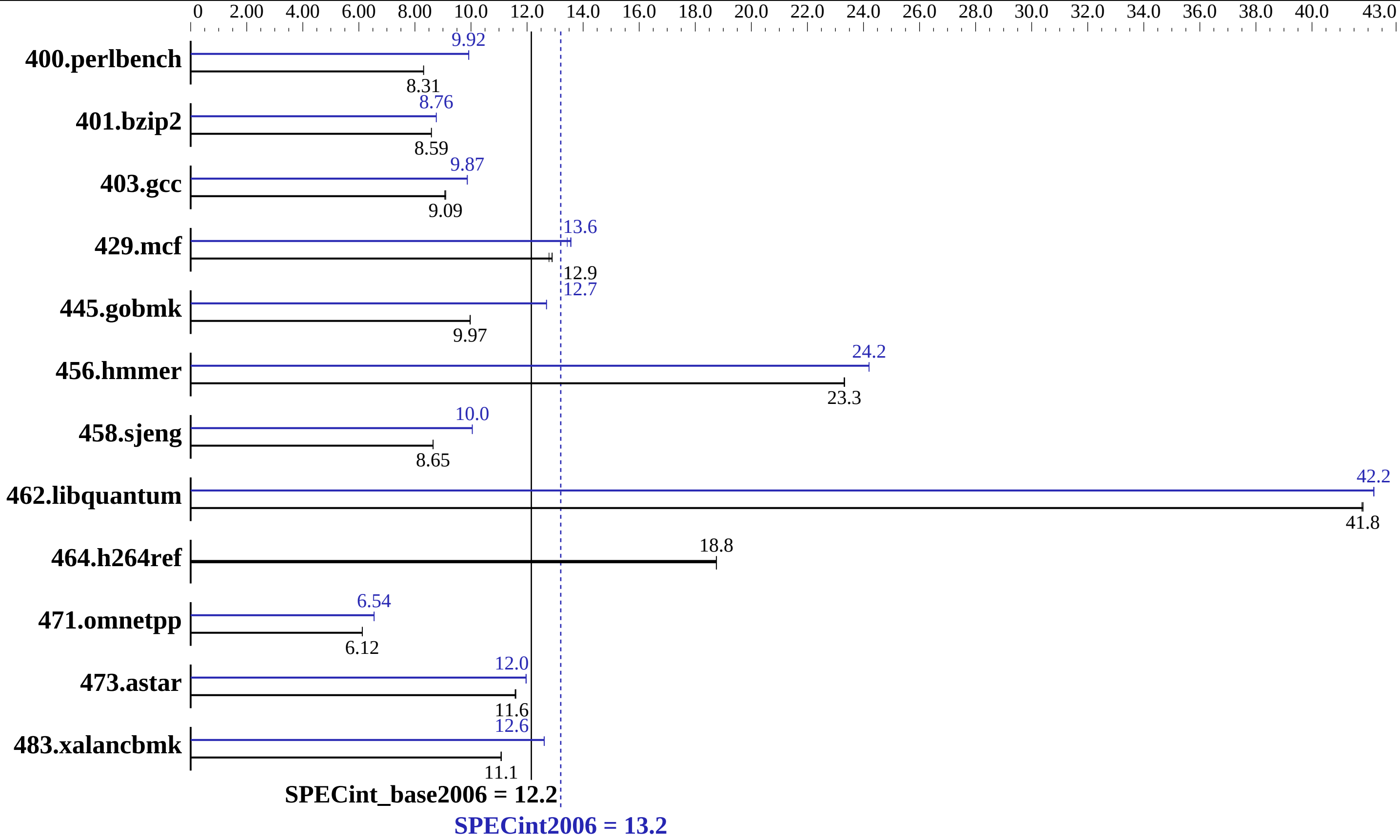
Hewlett-Packard Company

HP Integrity rx3600
(1.4GHz/12MB Dual-Core Intel Itanium 2)

SPECint®2006 = 13.2

SPECint_base2006 = 12.2

CPU2006 license:	03	**Test date:**	Jan-2007
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Sep-2006
Tested by:	Hewlett-Packard Company	**Software Availability:**	Sep-2006



Hardware

CPU Name:	Dual-Core Intel Itanium 2 9020
CPU Characteristics:	1.4GHz/12MB, 533MHz FSB
CPU MHz:	1400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	16 KB I + 16 KB D on chip per core
Secondary Cache:	1 MB I + 256 KB D on chip per core
L3 Cache:	6 MB I+D on chip per core
Other Cache:	None
Memory:	16 GB (8x2GB DIMMs, AD124A 8-DIMM memory carrier)
Disk Subsystem:	73GB 10K RPM SAS
Other Hardware:	None

Software

Operating System:	HPUX11i-TCOE B.11.23.0609
Compiler:	HP C/aC++ Developer's Bundle C.11.23.12
Auto Parallel:	No
File System:	vxfs
System State:	Multi-user
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill Smartheap 8.0

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Results Table

	Base						Peak					
Benchmark	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	1176	8.30	**1176**	**8.31**	1175	8.31	985	9.92	984	9.93	**985**	**9.92**
401.bzip2	**1124**	**8.59**	1124	8.58	1123	8.59	**1101**	**8.76**	1101	8.76	1102	8.76
403.gcc	889	9.06	885	9.10	**885**	**9.09**	815	9.87	816	9.86	**816**	**9.87**
429.mcf	713	12.8	707	12.9	**708**	**12.9**	**673**	**13.6**	672	13.6	679	13.4
445.gobmk	**1052**	**9.97**	1051	9.98	1053	9.96	826	12.7	**827**	**12.7**	827	12.7
456.hmmer	**400**	**23.3**	400	23.3	400	23.3	386	24.2	**385**	**24.2**	385	24.2
458.sjeng	1400	8.65	1398	8.65	**1399**	**8.65**	1205	10.0	1204	10.0	**1204**	**10.0**
462.libquantum	495	41.8	**496**	**41.8**	496	41.8	491	42.2	491	42.2	**491**	**42.2**
464.h264ref	1180	18.8	1180	18.8	**1180**	**18.8**	1180	18.8	1180	18.8	**1180**	**18.8**
471.omnetpp	1021	6.12	**1021**	**6.12**	1021	6.12	954	6.55	955	6.54	**955**	**6.54**
473.astar	**605**	**11.6**	605	11.6	607	11.6	587	12.0	586	12.0	**587**	**12.0**
483.xalancbmk	622	11.1	**623**	**11.1**	624	11.1	**547**	**12.6**	547	12.6	547	12.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
The system had the September 2006 HP-UX 11i v2 Technical Computing
Operating Environment (TCOE) and compilers installed, along with
the following patches:

PHSS_34858  linker + fdp cumulative patch
PHSS_34853  Math Library Cumulative Patch
PHSS_34854  Integrity Unwind Library
PHSS_34855  HP C Compiler (A.06.12)
PHSS_34856  aC++ Compiler (A.06.12)
PHSS_34857  u2comp/be/plugin library patch
PHSS_34395  FORTRAN I/O Library [libIO77]
PHSS_34397  FORTRAN Intrinsics [libF90 B.11.23.17]
PHSS_34399  Fortran Product Patch, v3.1 to v3.1.1
PHKL_34020  Perfmon enhancements and Itanium Dual-Core

The following kernel tunables were set, in addition to the defaults
set by the Technical Computing OE:

dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
maxssiz=401604608
```

Platform Notes

```
The "cpuconfig" EFI command was used prior to booting
  to deconfigure processors.

Although two cores were enabled during testing,
  the SPEC CPU2006 benchmarks used only one core.
```

Base Compiler Invocation

C benchmarks:
`/opt/ansic/bin/cc -Ae`

C++ benchmarks:
`/opt/aCC/bin/aCC -Aa`

Base Portability Flags

400.perlbench: -DSPEC_CPU_HPUX_IA64
403.gcc: -DSPEC_CPU_HPUX
462.libquantum: -DSPEC_CPU_HPUX
483.xalancbmk: -DSPEC_CPU_HPUX_IA64

Base Optimization Flags

C benchmarks:
```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
```

C++ benchmarks:
```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
/usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a
```

Peak Compiler Invocation

C benchmarks:
`/opt/ansic/bin/cc -Ae`

C++ benchmarks:
`/opt/aCC/bin/aCC -Aa`

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Peak Portability Flags

400.perlbench: -DSPEC_CPU_HPUX_IA64
403.gcc: -DSPEC_CPU_HPUX
462.libquantum: -DSPEC_CPU_HPUX
483.xalancbmk: -DSPEC_CPU_HPUX_IA64

Peak Optimization Flags

C benchmarks:

400.perlbench: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M -Wl,-N

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: Same as 400.perlbench

445.gobmk: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M +Odataprefetch=direct

456.hmmer: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M

458.sjeng: Same as 445.gobmk

462.libquantum: Same as 456.hmmer

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M /usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

473.astar: +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M +Onoparmsoverlap /usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

483.xalancbmk: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2) +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M -Wl,+pi,64M +Onoparmsoverlap /usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.07.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.07.xml

Tested with SPEC CPU2006 v1.0.
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