



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®\_rate2006 = 33.3

PowerEdge 840 (Intel Xeon X3220, 2.40 GHz)

SPECfp\_rate\_base2006 = 32.6

CPU2006 license: 55

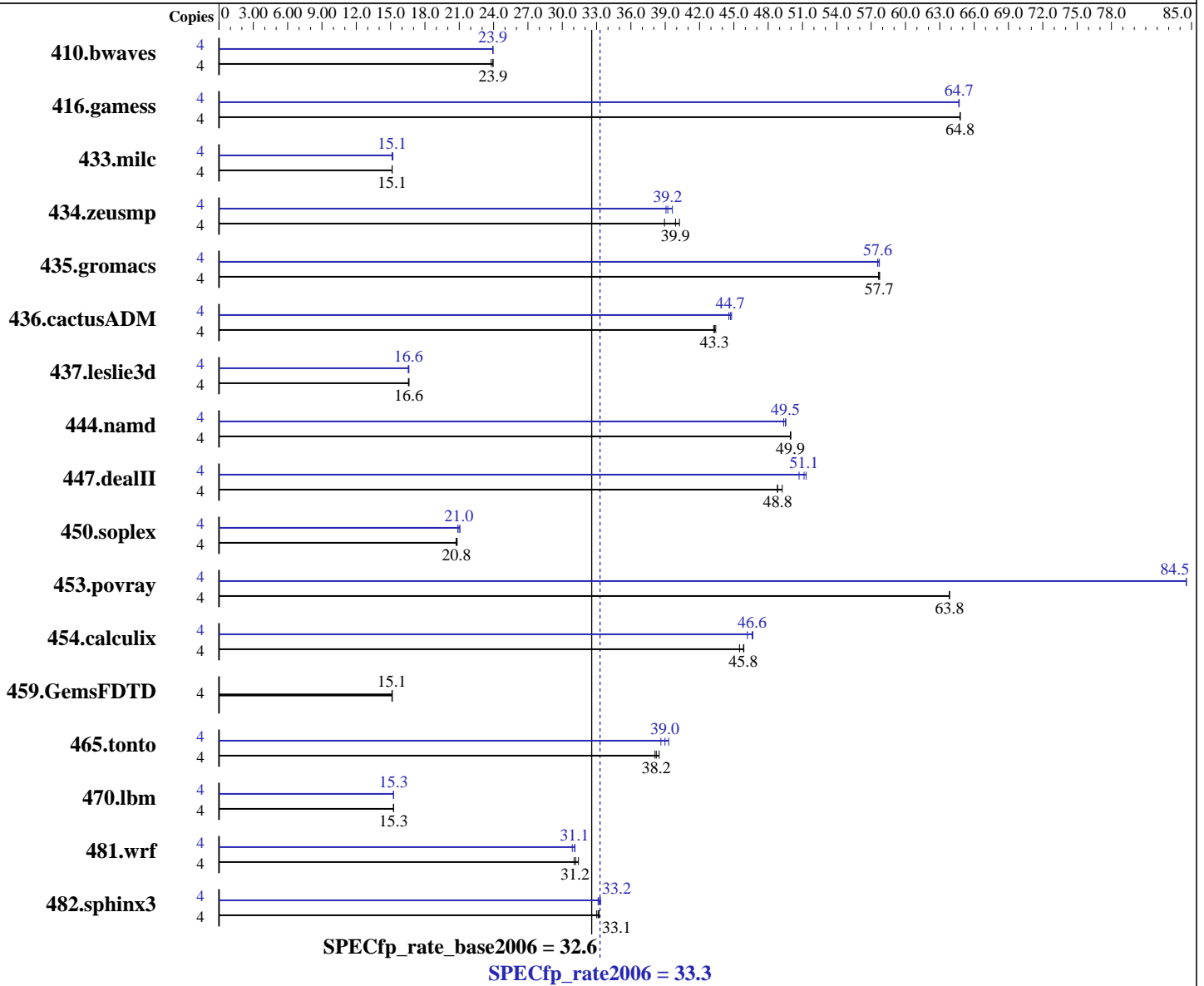
Test date: Jan-2007

Test sponsor: Dell Inc.

Hardware Availability: Jan-2007

Tested by: Dell Inc.

Software Availability: Oct-2006



### Hardware

CPU Name: Intel Xeon X3220  
 CPU Characteristics: 1066MHz system bus  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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### Software

Operating System: Windows Server 2003 Enterprise x64 Edition  
 Compiler: Intel C++ Compiler 9.1 for EM64T (20060925Z)  
 Intel FORTRAN Compiler 9.1 for EM64T (20060925Z)  
 Microsoft Visual Studio 2005 (8.0.50727.42)  
 MicroQuill SmartHeap Library 8.0 for x64  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 64-bit

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L3 Cache: None  
Other Cache: None  
Memory: 8 GB (4x2 GB 533MHz ECC DDR2 SDRAM)  
Disk Subsystem: 1 x 73GB SAS 10000 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	2285	23.8	2269	24.0	<u>2270</u>	<u>23.9</u>	4	<u>2270</u>	<u>23.9</u>	2272	23.9	2270	23.9		
416.gamess	4	<u>1209</u>	<u>64.8</u>	1209	64.8	1209	64.8	4	1211	64.7	1211	64.7	<u>1211</u>	<u>64.7</u>		
433.milc	4	2426	15.1	<u>2426</u>	<u>15.1</u>	2428	15.1	4	<u>2427</u>	<u>15.1</u>	2428	15.1	2416	15.2		
434.zeusmp	4	904	40.2	935	38.9	<u>912</u>	<u>39.9</u>	4	<u>928</u>	<u>39.2</u>	932	39.1	919	39.6		
435.gromacs	4	495	57.6	495	57.7	<u>495</u>	<u>57.7</u>	4	496	57.6	<u>496</u>	<u>57.6</u>	495	57.7		
436.cactusADM	4	1106	43.2	1101	43.4	<u>1104</u>	<u>43.3</u>	4	1073	44.6	1067	44.8	<u>1069</u>	<u>44.7</u>		
437.leslie3d	4	2264	16.6	<u>2267</u>	<u>16.6</u>	2271	16.6	4	2273	16.5	<u>2269</u>	<u>16.6</u>	2265	16.6		
444.namd	4	<u>642</u>	<u>49.9</u>	642	50.0	642	49.9	4	650	49.4	648	49.5	<u>648</u>	<u>49.5</u>		
447.dealII	4	938	48.8	<u>937</u>	<u>48.8</u>	930	49.2	4	892	51.3	903	50.7	<u>895</u>	<u>51.1</u>		
450.soplex	4	<u>1607</u>	<u>20.8</u>	1610	20.7	1603	20.8	4	1598	20.9	1584	21.1	<u>1590</u>	<u>21.0</u>		
453.povray	4	<u>333</u>	<u>63.8</u>	333	63.8	333	63.8	4	<u>252</u>	<u>84.5</u>	252	84.5	252	84.5		
454.calculix	4	<u>720</u>	<u>45.8</u>	719	45.9	725	45.5	4	707	46.7	<u>708</u>	<u>46.6</u>	714	46.2		
459.GemsFDTD	4	<u>2806</u>	<u>15.1</u>	2801	15.2	2808	15.1	4	<u>2806</u>	<u>15.1</u>	2801	15.2	2808	15.1		
465.tonto	4	1033	38.1	1023	38.5	<u>1030</u>	<u>38.2</u>	4	1019	38.6	1001	39.3	<u>1010</u>	<u>39.0</u>		
470.lbm	4	<u>3602</u>	<u>15.3</u>	3602	15.3	3601	15.3	4	<u>3602</u>	<u>15.3</u>	3605	15.2	3600	15.3		
481.wrf	4	1440	31.0	1422	31.4	<u>1433</u>	<u>31.2</u>	4	1447	30.9	1436	31.1	<u>1438</u>	<u>31.1</u>		
482.sphinx3	4	2361	33.0	<u>2353</u>	<u>33.1</u>	2347	33.2	4	2353	33.1	2337	33.4	<u>2346</u>	<u>33.2</u>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Adjacent Sector Prefetch disabled in BIOS, default enabled  
Binaries were built on Windows XP Professional x64 Edition

## Base Compiler Invocation

C benchmarks:  
icl -Qc99

C++ benchmarks:  
icl

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qc99 ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_P64  
 416.gamess: -DSPEC\_CPU\_P64  
 433.milc: -D\_Complex= -DSPEC\_CPU\_P64  
 434.zeusmp: -DSPEC\_CPU\_P64  
 435.gromacs: -D\_Complex= -DSPEC\_CPU\_P64  
 436.cactusADM: -D\_Complex= -DSPEC\_CPU\_P64 -Qlowercase /assume:underscore  
 437.leslie3d: -DSPEC\_CPU\_P64  
 444.namd: -DSPEC\_CPU\_P64 /TP  
 447.dealII: -D\_Complex= -DSPEC\_CPU\_P64 -DBOOST\_NO\_INTRINSIC\_WCHAR\_T  
 -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
 450.soplex: -DSPEC\_CPU\_P64  
 453.povray: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_WINDOWS\_ICL  
 454.calculix: -D\_Complex= -DSPEC\_CPU\_P64 -DSPEC\_CPU\_NOZMODIFIER  
 -Qlowercase  
 459.GemsFDTD: -DSPEC\_CPU\_P64  
 465.tonto: -DSPEC\_CPU\_P64  
 470.lbm: -D\_Complex= -DSPEC\_CPU\_P64  
 481.wrf: -DSPEC\_CPU\_P64 -DSPEC\_CPU\_WINDOWS\_ICL  
 482.sphinx3: -D\_Complex= -DSPEC\_CPU\_P64

## Base Optimization Flags

C benchmarks:

-fast /F950000000 shlw64M.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx\_features /F950000000 shlw64M.lib  
-link /FORCE:MULTIPLE

Fortran benchmarks:

-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

-fast /F950000000 -link /FORCE:MULTIPLE



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## Peak Compiler Invocation

C benchmarks:  
icl -Qc99

C++ benchmarks:  
icl

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icl -Qc99 ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:  
ONESTEP -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto\_ilp32  
/F950000000 shlW64M.lib -link /FORCE:MULTIPLE

C++ benchmarks:  
ONESTEP -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto\_ilp32  
-Qcxx\_features /F950000000 shlW64M.lib  
-link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: ONESTEP -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast  
-Qauto\_ilp32 /F950000000 -link /FORCE:MULTIPLE

416.gamess: ONESTEP -fast -Qauto\_ilp32 /F950000000  
-link /FORCE:MULTIPLE

434.zeusmp: Same as 410.bwaves

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: basepeak = yes

465.tonto: Same as 410.bwaves

Benchmarks using both Fortran and C:

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## Peak Optimization Flags (Continued)

435.gromacs: -fast -Qauto\_ilp32 /F950000000  
-link /FORCE:MULTIPLE

436.cactusADM: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qauto\_ilp32  
/F950000000 -link /FORCE:MULTIPLE

454.calculix: Same as 436.cactusADM

481.wrf: Same as 435.gromacs

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.x64.flags.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.x64.flags.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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