



SPEC® CINT2006 Result

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Bull SAS

SPECint®2006 = 10.2

NovaScale B260 (Intel Xeon processor 5110,1.60GHz)

SPECint_base2006 = 9.84

CPU2006 license: 20

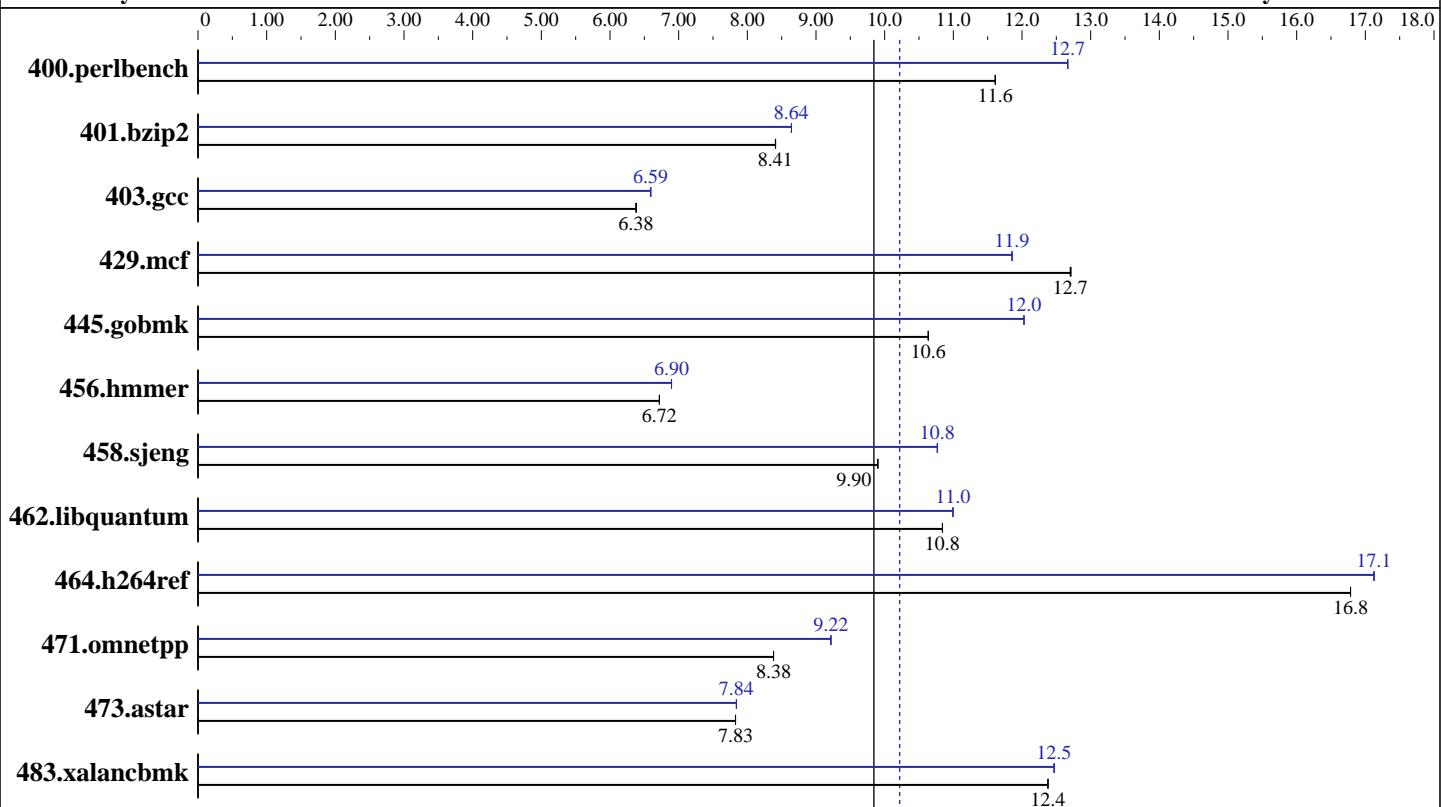
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Dec-2006

Hardware Availability: Dec-2006

Software Availability: Dec-2006



Hardware

CPU Name: Intel Xeon 5110
CPU Characteristics: 1.60 GHz, 4MB L2, 1066MHz bus
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 2 cores/chip
CPU(s) orderable: 1 to 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (2GB DIMMx4, FB-DIMM PC2-5300F ECC CL5)
Disk Subsystem: 73 GB SAS, 10000RPM
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition (32 bits)
Service Pack1
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.033 Build no 20061103Z
Microsoft Visual Studio .NET 2003 (lib & linker)
MicroQuill SmartHeap Library 8.0 (shlw32M.lib)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: None



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	841	11.6	841	11.6	842	11.6	771	12.7	771	12.7	771	12.7
401.bzip2	1147	8.41	1148	8.41	1148	8.41	1117	8.64	1117	8.64	1117	8.64
403.gcc	1261	6.39	1262	6.38	1262	6.38	1222	6.59	1221	6.59	1220	6.60
429.mcf	717	12.7	718	12.7	718	12.7	769	11.9	769	11.9	769	11.9
445.gobmk	986	10.6	986	10.6	986	10.6	872	12.0	872	12.0	872	12.0
456.hmmer	1389	6.72	1389	6.72	1389	6.72	1353	6.90	1353	6.90	1353	6.90
458.sjeng	1222	9.90	1222	9.90	1222	9.90	1124	10.8	1124	10.8	1124	10.8
462.libquantum	1911	10.8	1911	10.8	1911	10.8	1885	11.0	1885	11.0	1884	11.0
464.h264ref	1319	16.8	1318	16.8	1318	16.8	1292	17.1	1292	17.1	1292	17.1
471.omnetpp	746	8.38	746	8.38	746	8.38	678	9.22	678	9.22	678	9.22
473.astar	897	7.83	897	7.83	897	7.83	895	7.84	895	7.84	895	7.84
483.xalancbmk	557	12.4	557	12.4	557	12.4	553	12.5	553	12.5	554	12.5

Results appear in the order in which they were run. **Bold underlined** text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INNTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib

-link /FORCE:MULTIPLE

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib  
        -link /FORCE:MULTIPLE
```



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags.20090715.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags.20090715.xml>



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Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 10:11:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 25 January 2007.