



# SPEC<sup>®</sup> CINT2006 Result

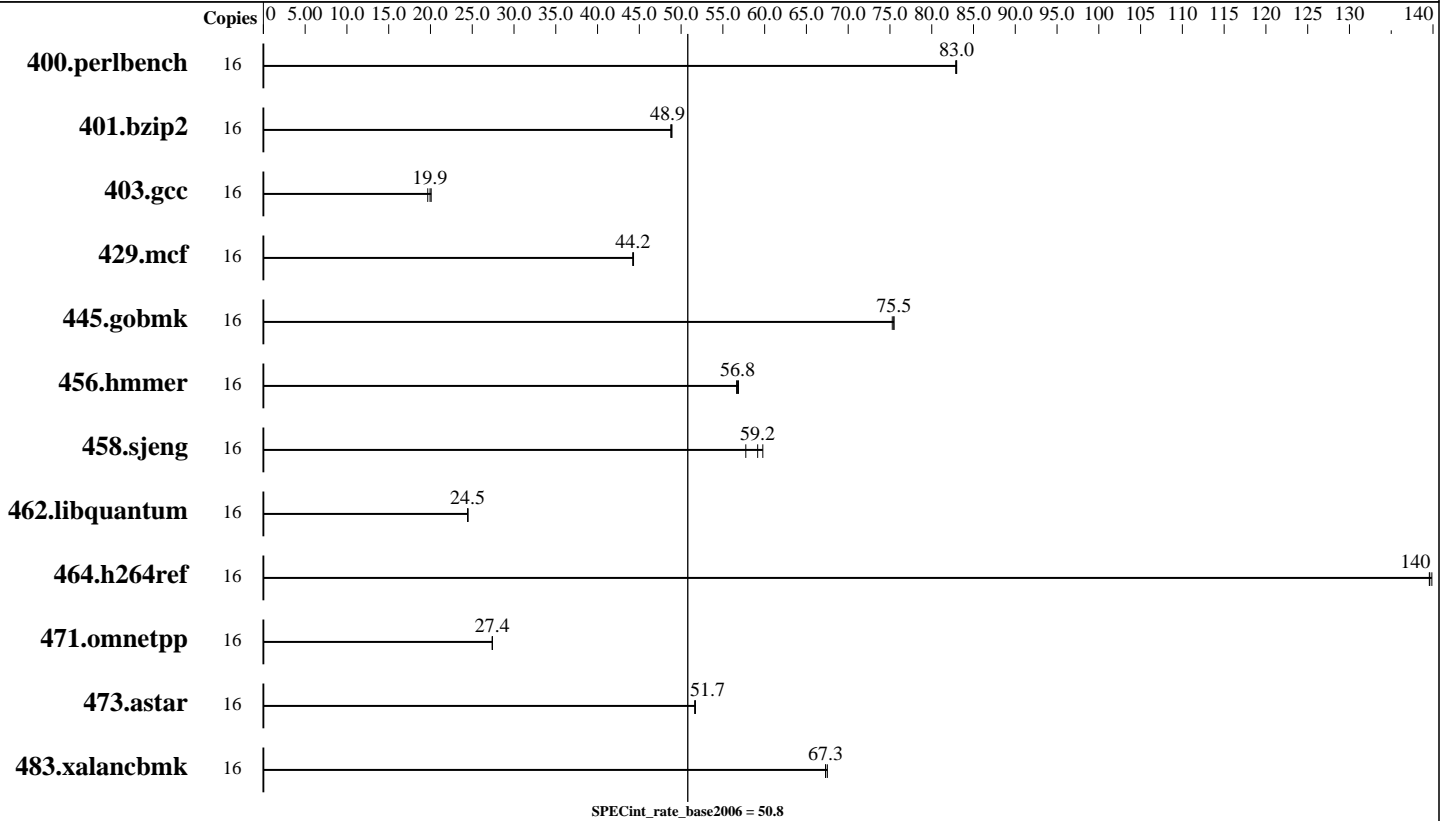
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IBM Corporation  
IBM System x3950

SPECint<sup>®</sup>\_rate2006 = Not Run  
SPECint\_rate\_base2006 = 50.8

CPU2006 license: 11  
Test sponsor: IBM Corporation  
Tested by: IBM Corporation

Test date: Dec-2006  
Hardware Availability: Oct-2006  
Software Availability: Aug-2006



### Hardware

CPU Name: Intel Xeon 7120N  
 CPU Characteristics: 667 MHz bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,4 chips  
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 32 GB (16 x 2048 MB ECC PC2-3200)  
 Disk Subsystem: 73 GB SAS, 10k RPM  
 Other Hardware: None

### Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20060816  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1886	82.9	<b>1884</b>	<b>83.0</b>	1884	83.0							
401.bzip2	16	3157	48.9	3166	48.8	<b>3160</b>	<b>48.9</b>							
403.gcc	16	6413	20.1	<b>6456</b>	<b>19.9</b>	6547	19.7							
429.mcf	16	3293	44.3	3300	44.2	<b>3298</b>	<b>44.2</b>							
445.gobmk	16	2230	75.3	2224	75.5	<b>2224</b>	<b>75.5</b>							
456.hammer	16	<b>2630</b>	<b>56.8</b>	2625	56.9	2636	56.6							
458.sjeng	16	3238	59.8	3353	57.7	<b>3272</b>	<b>59.2</b>							
462.libquantum	16	13535	24.5	<b>13556</b>	<b>24.5</b>	13558	24.5							
464.h264ref	16	2532	140	<b>2536</b>	<b>140</b>	2537	140							
471.omnetpp	16	<b>3647</b>	<b>27.4</b>	3647	27.4	3652	27.4							
473.astar	16	2172	51.7	2175	51.6	<b>2174</b>	<b>51.7</b>							
483.xalancbmk	16	1641	67.3	<b>1640</b>	<b>67.3</b>	1635	67.5							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios Settings  
Hardware Prefetch enabled  
Memory Array set to High-Performance Memory Array

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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