# SPEC® CINT2006 Result

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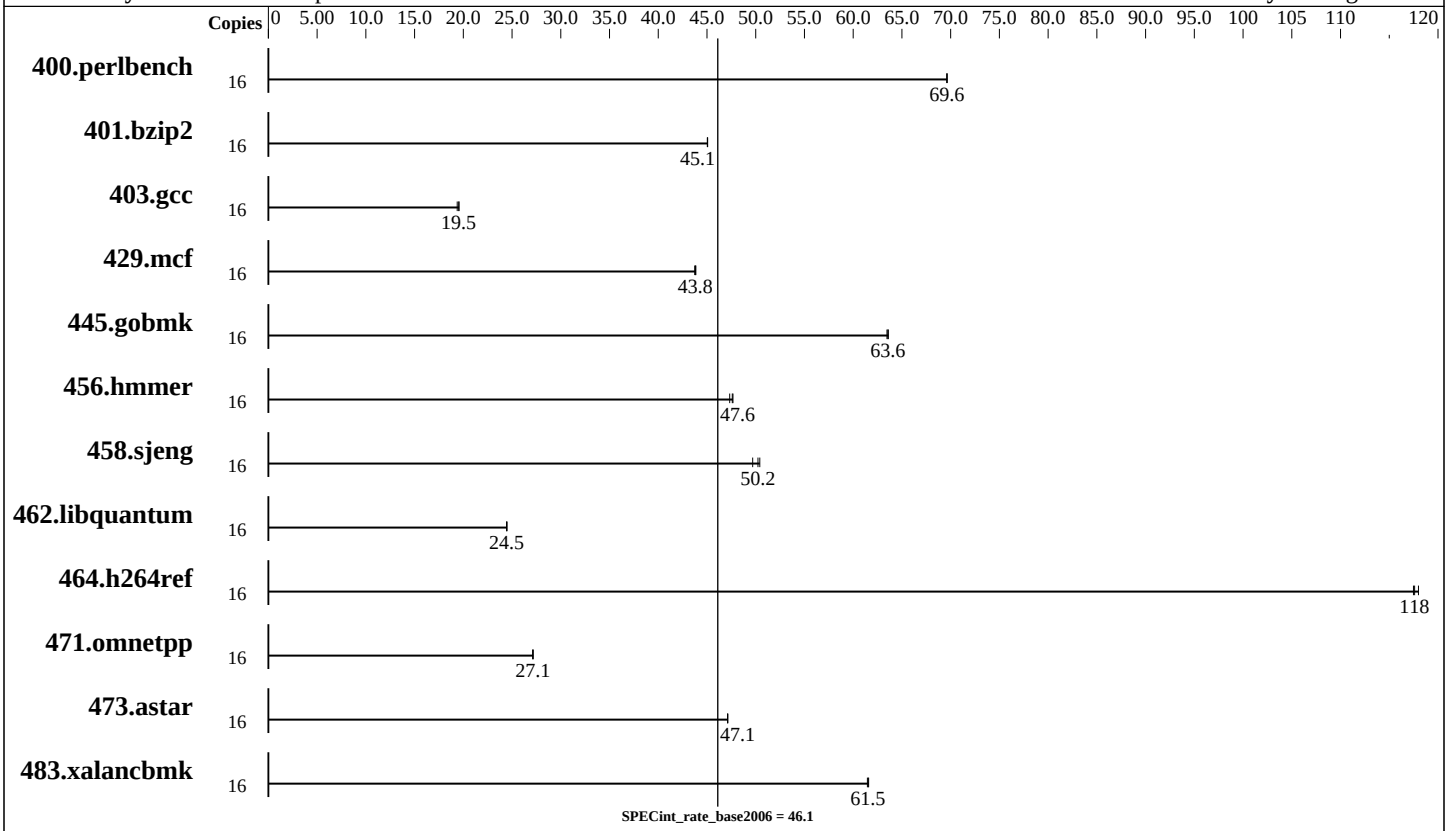
## IBM Corporation
## IBM System x3950

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 46.1

**CPU2006 license:** 11
**Test sponsor:** IBM Corporation
**Tested by:** IBM Corporation

**Test date:** Dec-2006
**Hardware Availability:** Oct-2006
**Software Availability:** Aug-2006




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon 7110N |
| CPU Characteristics: | 667 MHz bus |
| CPU MHz: | 2500 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 4 chips, 2 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2,4 chips |
| Primary Cache: | 12 K micro-ops I + 16 KB D on chip per core |
| Secondary Cache: | 1 MB I+D on chip per core |
| L3 Cache: | 4 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 32 GB (16 x 2048 MB ECC PC2-3200) |
| Disk Subsystem: | 73 GB SAS, 10k RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit) |
| Compiler: | Intel C++ Compiler for IA32 version 9.1 Build no 20060816<br>Microsoft Visual Studio .Net 2003 (for libraries) |
| Auto Parallel: | No |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 32-bit |
| Peak Pointers: | Not Applicable |
| Other Software: | Smart Heap Library, Version 8 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 2245 | 69.6 | 2246 | 69.6 | **2245** | **69.6** | | | | | | | |
| 401.bzip2 | 16 | 3429 | 45.0 | 3426 | 45.1 | **3427** | **45.1** | | | | | | | |
| 403.gcc | 16 | 6648 | 19.4 | **6596** | **19.5** | 6584 | 19.6 | | | | | | | |
| 429.mcf | 16 | **3330** | **43.8** | 3327 | 43.9 | 3336 | 43.7 | | | | | | | |
| 445.gobmk | 16 | **2641** | **63.6** | 2645 | 63.5 | 2639 | 63.6 | | | | | | | |
| 456.hmmer | 16 | **3136** | **47.6** | 3154 | 47.3 | 3132 | 47.7 | | | | | | | |
| 458.sjeng | 16 | 3840 | 50.4 | 3896 | 49.7 | **3854** | **50.2** | | | | | | | |
| 462.libquantum | 16 | 13545 | 24.5 | **13559** | **24.5** | 13560 | 24.4 | | | | | | | |
| 464.h264ref | 16 | 3014 | 117 | **3011** | **118** | 3001 | 118 | | | | | | | |
| 471.omnetpp | 16 | 3687 | 27.1 | 3682 | 27.2 | **3684** | **27.1** | | | | | | | |
| 473.astar | 16 | 2384 | 47.1 | 2383 | 47.1 | **2384** | **47.1** | | | | | | | |
| 483.xalancbmk | 16 | 1796 | 61.5 | **1794** | **61.5** | 1793 | 61.6 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
Bios Settings
  Hardware Prefetch enabled
  Memory Array set to High-Performance Memory Array
```

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

## Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

## Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

## Base Optimization Flags (Continued)

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml


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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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