SPEC® CINT2006 Result

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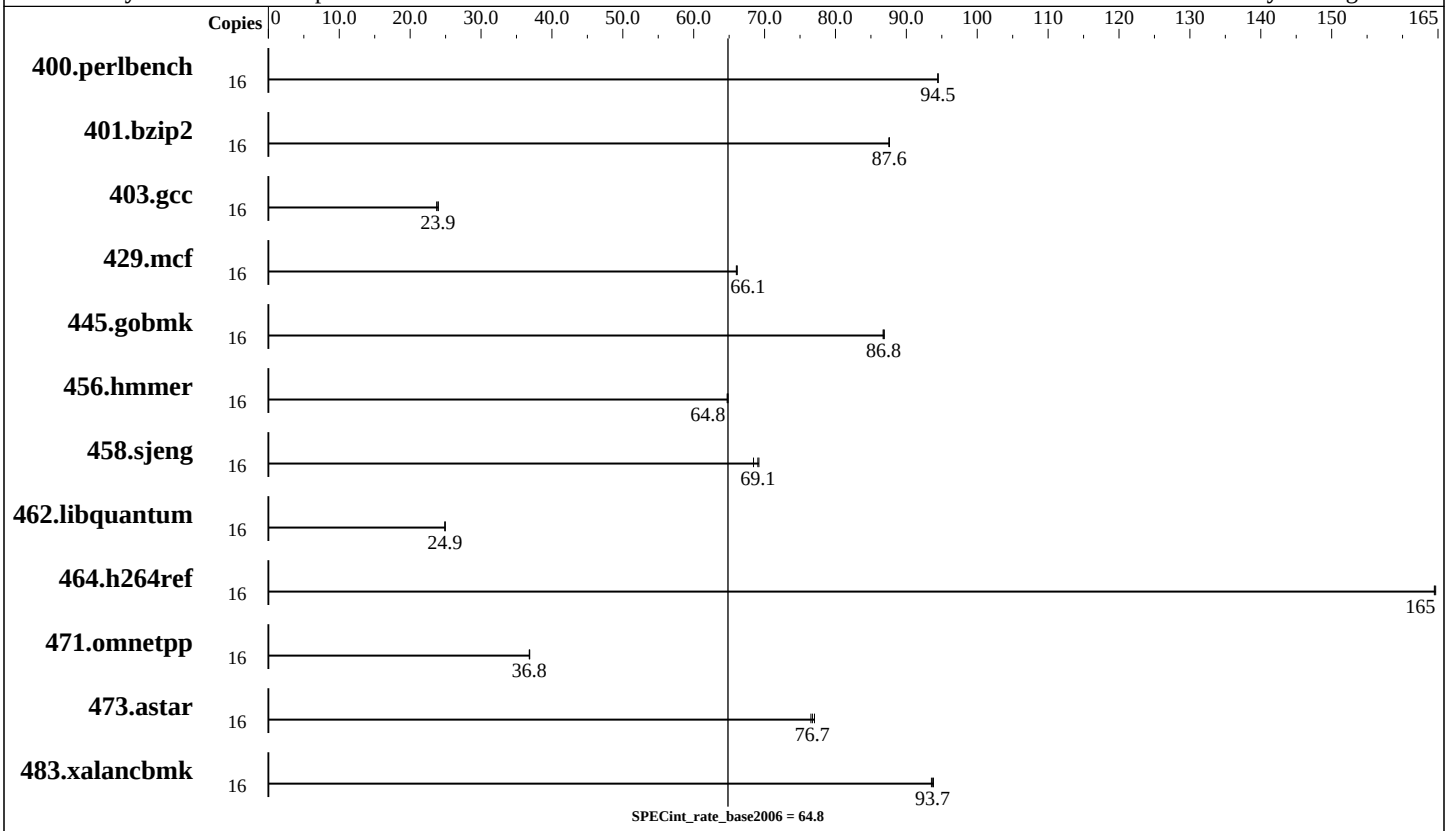
IBM Corporation
IBM System x3800

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 64.8

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Dec-2006
Hardware Availability: Oct-2006
Software Availability: Aug-2006



Hardware

CPU Name:	Intel Xeon 7140N
CPU Characteristics:	667 MHz bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	16 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16 x 2048 MB ECC PC2-3200)
Disk Subsystem:	73 GB SAS, 10k RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1655	94.5	1655	94.4	**1655**	**94.5**							
401.bzip2	16	1764	87.5	1762	87.6	**1763**	**87.6**							
403.gcc	16	**5387**	**23.9**	5372	24.0	5427	23.7							
429.mcf	16	2207	66.1	**2208**	**66.1**	2210	66.0							
445.gobmk	16	1932	86.9	1935	86.7	**1934**	**86.8**							
456.hmmer	16	2303	64.8	2307	64.7	**2303**	**64.8**							
458.sjeng	16	2798	69.2	**2804**	**69.1**	2829	68.4							
462.libquantum	16	13296	24.9	13307	24.9	**13304**	**24.9**							
464.h264ref	16	2150	165	**2152**	**165**	2153	164							
471.omnetpp	16	2717	36.8	2712	36.9	**2715**	**36.8**							
473.astar	16	1458	77.0	**1463**	**76.7**	1468	76.5							
483.xalancbmk	16	1180	93.5	**1178**	**93.7**	1177	93.8							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios Settings
  Hardware Prefetch enabled
  Memory Array set to High-Performance Memory Array
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:

```
-fast /F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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