SPEC® CINT2006 Result

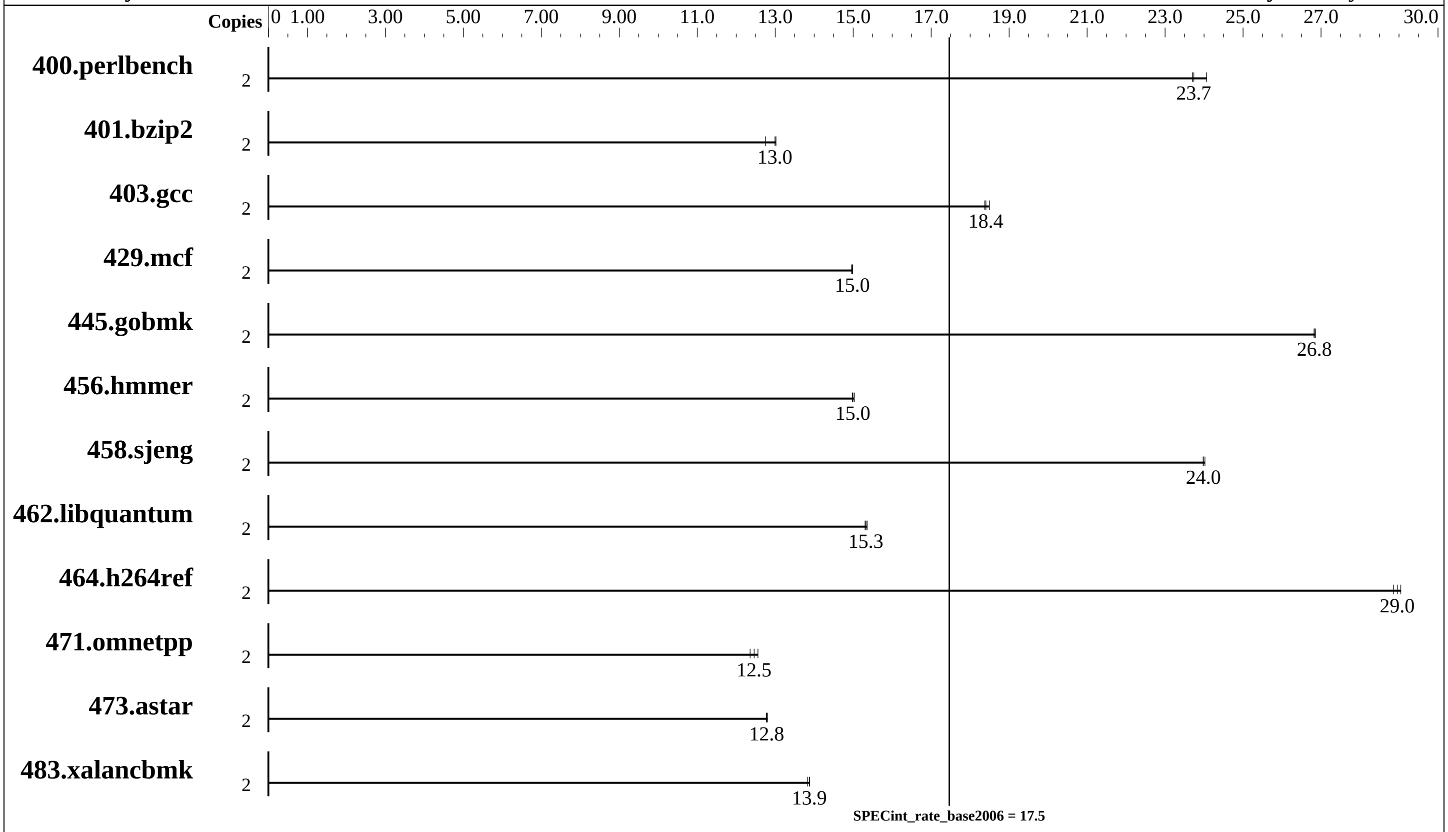
Shuttle
(Test Sponsor: Advanced Micro Devices)

Shuttle SN25P (AMD Athlon 64 FX-60)

SPECint®_rate2006 = --

SPECint_rate_base2006 = 17.5

CPU2006 license:	49	**Test date:**	Apr-2006
Test sponsor:	Advanced Micro Devices	**Hardware Availability:**	Feb-2006
Tested by:	Advanced Micro Devices	**Software Availability:**	May-2006



Hardware

CPU Name:	AMD Athlon 64 FX-60
CPU Characteristics:	
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2 x 1 GB DDR400 CL3)
Disk Subsystem:	SATA 250GB
Other Hardware:	None

Software

Operating System:	SUSE Linux 10.1 (for AMD64)
Compiler:	gcc , g++ 4.1.0 (for AMD64)
Auto Parallel:	No
File System:	ext3
System State:	runlevel 5
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	812	24.1	**823**	**23.7**	824	23.7							
401.bzip2	2	1482	13.0	**1485**	**13.0**	1514	12.7							
403.gcc	2	876	18.4	**875**	**18.4**	871	18.5							
429.mcf	2	1219	15.0	1217	15.0	**1218**	**15.0**							
445.gobmk	2	781	26.9	**782**	**26.8**	782	26.8							
456.hmmer	2	1246	15.0	1242	15.0	**1244**	**15.0**							
458.sjeng	2	1007	24.0	1009	24.0	**1009**	**24.0**							
462.libquantum	2	2698	15.4	2708	15.3	**2703**	**15.3**							
464.h264ref	2	**1529**	**29.0**	1534	28.9	1524	29.0							
471.omnetpp	2	995	12.6	**1003**	**12.5**	1012	12.4							
473.astar	2	1097	12.8	1099	12.8	**1098**	**12.8**							
483.xalancbmk	2	994	13.9	998	13.8	**994**	**13.9**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

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Tested by AMD

 The system may be built with a PCI-E Video card
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Base Compiler Invocation

C benchmarks:
gcc

C++ benchmarks:
g++

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-O3 -funroll-loops -fno-inline-functions`

C++ benchmarks:
`-O3 -funroll-loops`

Base Other Flags

C benchmarks:
`-m32`

C++ benchmarks:
`-m32`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v91.
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