SPEC® CINT2006 Result

Shuttle

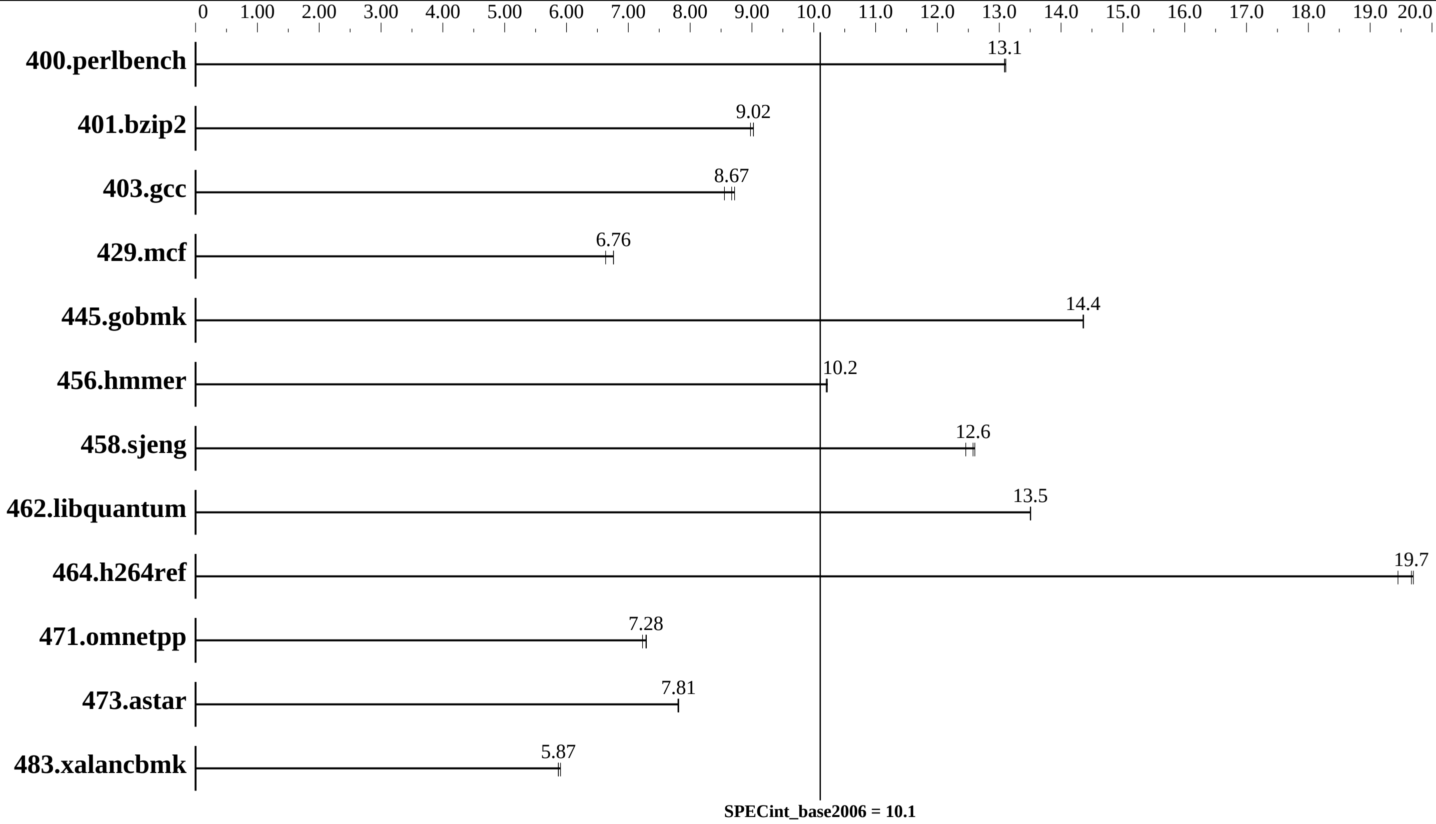
(Test Sponsor: Advanced Micro Devices)

Shuttle SN25P (AMD Athlon 64 FX-60)

SPECint®2006 = --

SPECint_base2006 = 10.1

CPU2006 license:	49	**Test date:**	Apr-2006
Test sponsor:	Advanced Micro Devices	**Hardware Availability:**	Feb-2006
Tested by:	Advanced Micro Devices	**Software Availability:**	May-2006



Hardware

CPU Name:	AMD Athlon 64 FX-60
CPU Characteristics:	
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2 x 1 GB DDR400 CL3)
Disk Subsystem:	SATA 250GB
Other Hardware:	None

Software

Operating System:	SUSE Linux 10.1 (for AMD64)
Compiler:	gcc , g++ 4.1.0 (for AMD64)
Auto Parallel:	No
File System:	ext3
System State:	runlevel 5
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	None

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	745	13.1	747	13.1	**746**	**13.1**						
401.bzip2	1075	8.98	1069	9.02	**1069**	**9.02**						
403.gcc	941	8.56	**928**	**8.67**	923	8.72						
429.mcf	1375	6.63	**1349**	**6.76**	1349	6.76						
445.gobmk	731	14.4	730	14.4	**731**	**14.4**						
456.hmmer	915	10.2	**914**	**10.2**	913	10.2						
458.sjeng	**962**	**12.6**	971	12.5	960	12.6						
462.libquantum	**1534**	**13.5**	1534	13.5	1535	13.5						
464.h264ref	1138	19.4	**1125**	**19.7**	1123	19.7						
471.omnetpp	**858**	**7.28**	864	7.23	857	7.29						
473.astar	898	7.82	900	7.80	**898**	**7.81**						
483.xalancbmk	**1175**	**5.87**	1176	5.87	1169	5.90						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested by AMD
 The system can be assembled using a PCI-E Video card
```

Base Compiler Invocation

C benchmarks:
gcc

C++ benchmarks:
g++

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64

Continued on next page

Base Portability Flags (Continued)

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-O3 -funroll-loops -fno-inline-functions

C++ benchmarks:

-O3 -funroll-loops

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.xml

Tested with SPEC CPU2006 v91.
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