# CINT2000 Result

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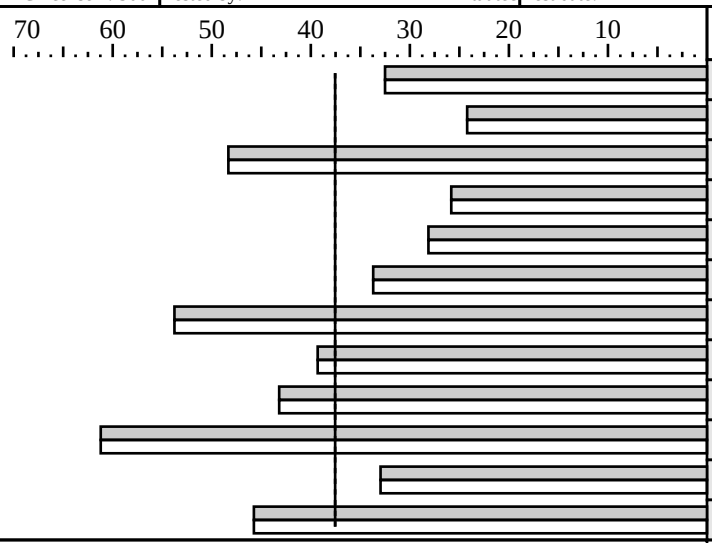
## Itautec

Servidor Itautec LX211 (Intel Xeon 5060 processor)

SPECint_rate2000 = 37.5

SPECint_rate_base2000 = 37.5

SPEC license #: 9001 | Tested by: Itautec | Test date: Nov-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006




| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|---|---|---|---|---|---|---|
| 164.gzip | 4 | 200 | 32.5 | 4 | 200 | 32.5 |
| 175.vpr | 4 | 268 | 24.2 | 4 | 268 | 24.2 |
| 176.gcc | 4 | 106 | 48.3 | 4 | 106 | 48.3 |
| 181.mcf | 4 | 323 | 25.8 | 4 | 323 | 25.8 |
| 186.crafty | 4 | 165 | 28.1 | 4 | 165 | 28.1 |
| 197.parser | 4 | 248 | 33.7 | 4 | 248 | 33.7 |
| 252.eon | 4 | 112 | 53.8 | 4 | 112 | 53.8 |
| 253.perlbmk | 4 | 212 | 39.3 | 4 | 212 | 39.3 |
| 254.gap | 4 | 118 | 43.2 | 4 | 118 | 43.2 |
| 255.vortex | 4 | 144 | 61.2 | 4 | 144 | 61.2 |
| 256.bzip2 | 4 | 211 | 33.0 | 4 | 211 | 33.0 |
| 300.twolf | 4 | 304 | 45.7 | 4 | 304 | 45.7 |

### Hardware

| | |
|---|---|
| CPU: | Intel Xeon 5060 processor (3.20 GHz, 2x2MB L2, 1066 MHz system bus) |
| CPU MHz: | 3200 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology enabled) |
| CPU(s) orderable: | 1 |
| Parallel: | No |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 2MB (I+D) on chip, per core |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 12GB (6x2GB DDR2-RAM PC2-5300 FB-DIMM CL5 ECC) |
| Disk Subsystem: | 1 x 120GB Serial ATA 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows Server 2003 Enterprise Edition + SP1 (32-bit) |
| Compiler: | Intel C++ Compiler 9.1<br>Build 20060519Z (for 32-bit applications),<br>Microsoft Visual Studio 2005(for libraries),<br>MicroQuill SmartHeap for SMP Library 8.0 |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
PORTABILITY FLAGS
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafty:  -DNT_i386
   252.eon:     -DHAS_ERRLIST
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
GENERAL
   +FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning flags
   for C programs:   -fast -QxP +FDO shlSMPMt.lib
   for C++ programs: -fast -Qcxx_features +FDO
EXTRA LIBRARIES
   MicroQuill SmartHeap for SMP Library 8.0 available from
   http://www.microquill.com/
```