# CINT2000 Result

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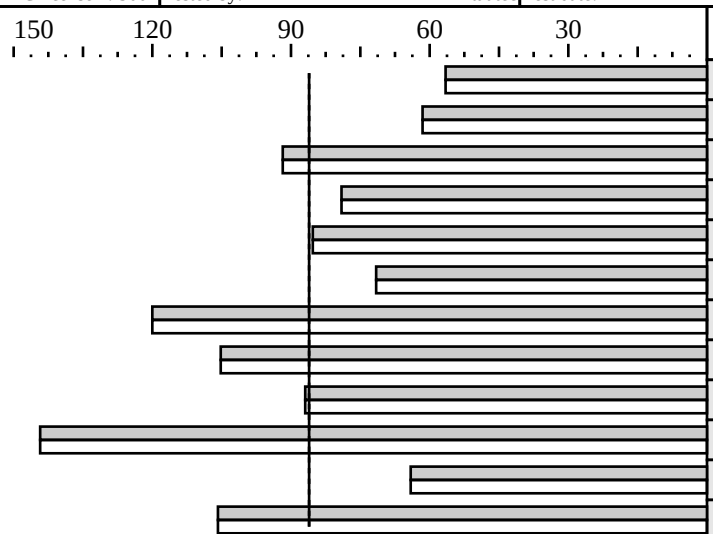
## Itautec

Servidor Itautec MX201 (Intel Xeon 5130 processor)

SPECint_rate2000 = 86.1

SPECint_rate_base2000 = 86.1

SPEC license #: 9001 | Tested by: Itautec | Test date: Oct-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006




| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|---|---|---|---|---|---|---|
| 164.gzip | 4 | 115 | 56.6 | 4 | 115 | 56.6 |
| 175.vpr | 4 | 106 | 61.5 | 4 | 106 | 61.5 |
| 176.gcc | 4 | 55.6 | 91.8 | 4 | 55.6 | 91.8 |
| 181.mcf | 4 | 106 | 79.1 | 4 | 106 | 79.1 |
| 186.crafty | 4 | 54.4 | 85.3 | 4 | 54.4 | 85.3 |
| 197.parser | 4 | 117 | 71.6 | 4 | 117 | 71.6 |
| 252.eon | 4 | 50.3 | 120 | 4 | 50.3 | 120 |
| 253.perlbmk | 4 | 79.4 | 105 | 4 | 79.4 | 105 |
| 254.gap | 4 | 58.7 | 86.9 | 4 | 58.7 | 86.9 |
| 255.vortex | 4 | 61.1 | 144 | 4 | 61.1 | 144 |
| 256.bzip2 | 4 | 109 | 64.1 | 4 | 109 | 64.1 |
| 300.twolf | 4 | 132 | 106 | 4 | 132 | 106 |

### Hardware

| | |
|---|---|
| CPU: | Intel Xeon 5130 processor (2.00GHz, 4MB L2 shared, 1333MHz system bus) |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 2 chips, 2 cores/chip |
| CPU(s) orderable: | 1,2 |
| Parallel: | No |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 4096KB(I+D) on chip, per chip, shared |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 8x1GB 533MHz ECC CL4 FB-DIMM DDR2 SDRAM |
| Disk Subsystem: | 1 x 73GB SCSI 10000 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows Server 2003 Enterprise Edition + SP1 (32-bit) |
| Compiler: | Intel C++ Compiler 9.1<br>Build 20060519Z (for 32-bit applications),<br>Microsoft Visual Studio .NET 7.0.9466 (for libraries),<br>MicroQuill SmartHeap for SMP Library 8.0 |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
PORTABILITY FLAGS
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafy:   -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
GENERAL
   +FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning flags
   for C programs:   -fast +FDO shlSMPMt.lib
   for C++ programs: -fast -Qcxx_features +FDO
EXTRA LIBRARIES
   MicroQuill SmartHeap for SMP Library 8.0 available from
   http://www.microquill.com/
```